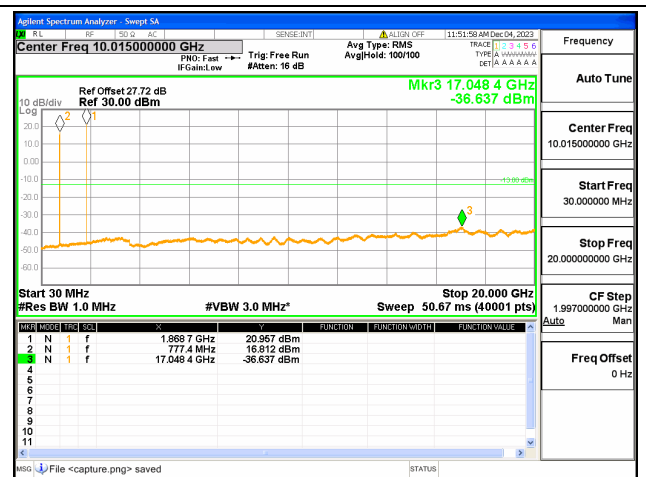
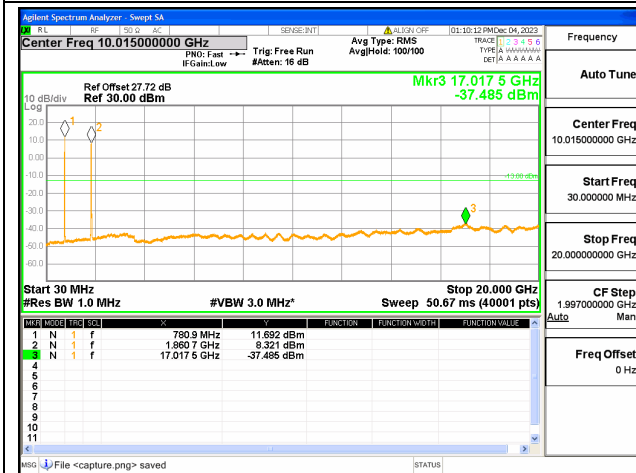


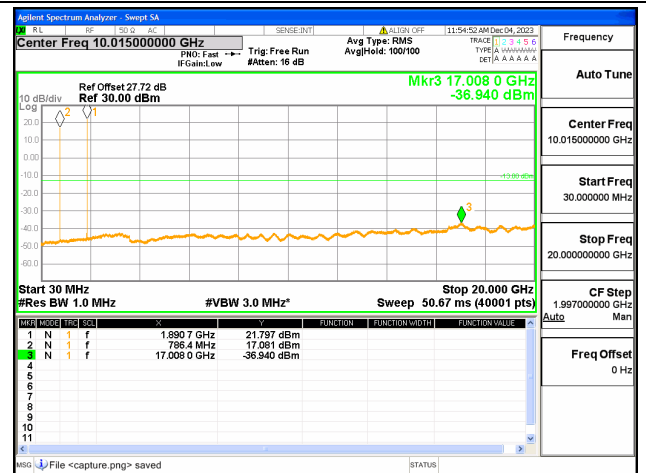
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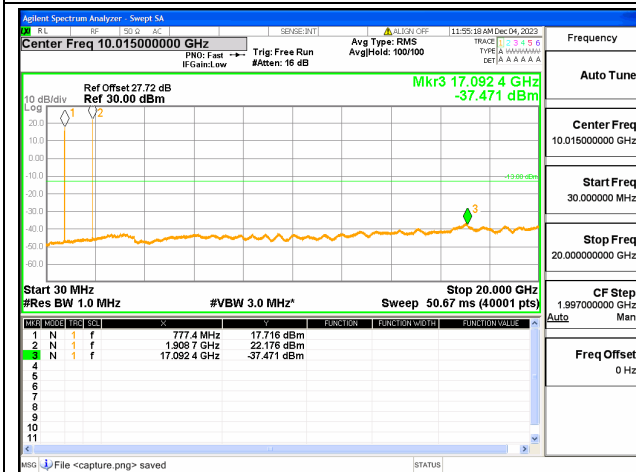
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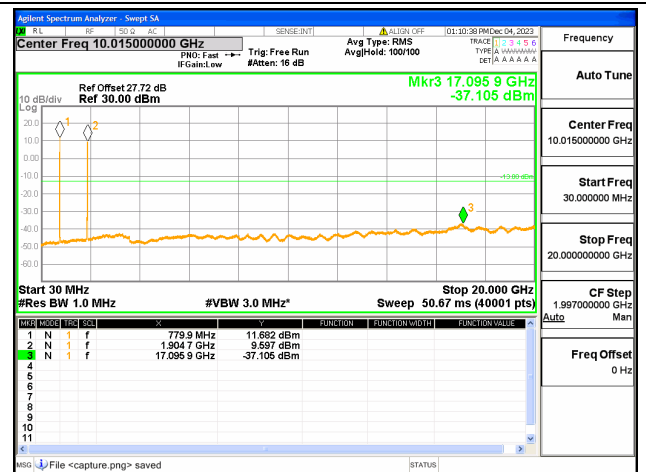
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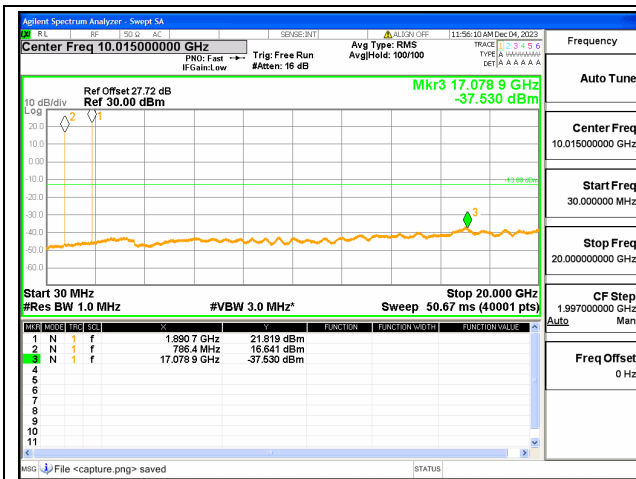
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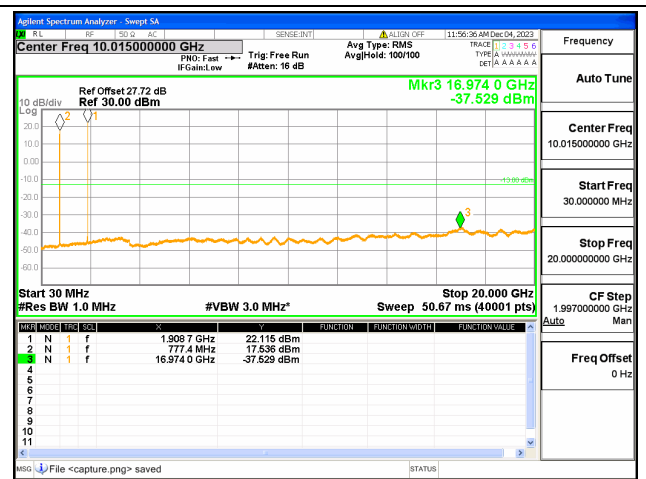
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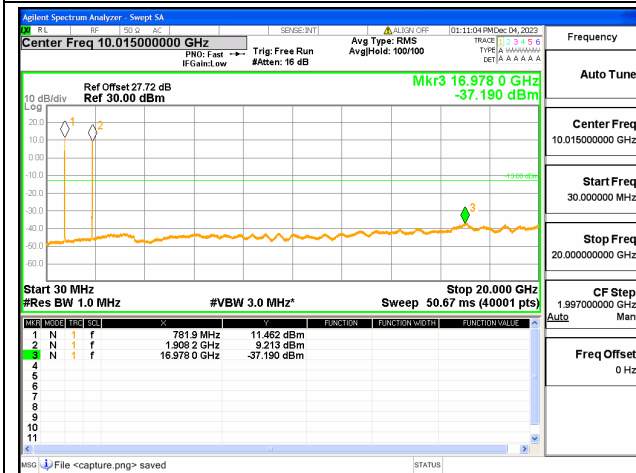
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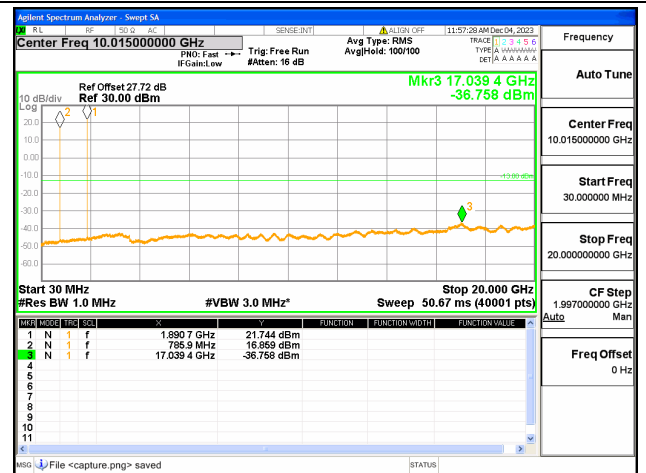
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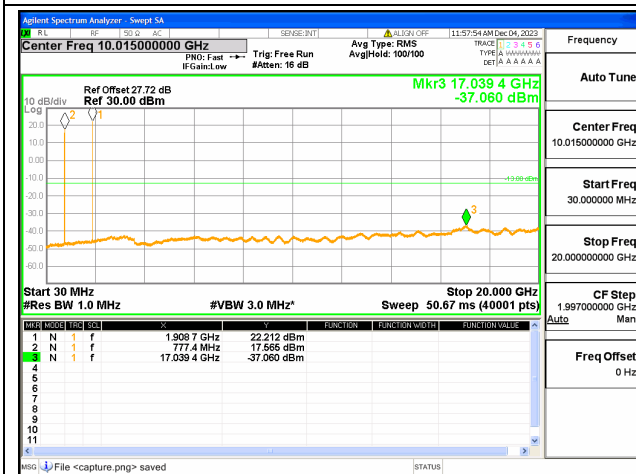
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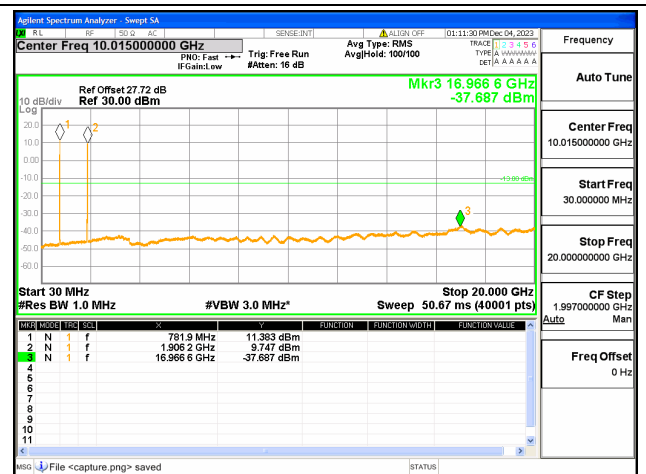
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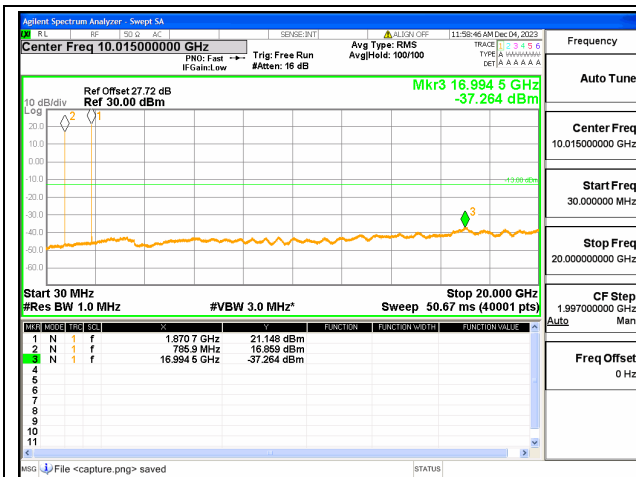
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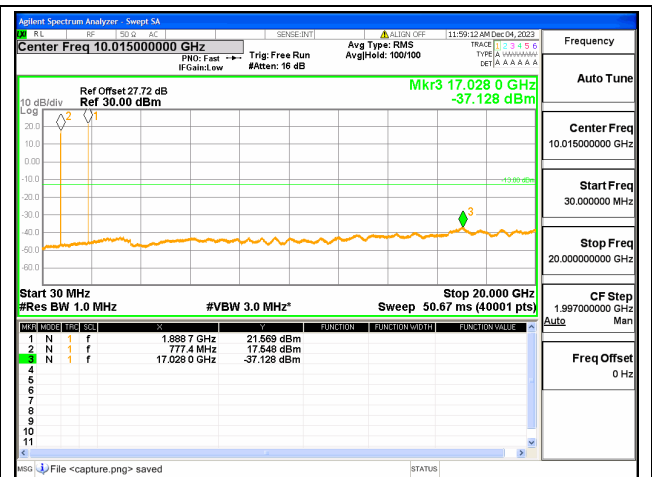
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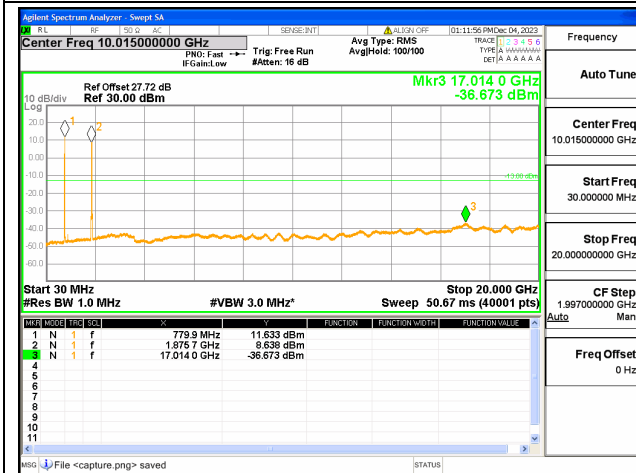
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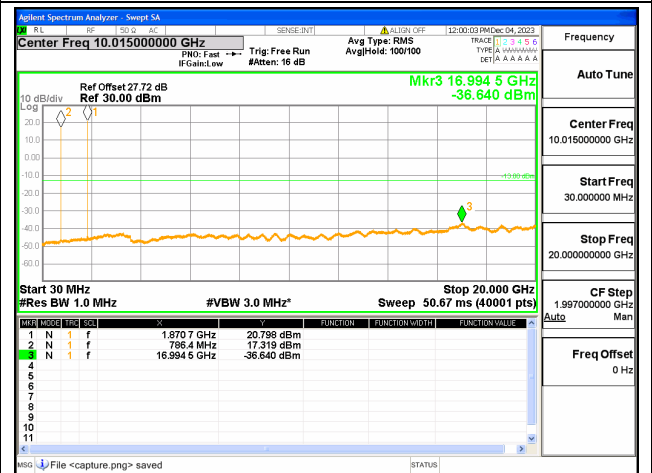
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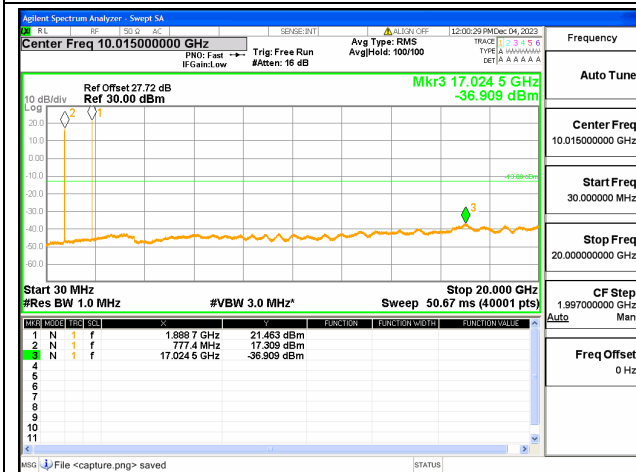
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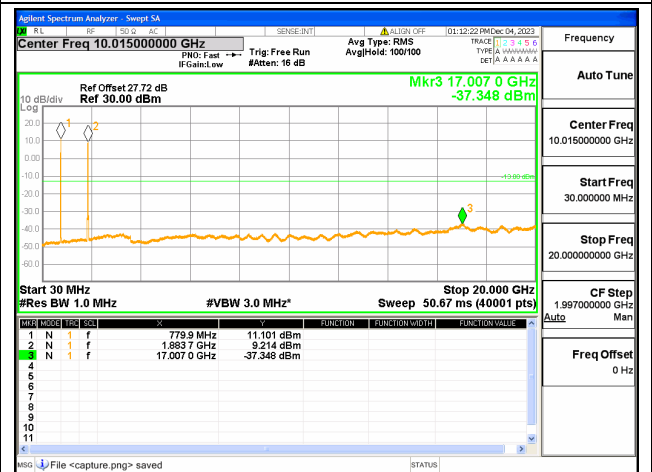
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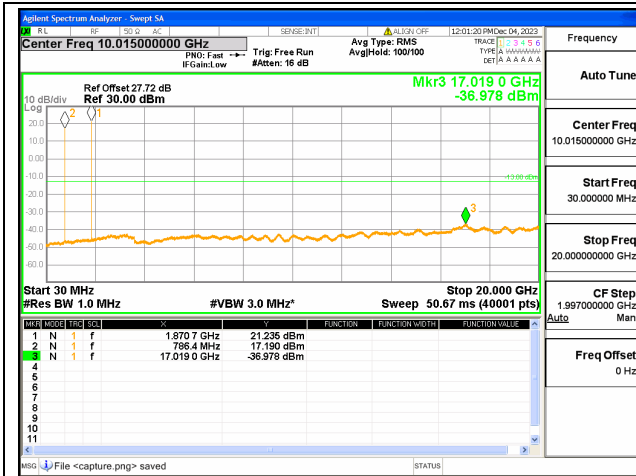
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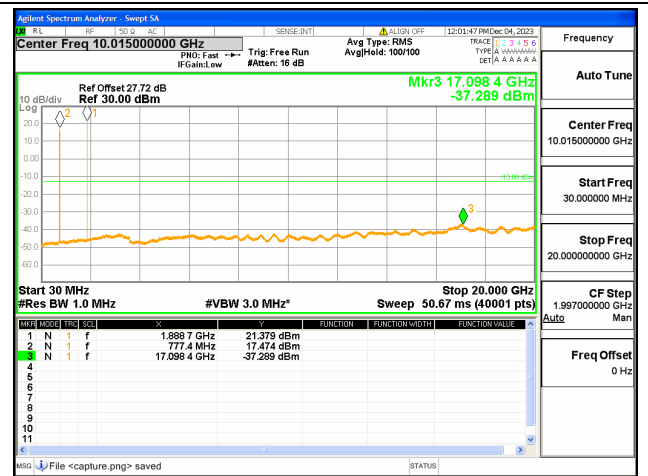
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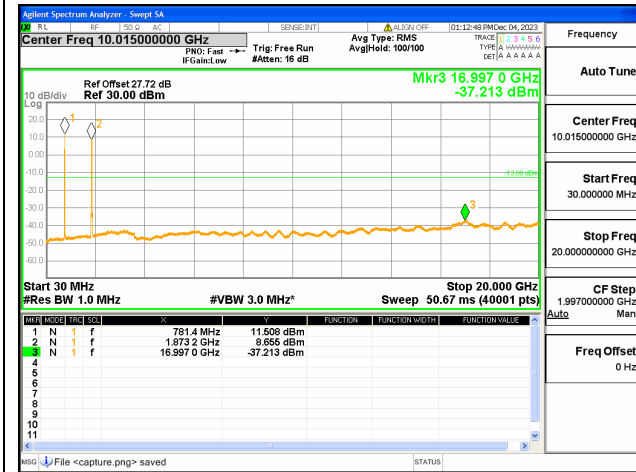
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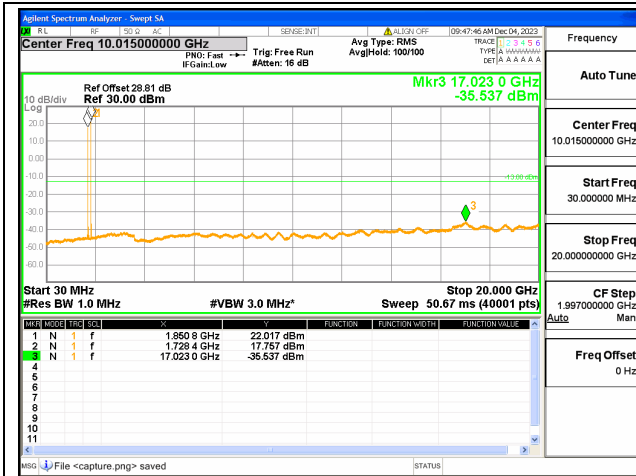


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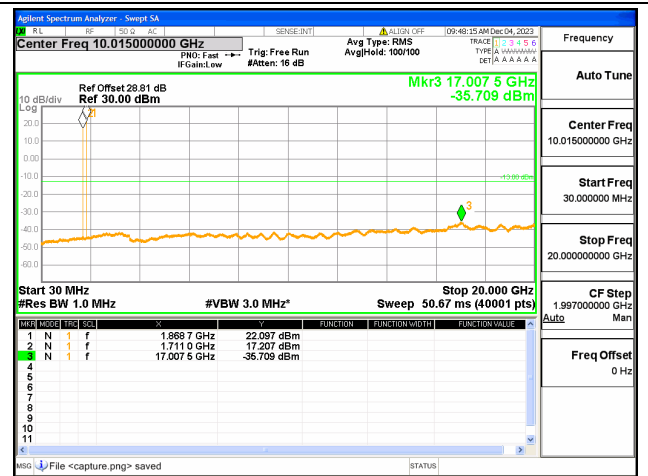


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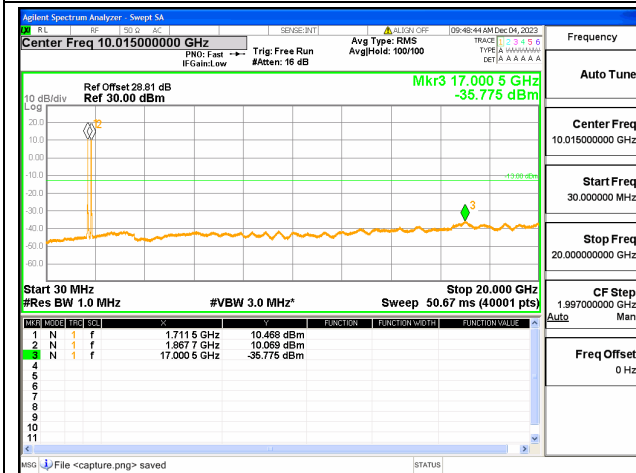
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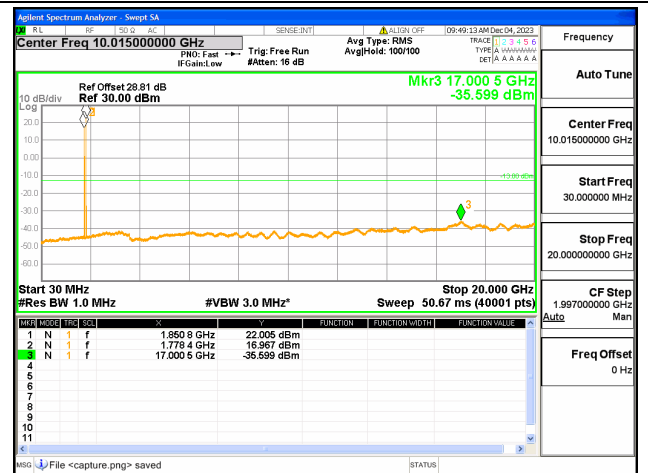
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1@0-1@99



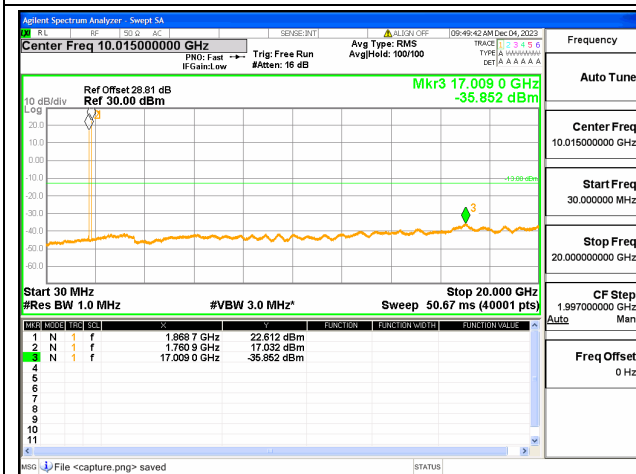
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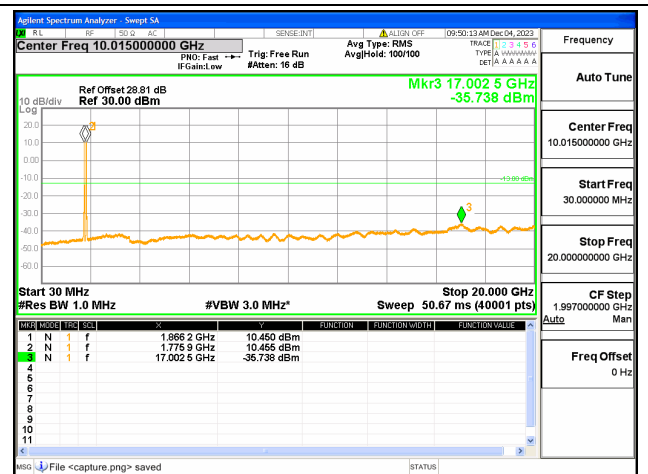
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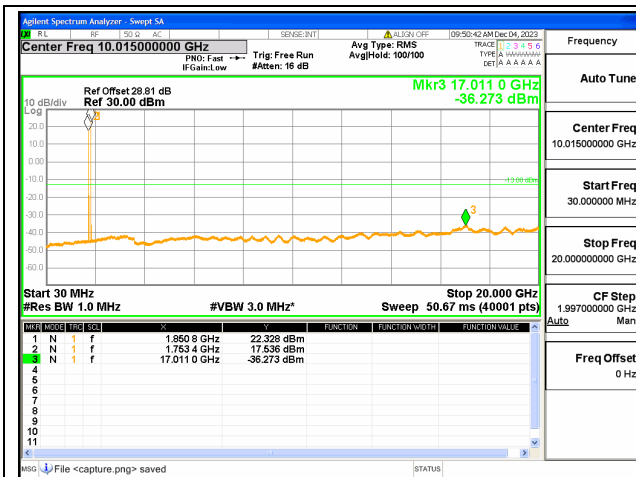
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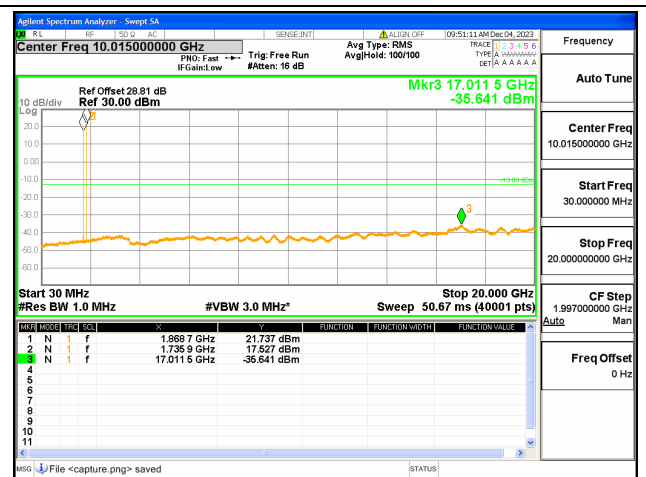
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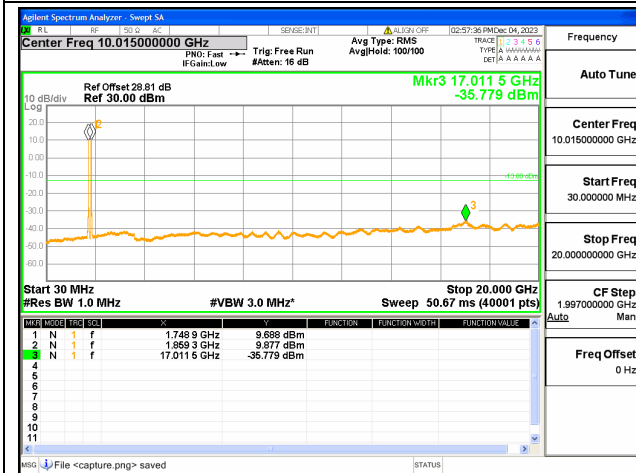
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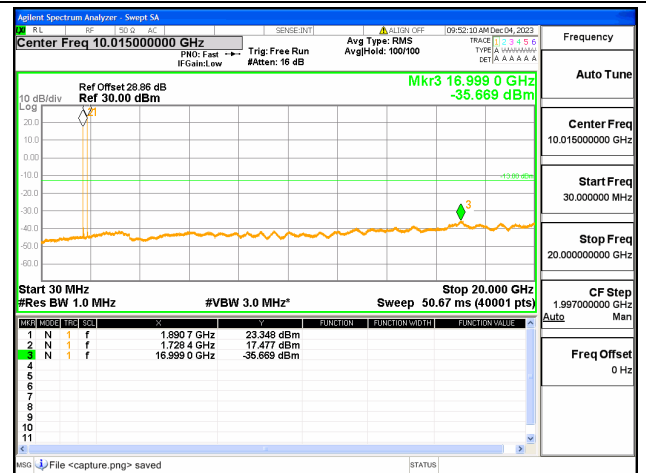
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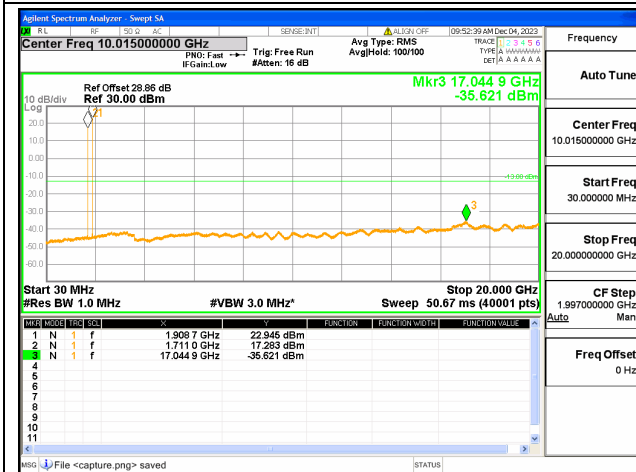
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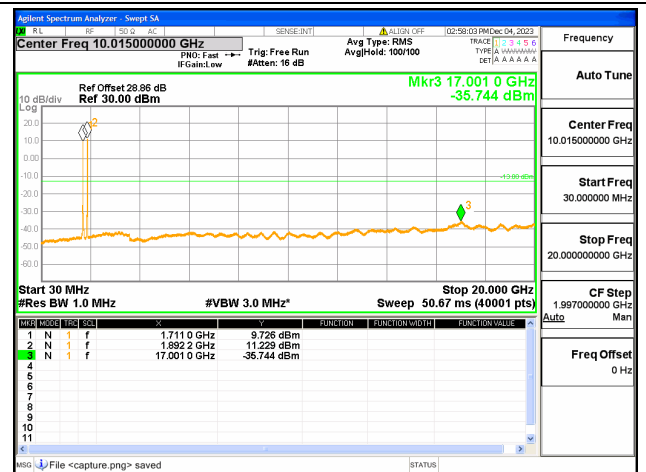
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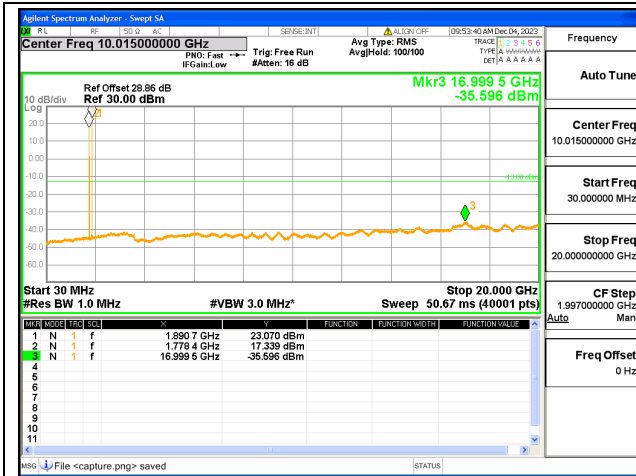
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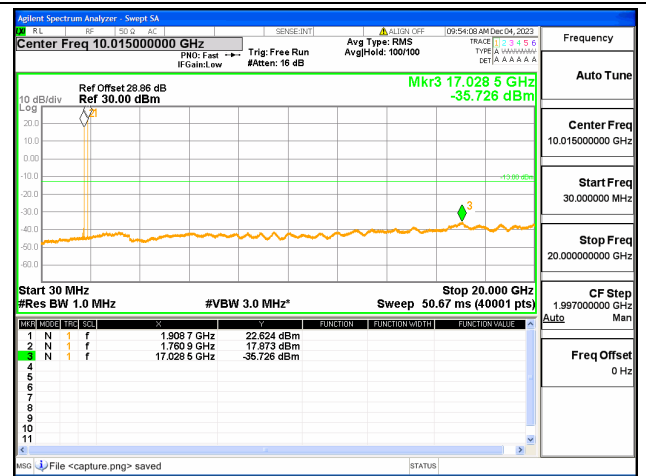
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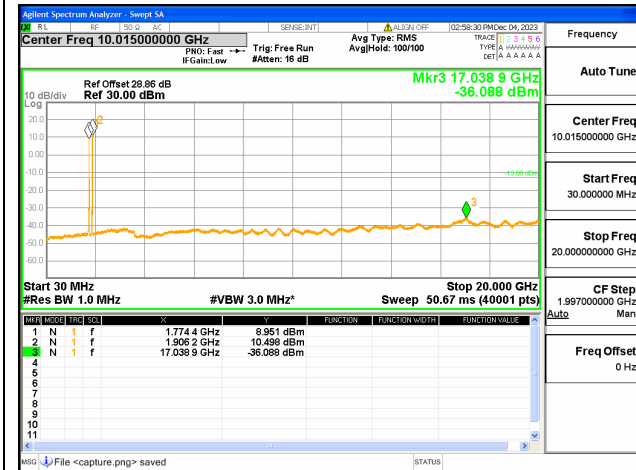
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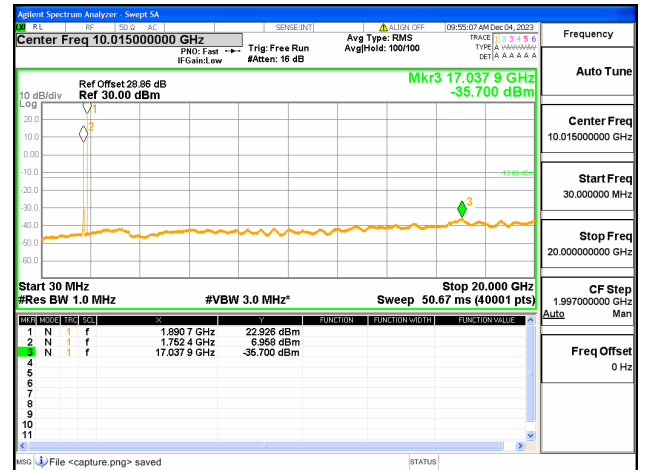
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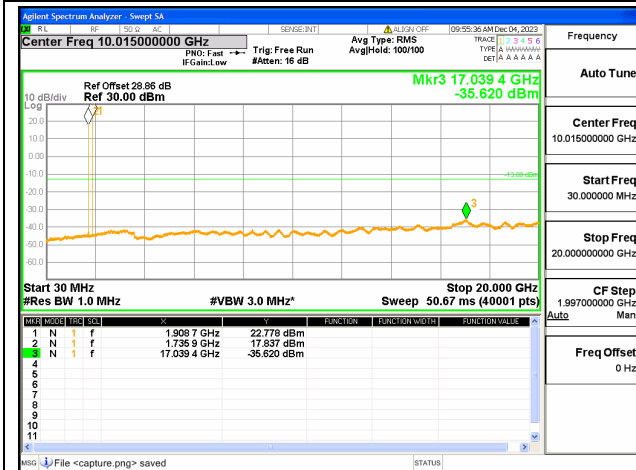
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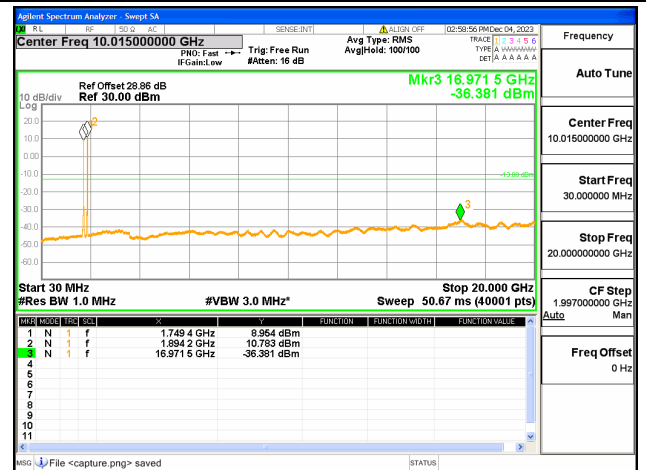
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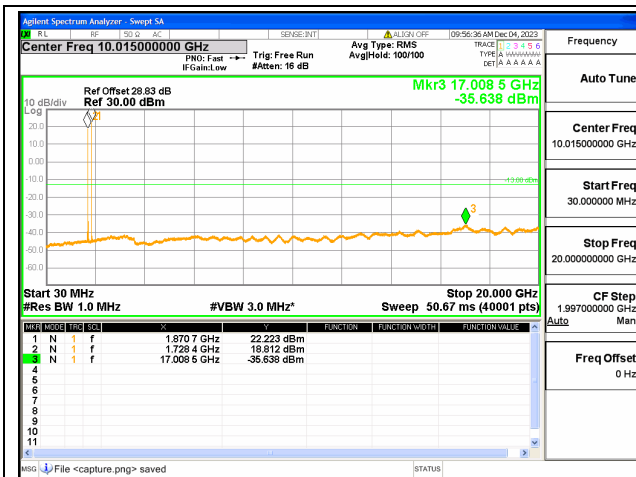
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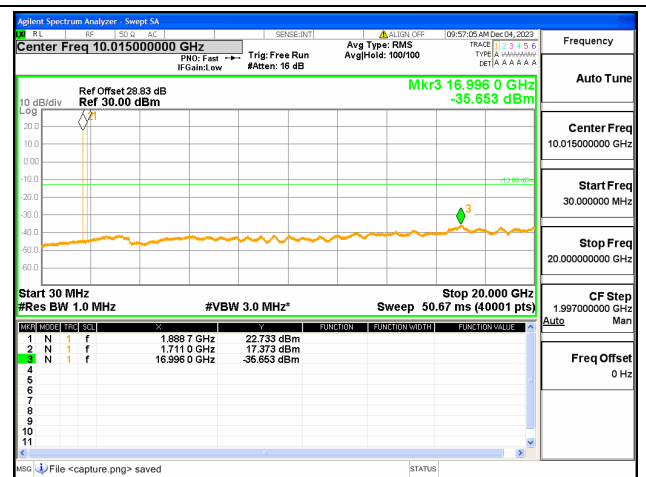
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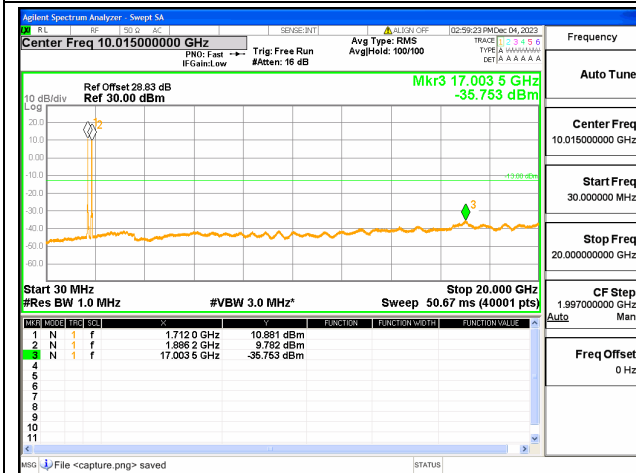
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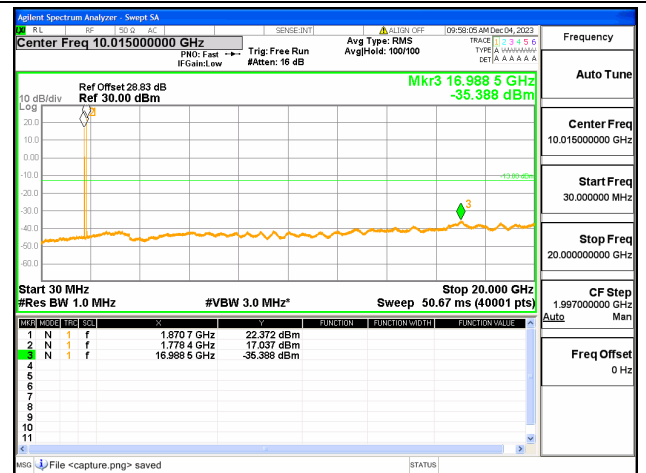
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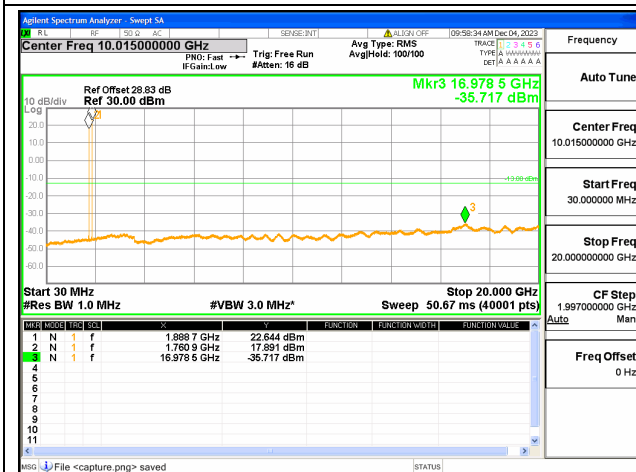
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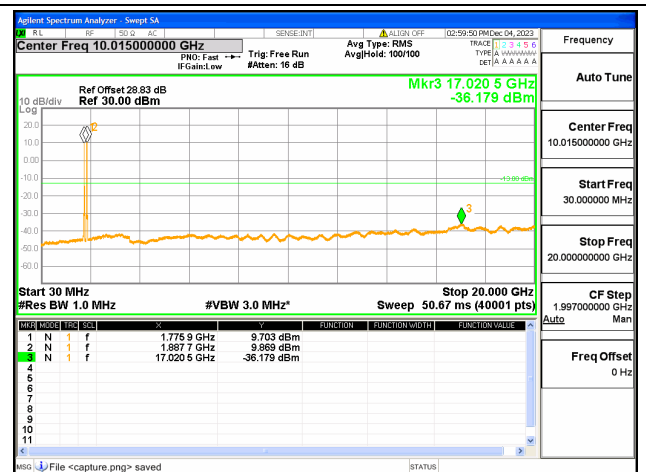
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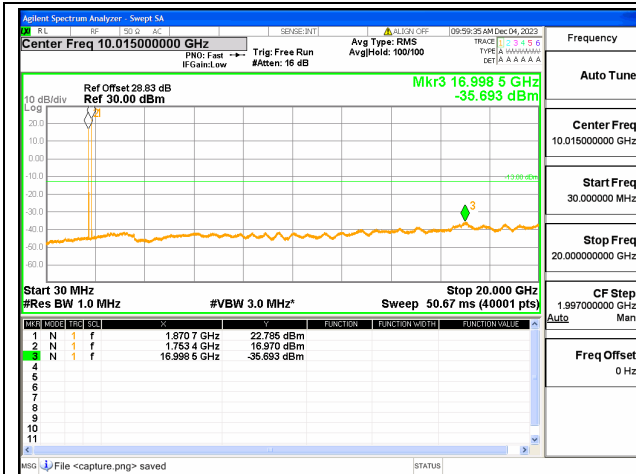
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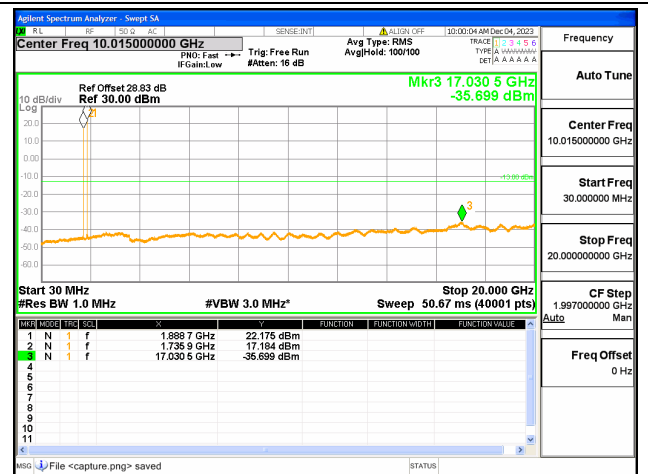
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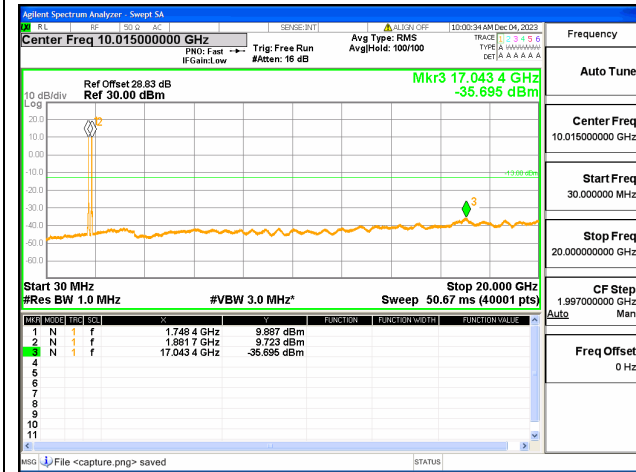
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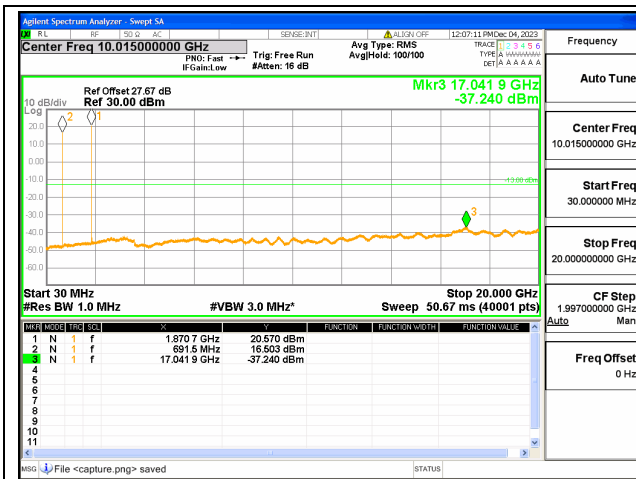


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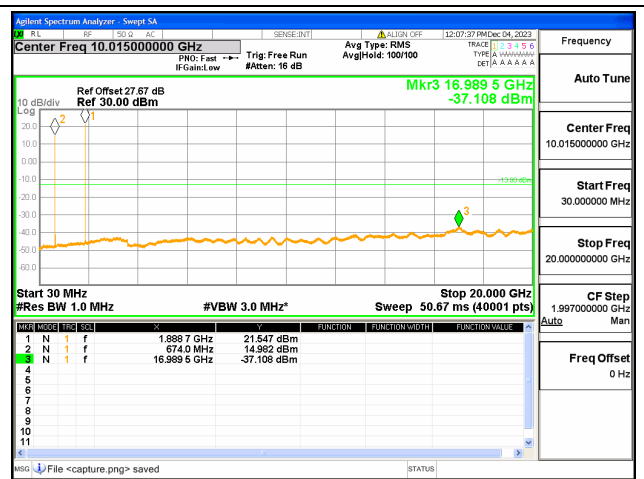


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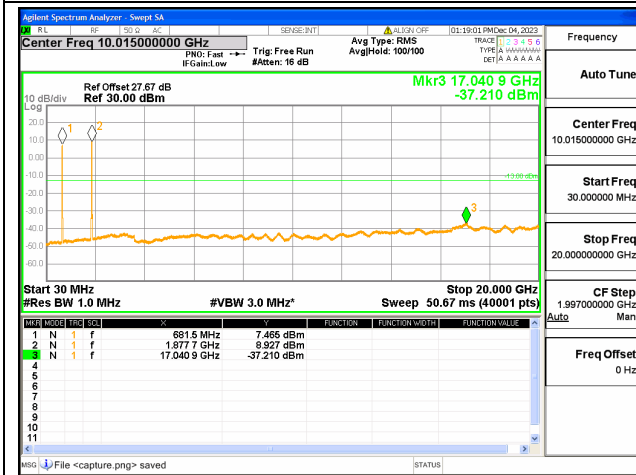
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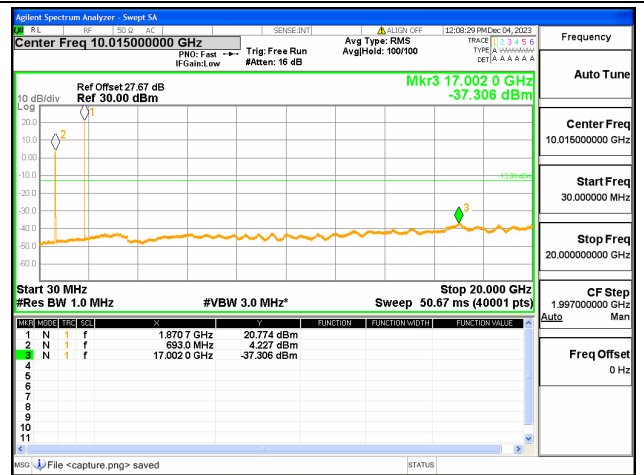
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1@0-1@99



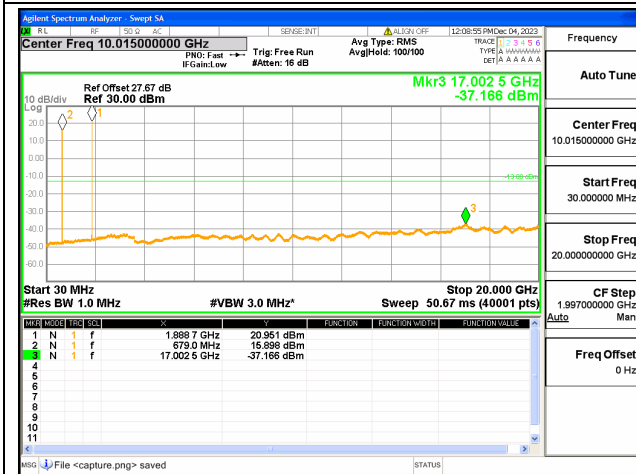
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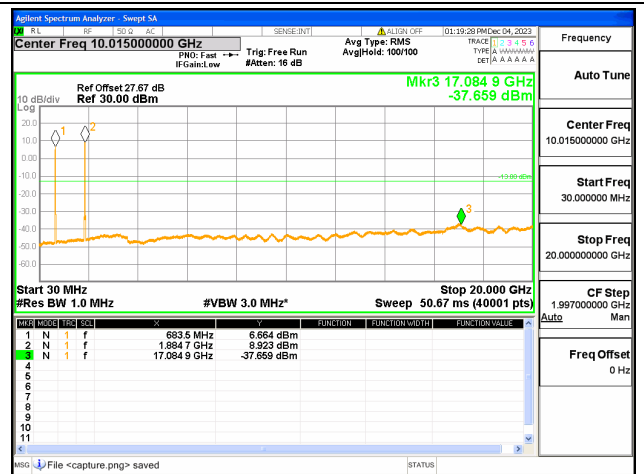
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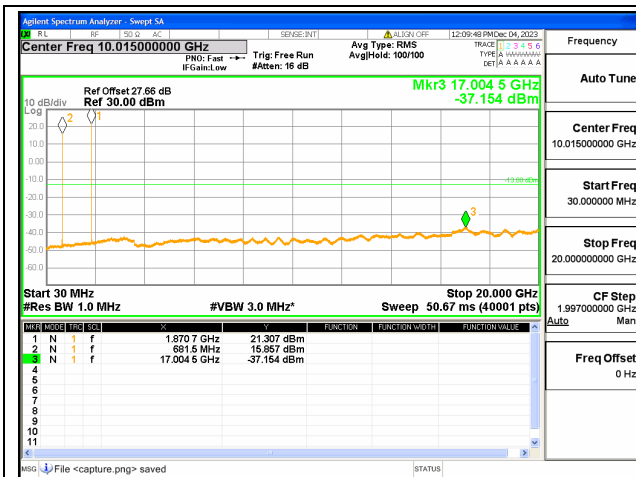
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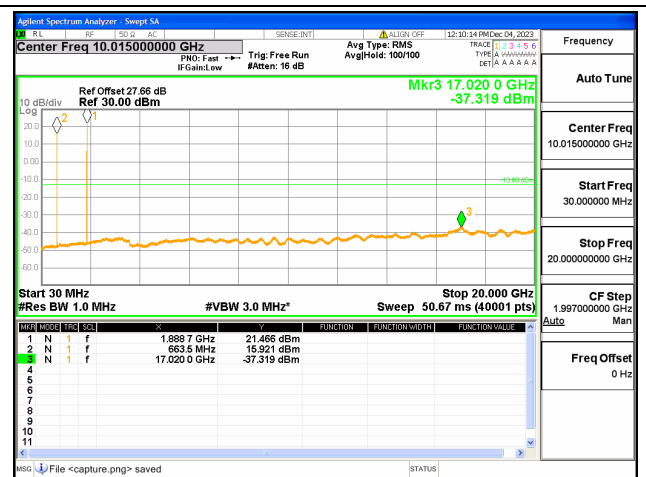
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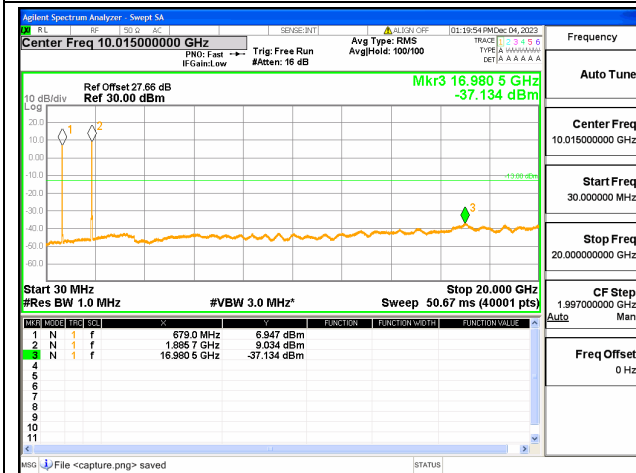
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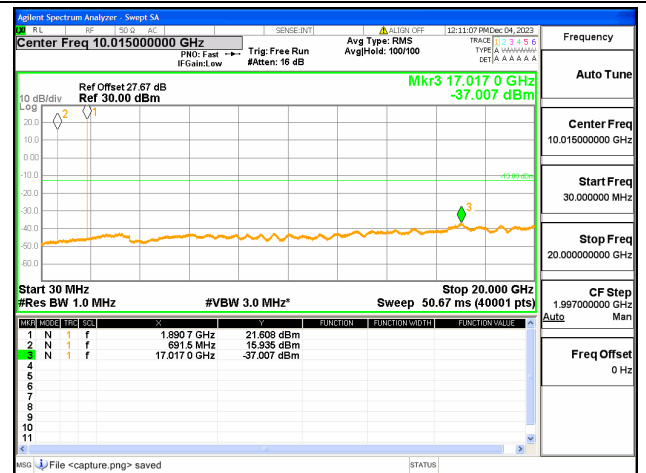
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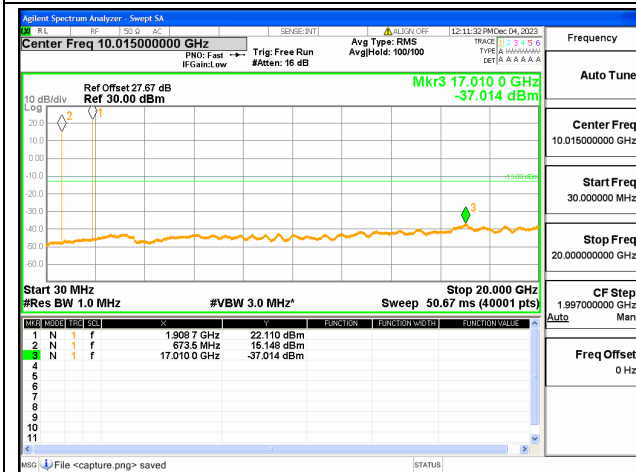
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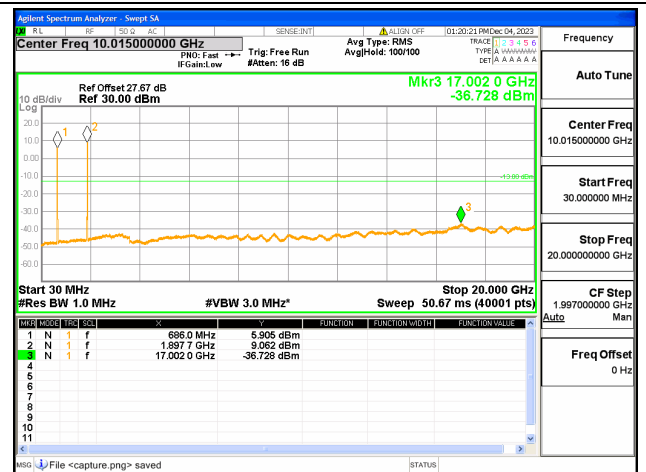
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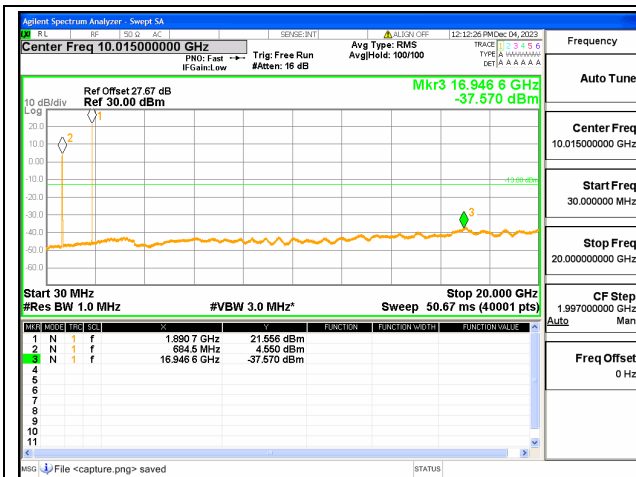
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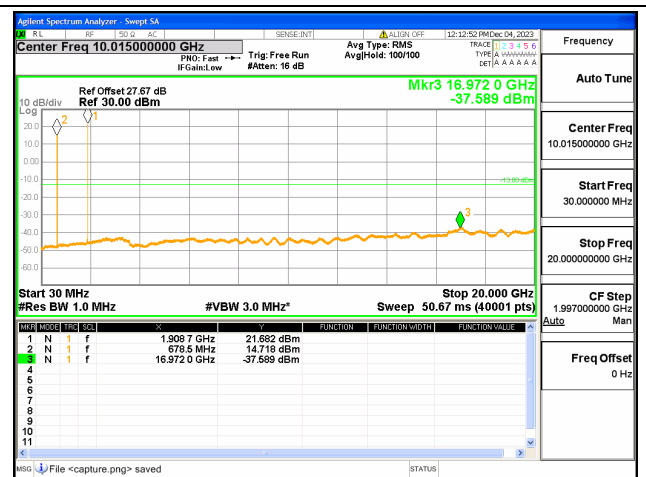
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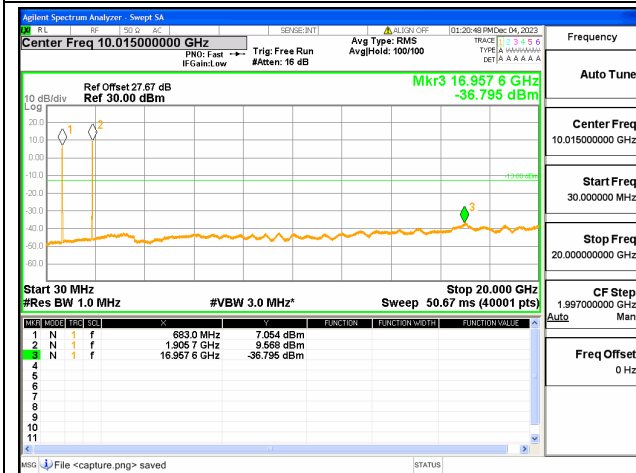
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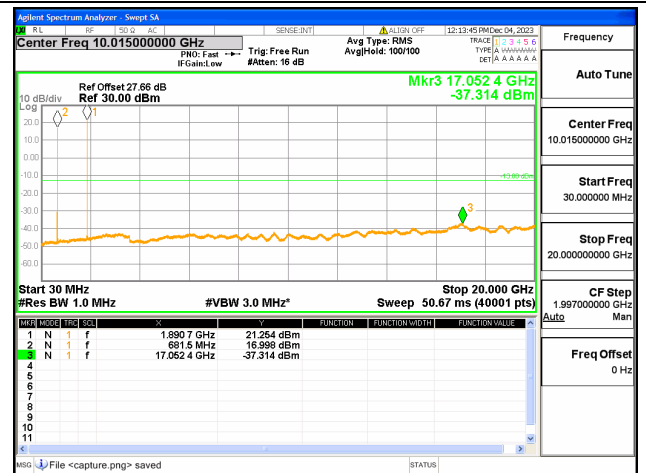
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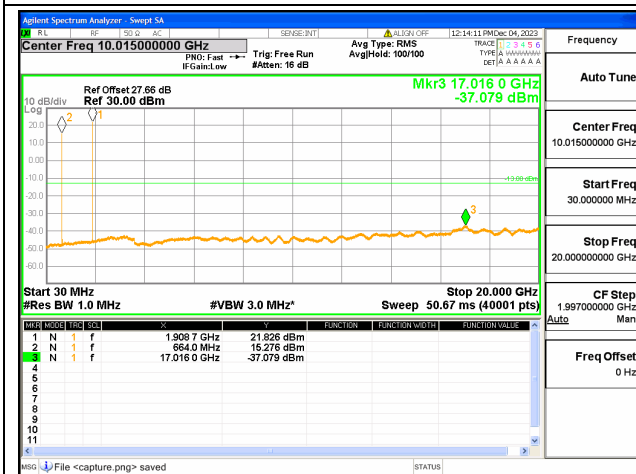
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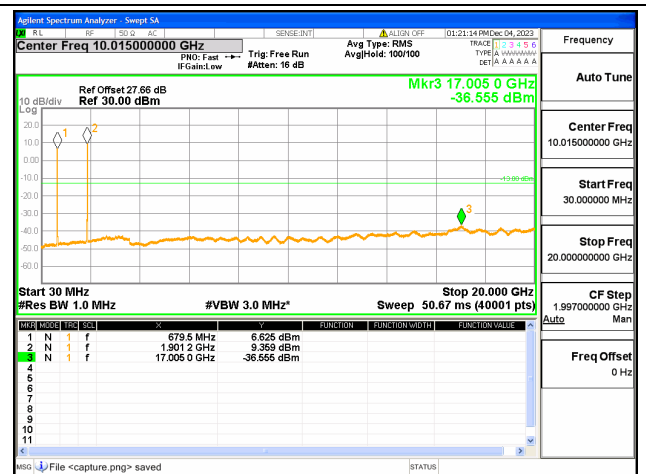
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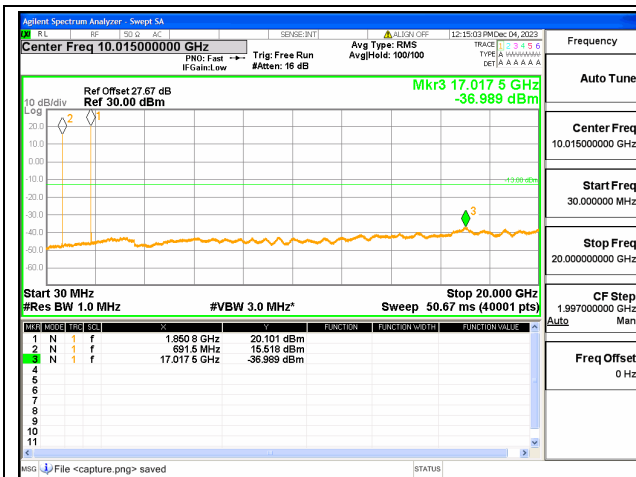
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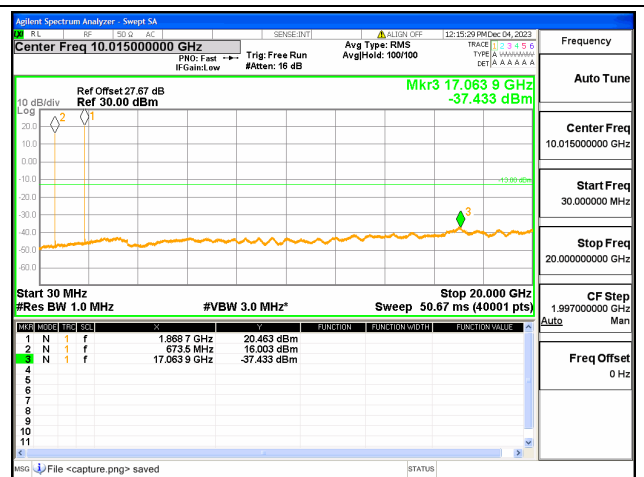
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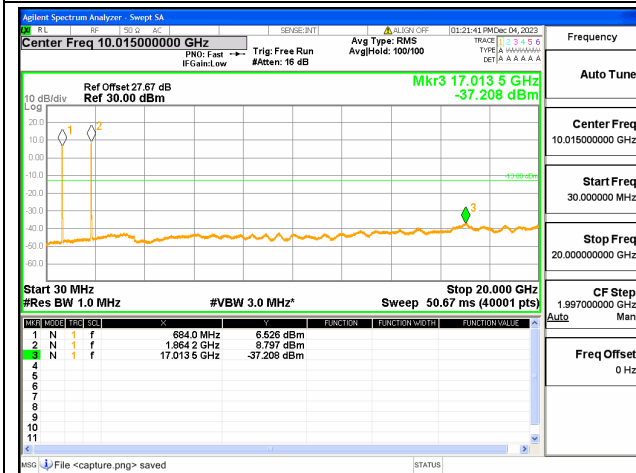
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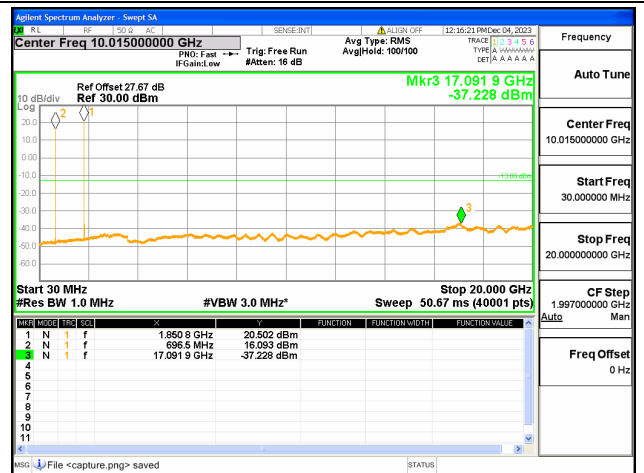
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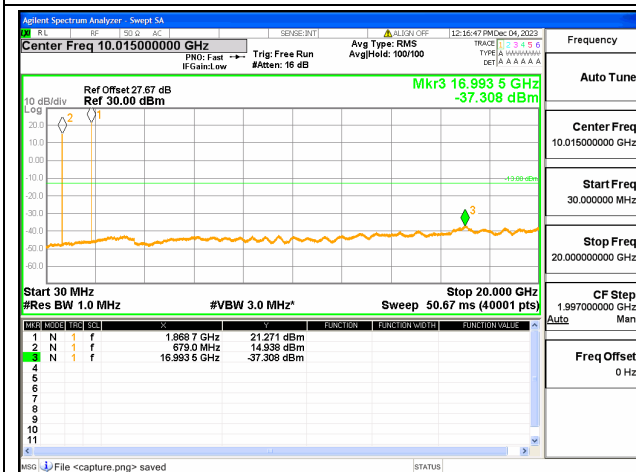
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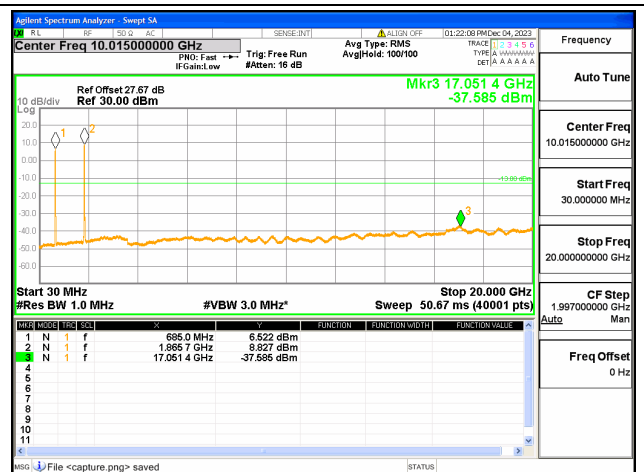
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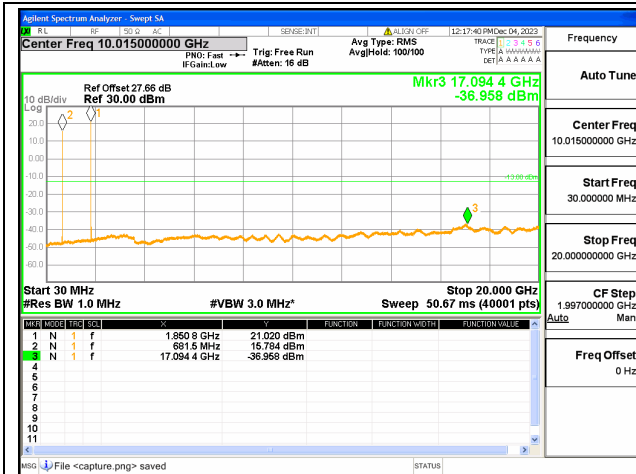
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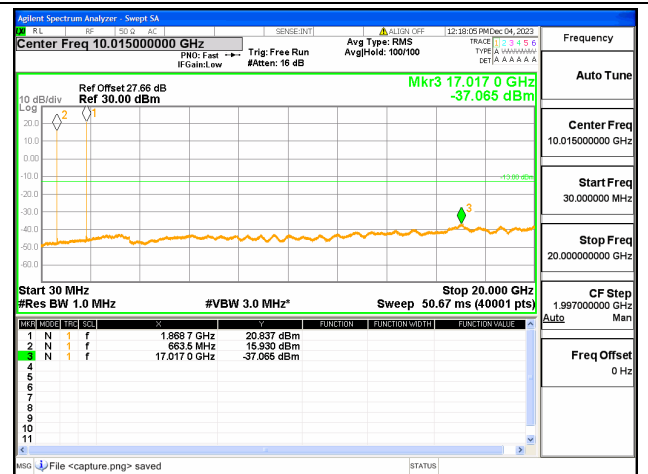
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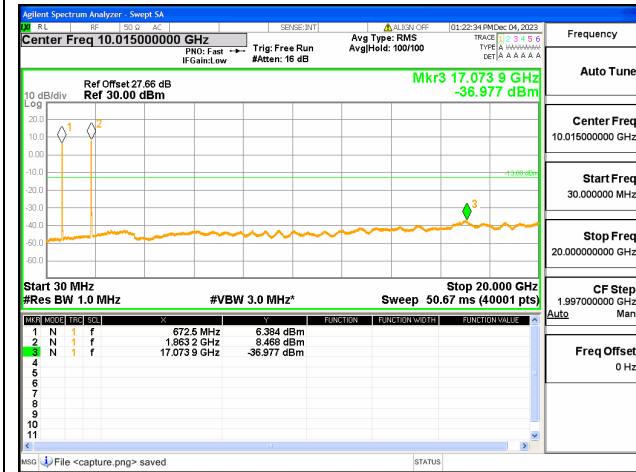
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2A-71A / 20+20MHz / QPSK / Low+Low CH /
1@0-1@99

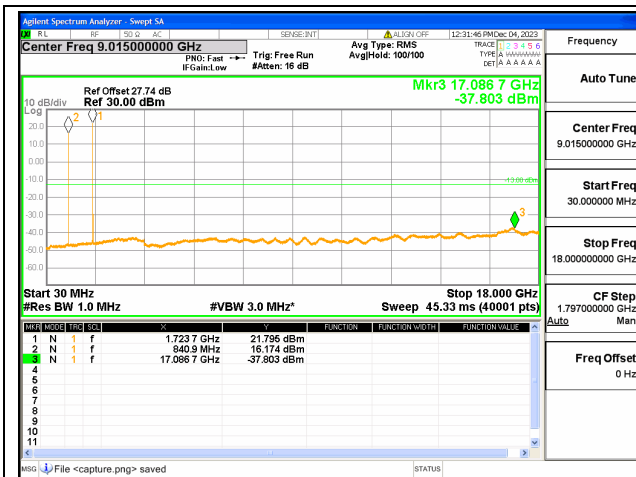


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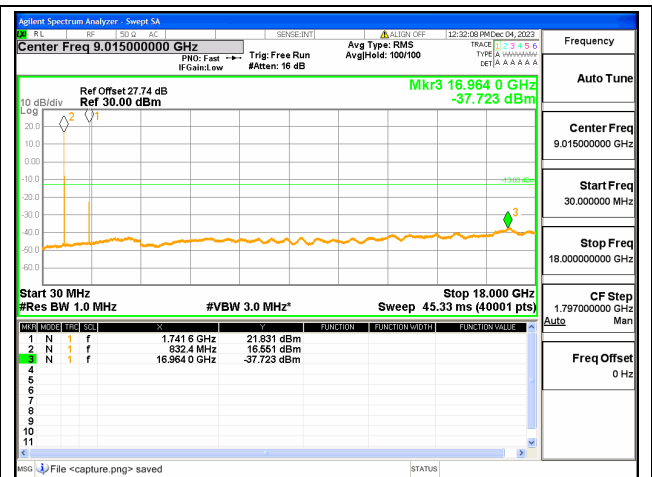


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100@0-100@0

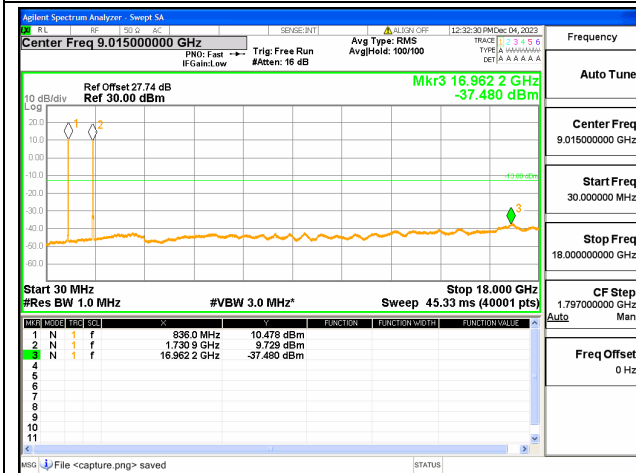
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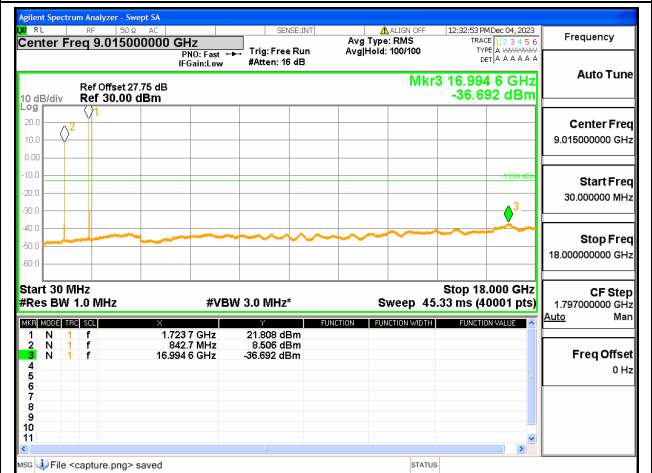
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1@0-1@49



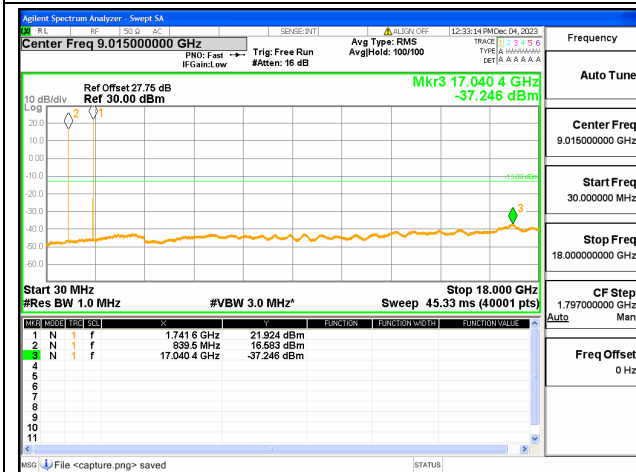
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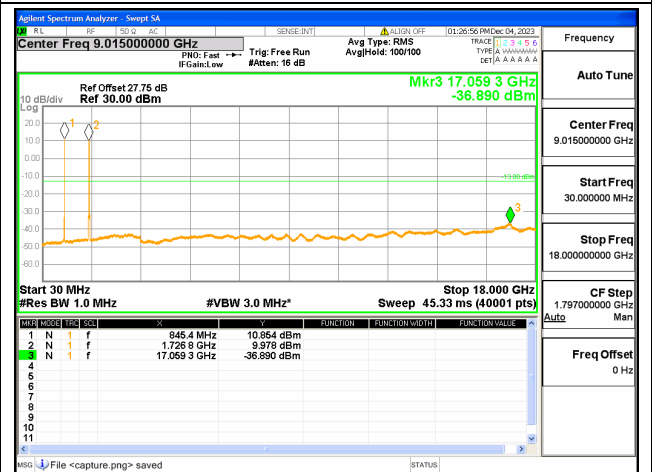
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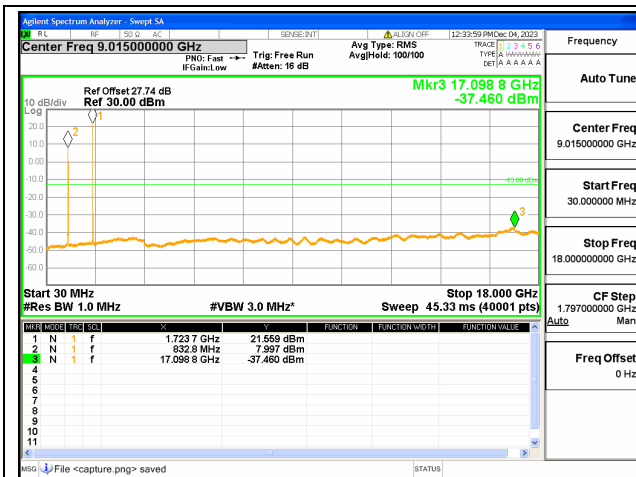
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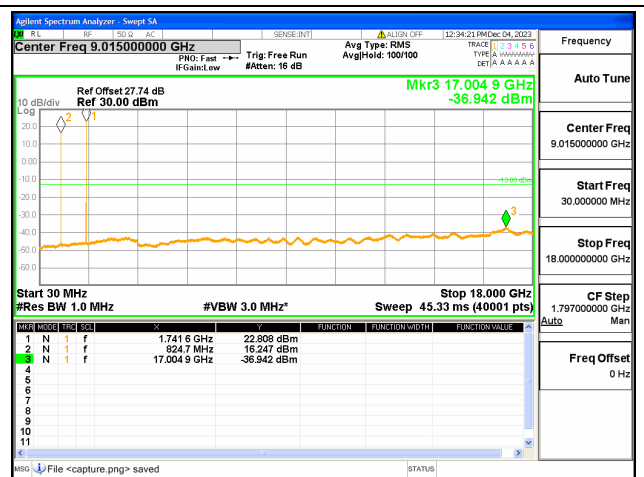
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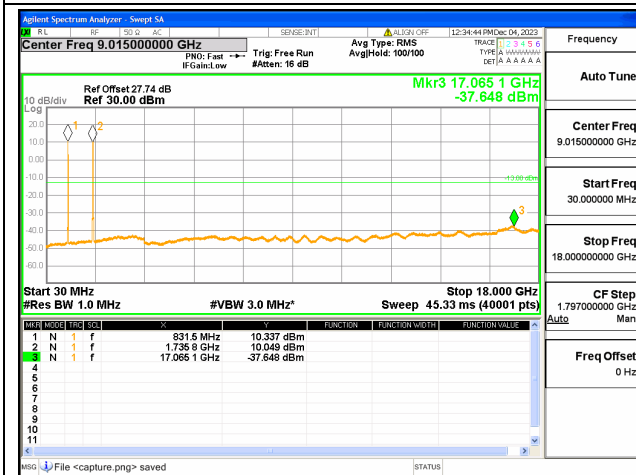
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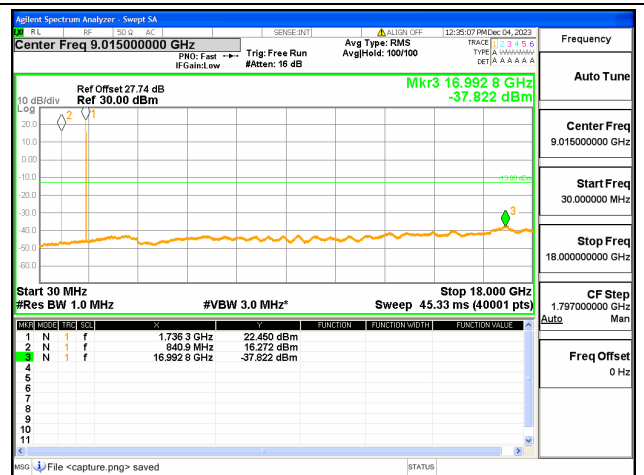
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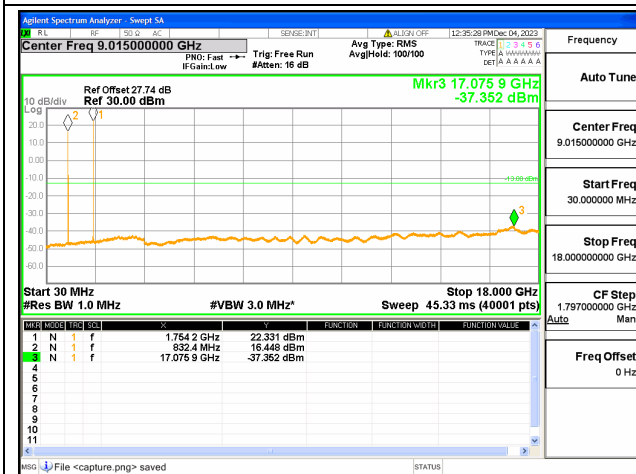
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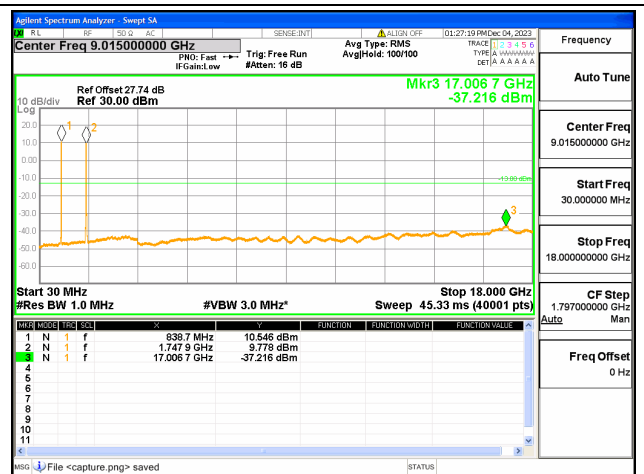
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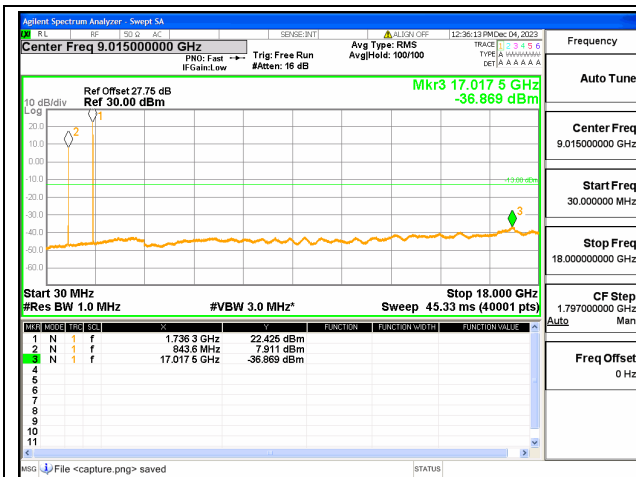
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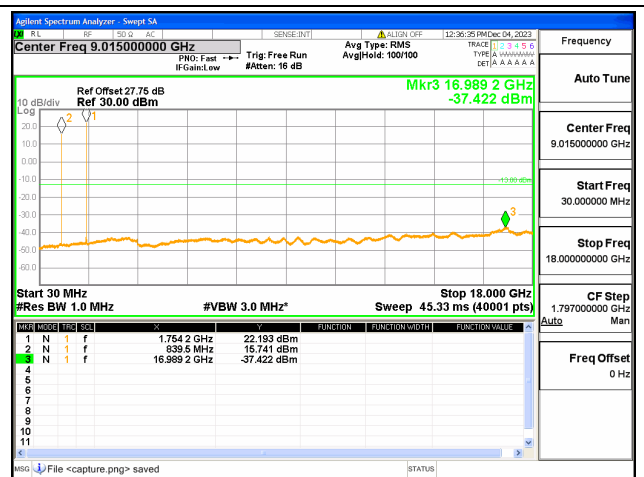
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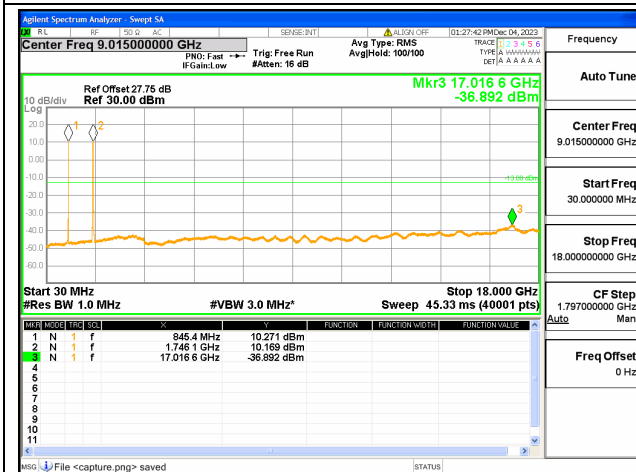
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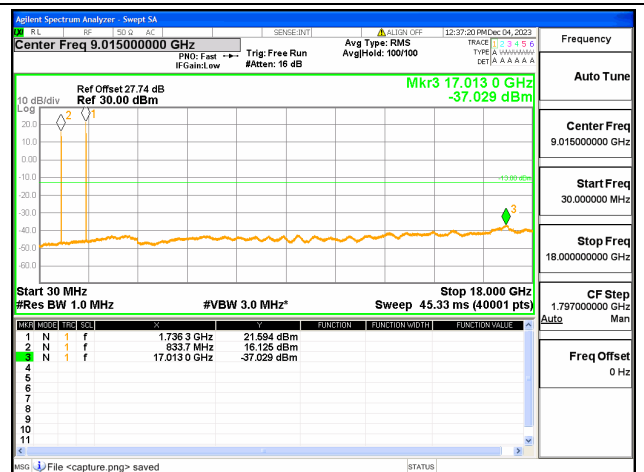
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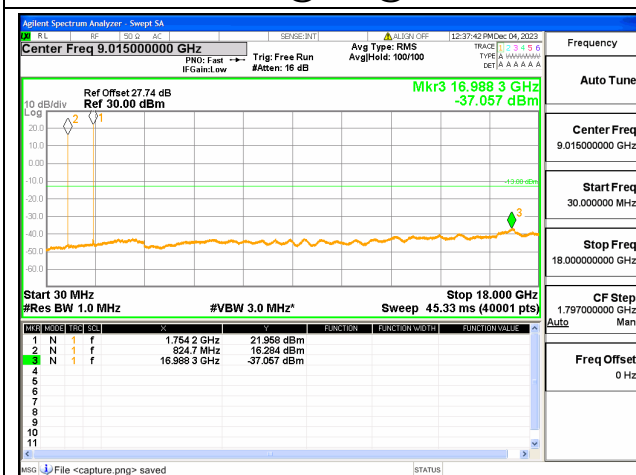
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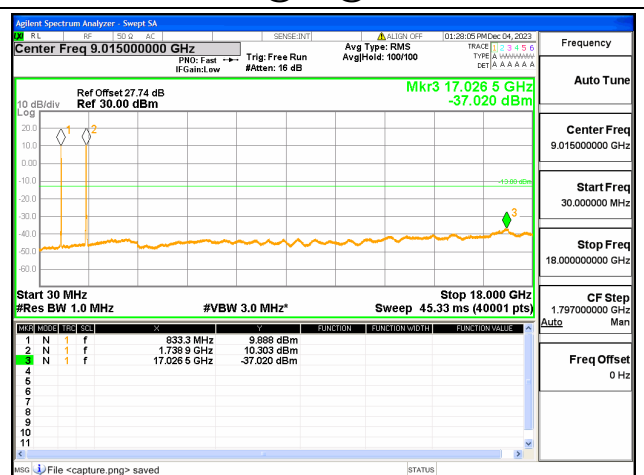
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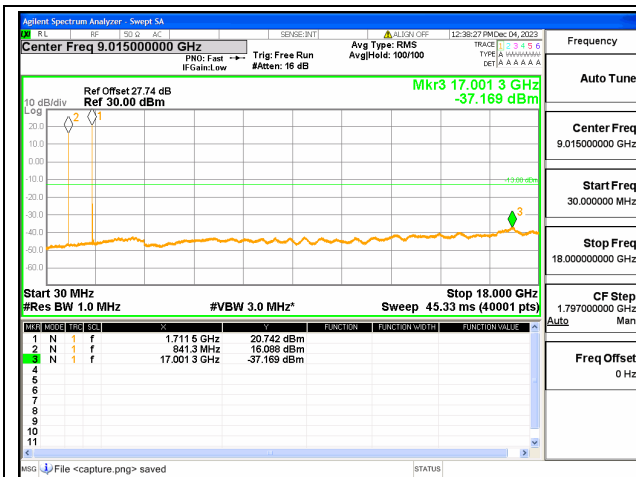
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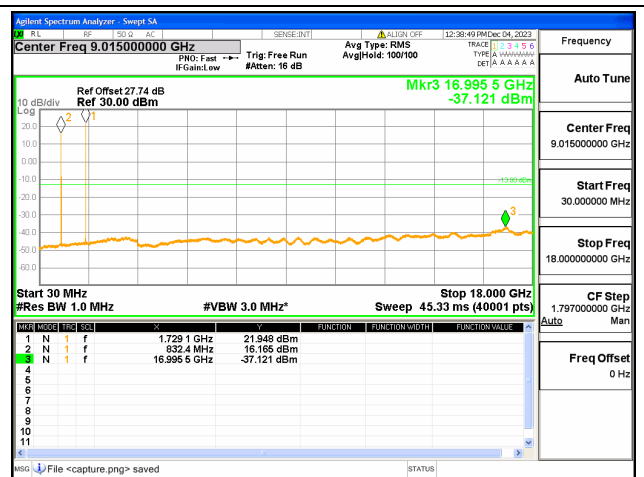
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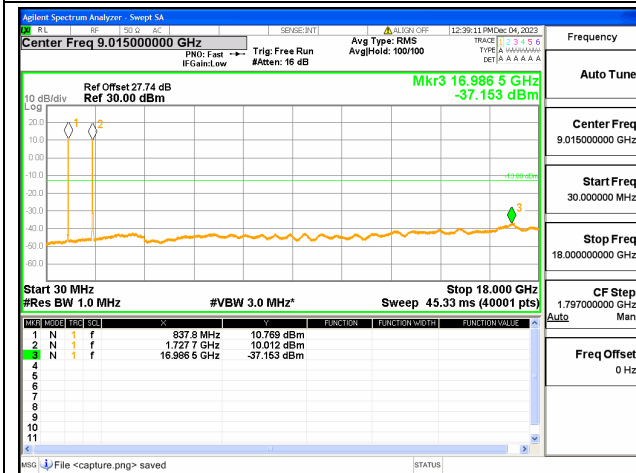
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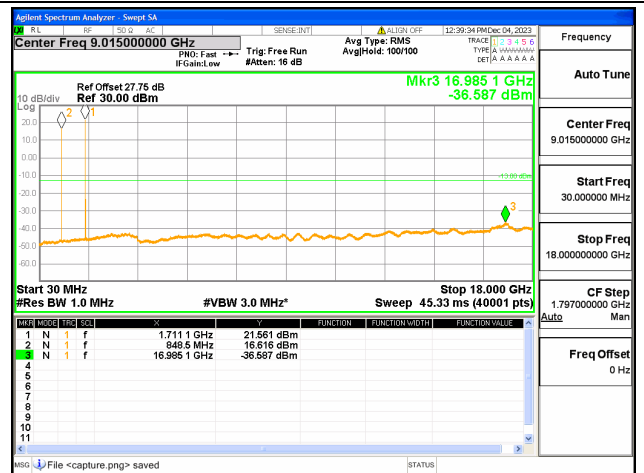
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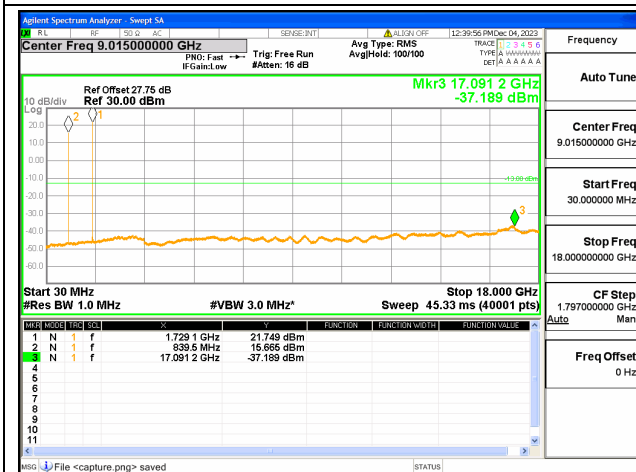
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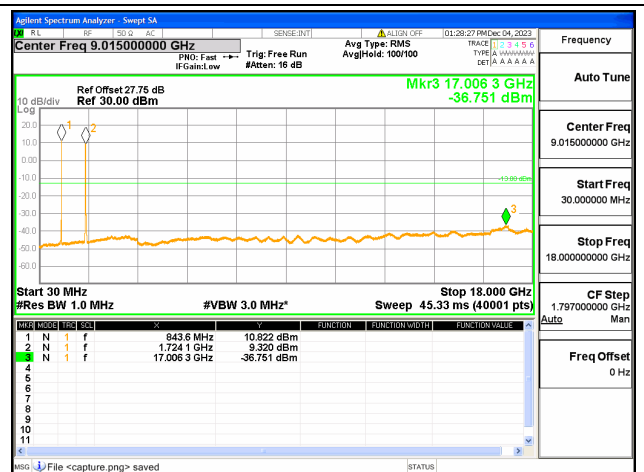
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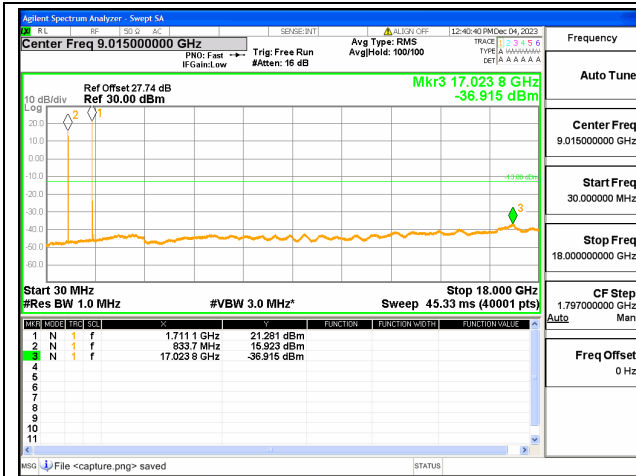
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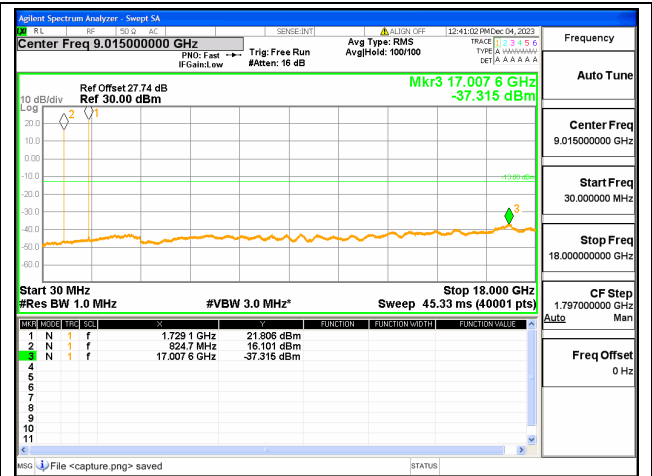
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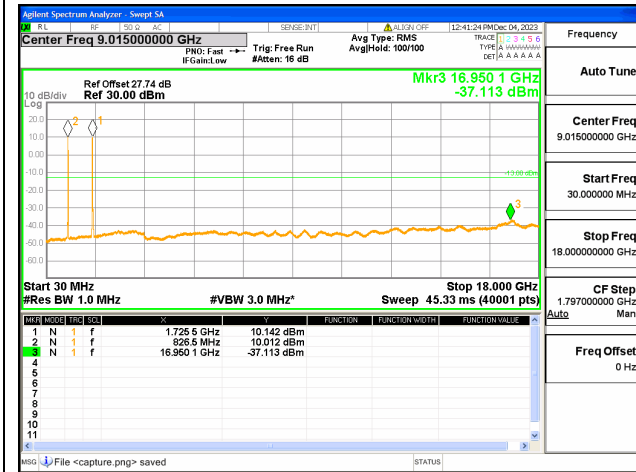
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1@0-1@49

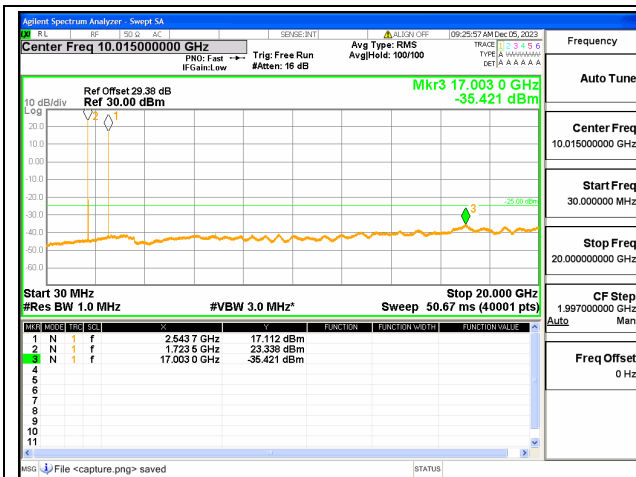


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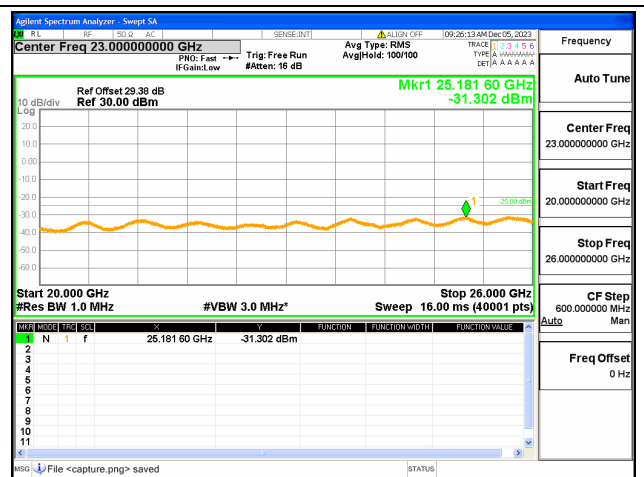


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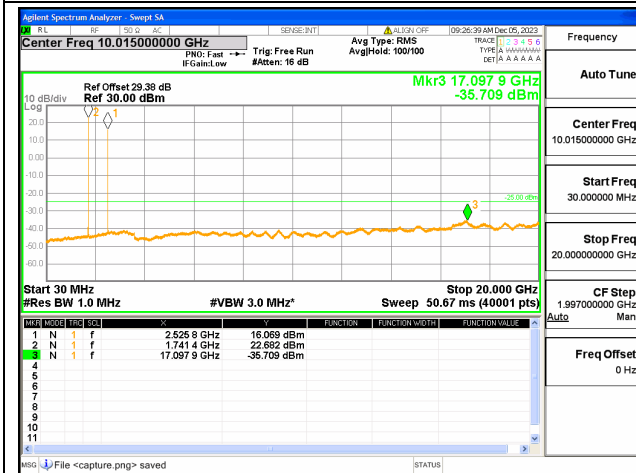
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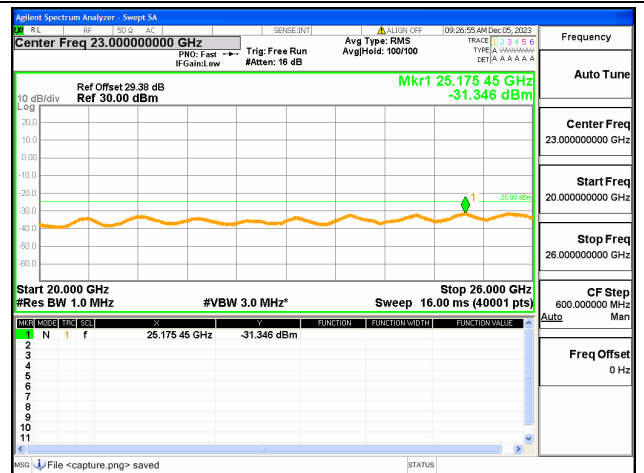
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CH / 1@0-1@99



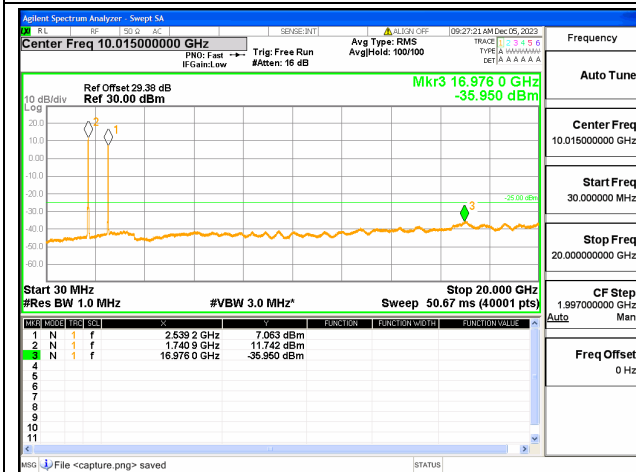
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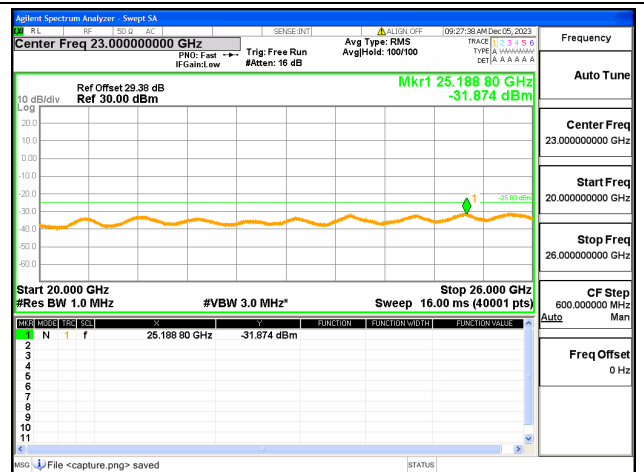
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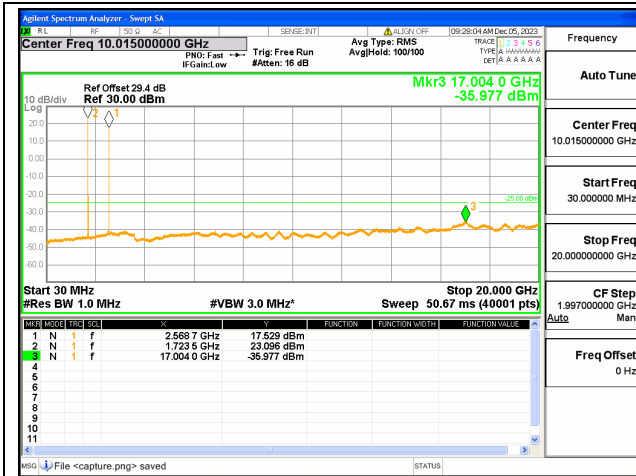
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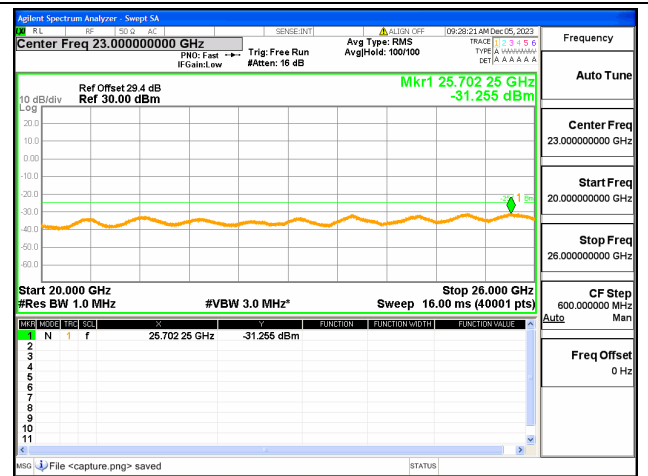
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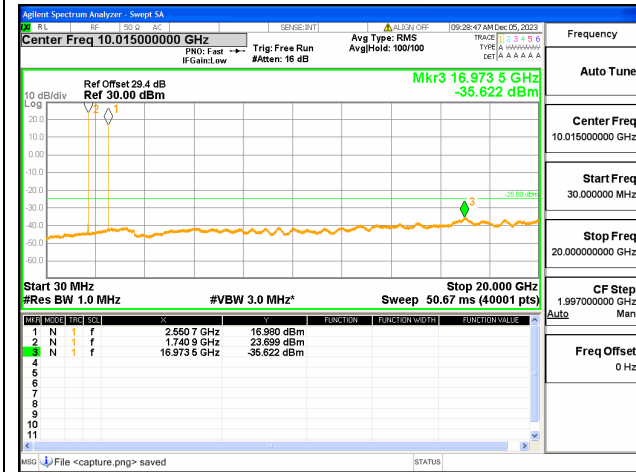
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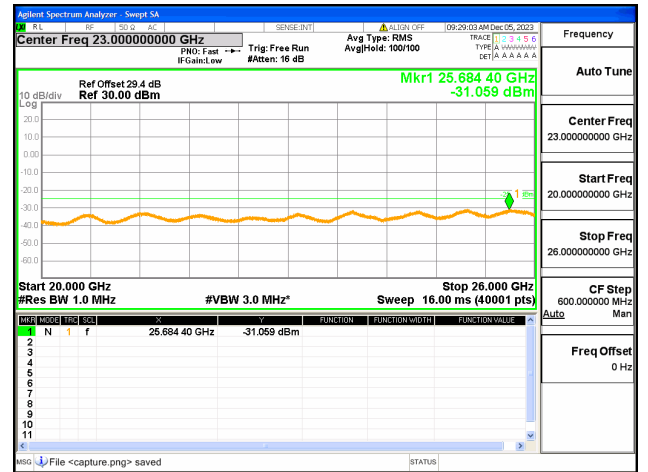
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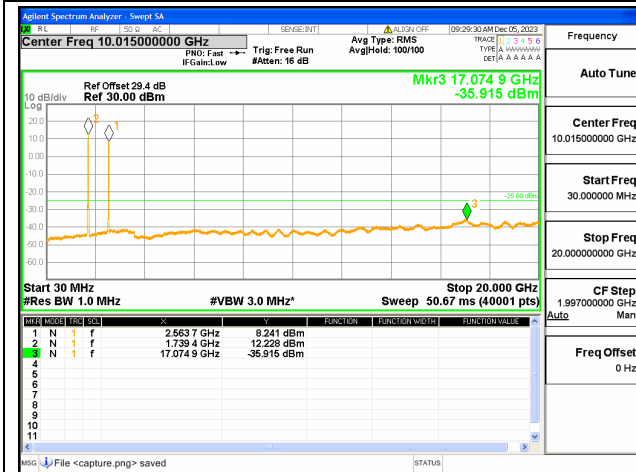
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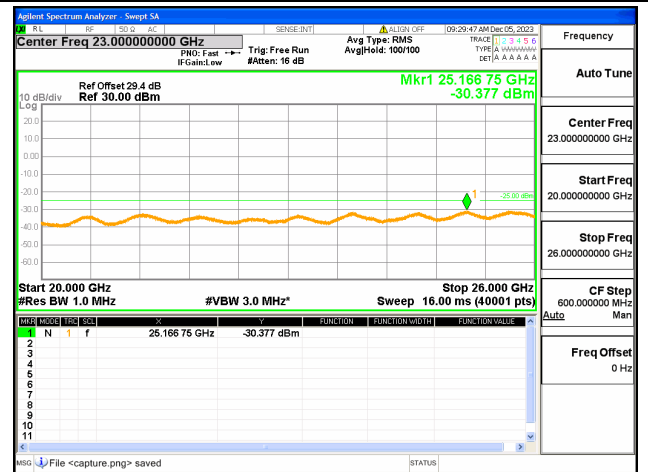
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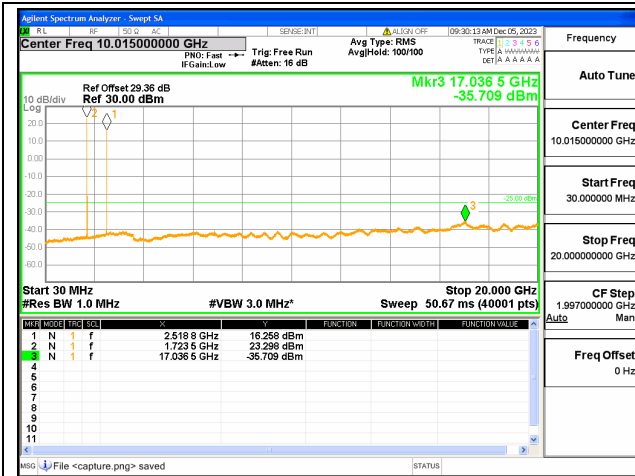
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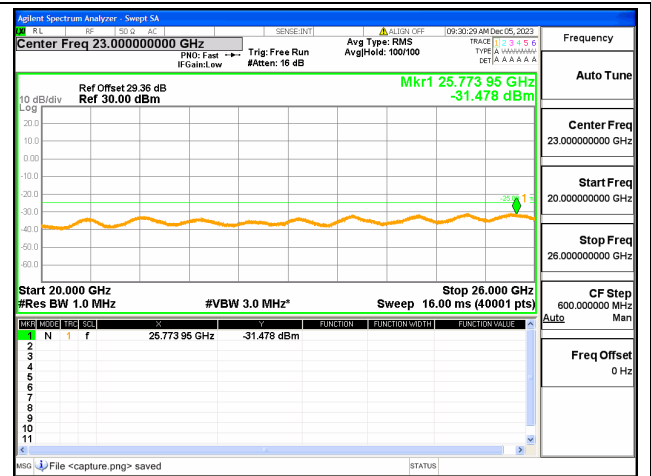
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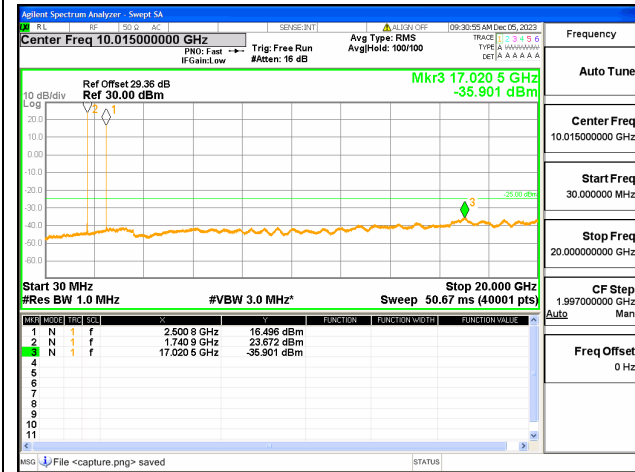
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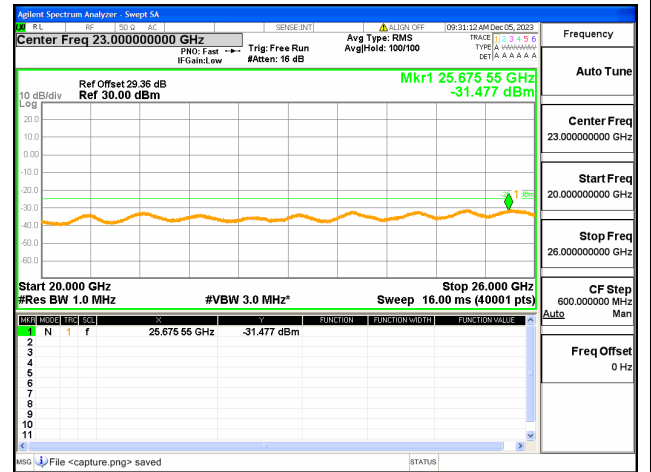
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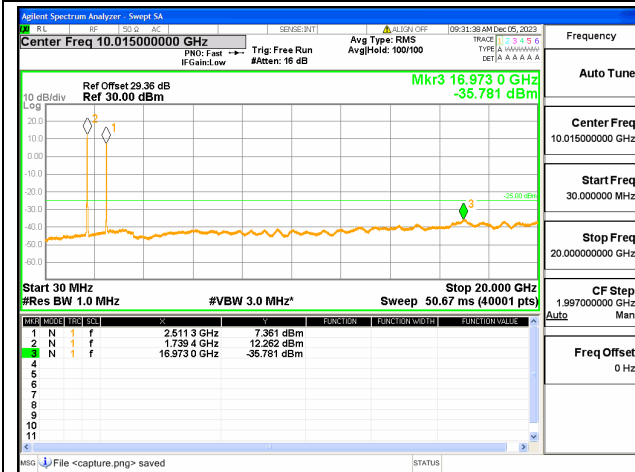
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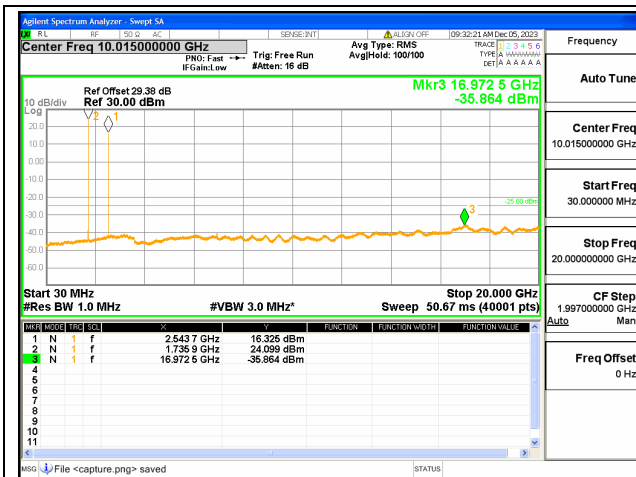
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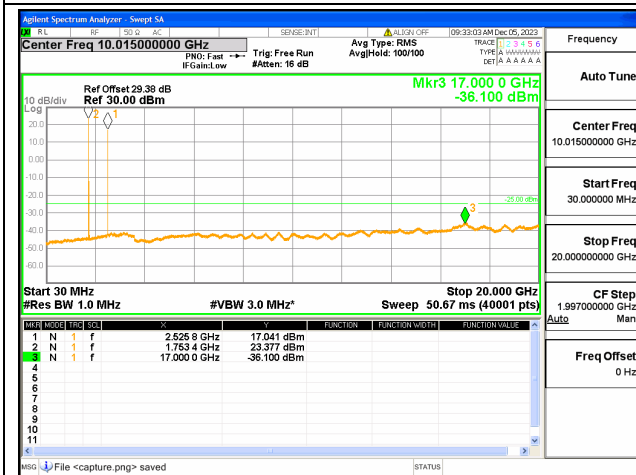
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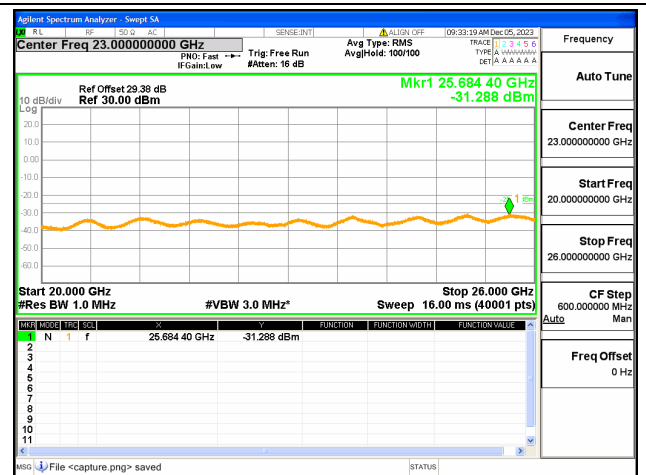
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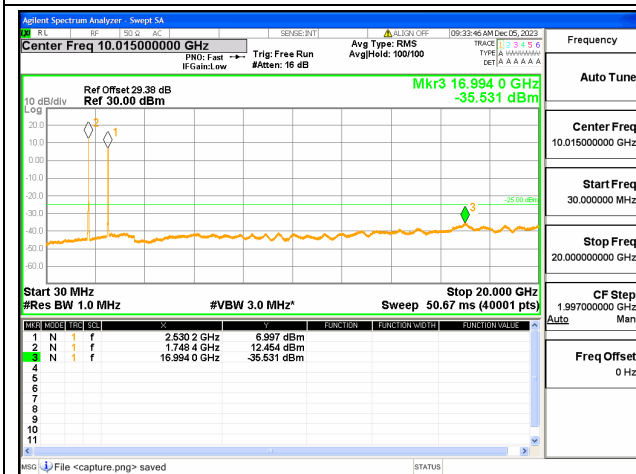
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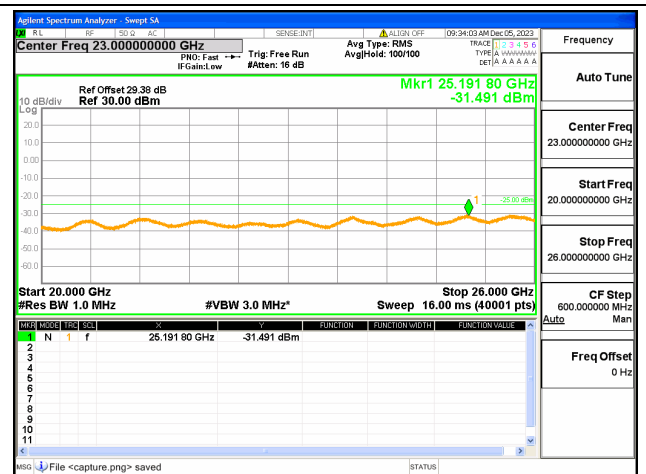
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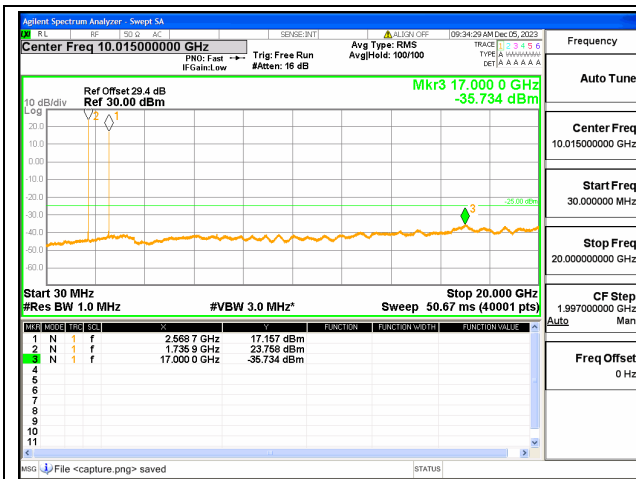
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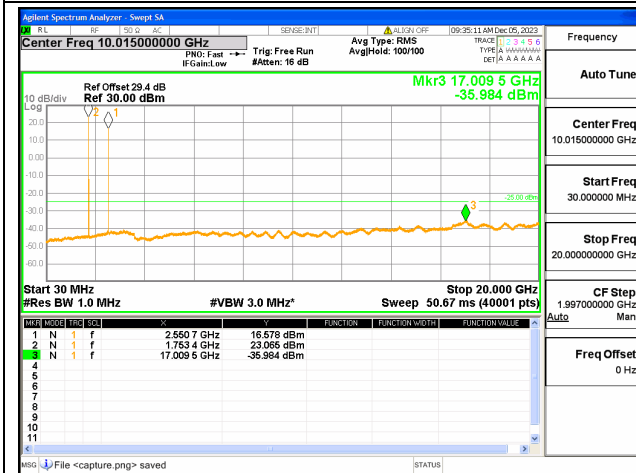
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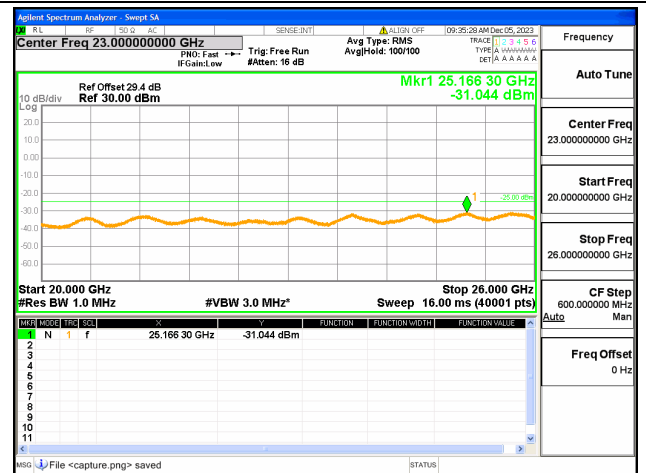
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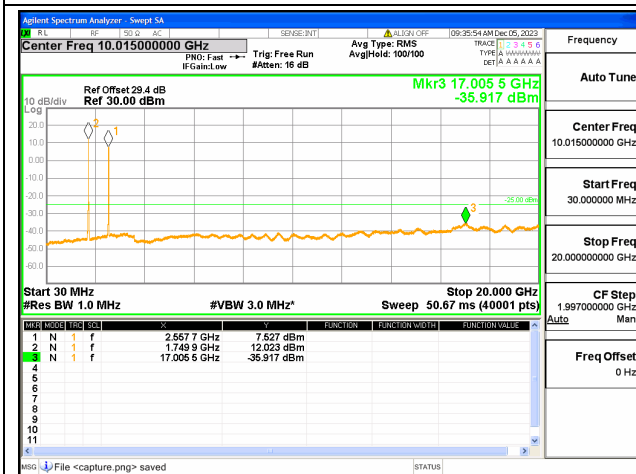
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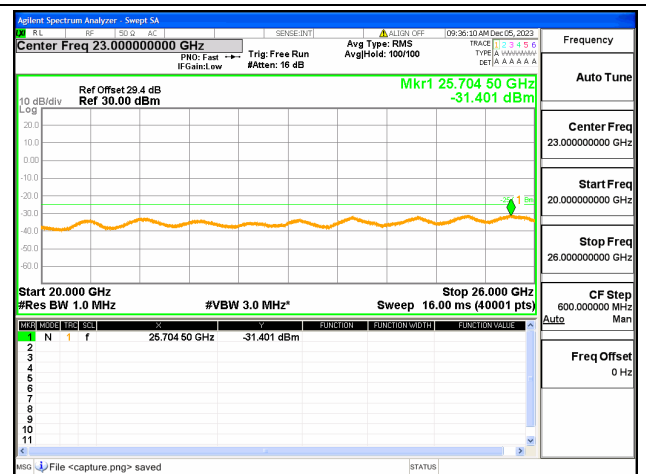
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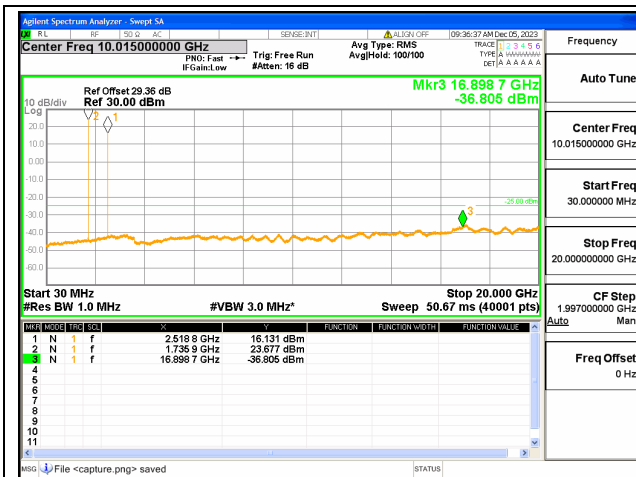
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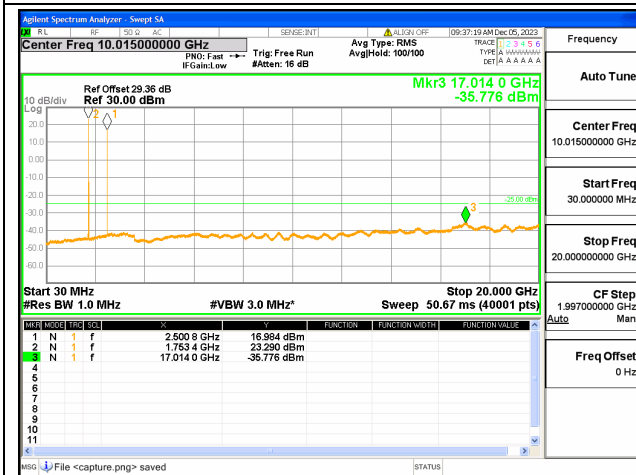
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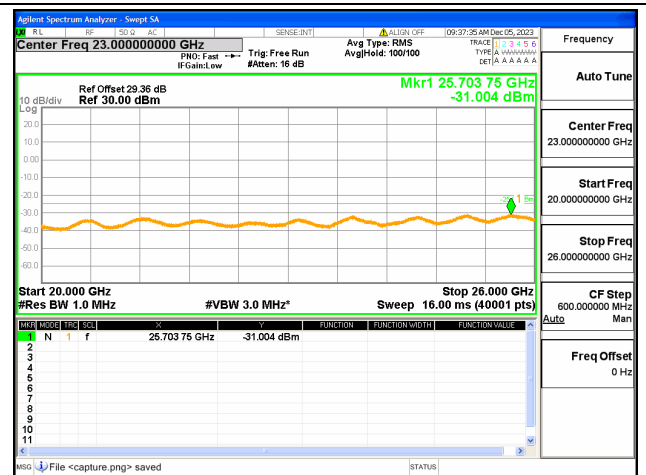
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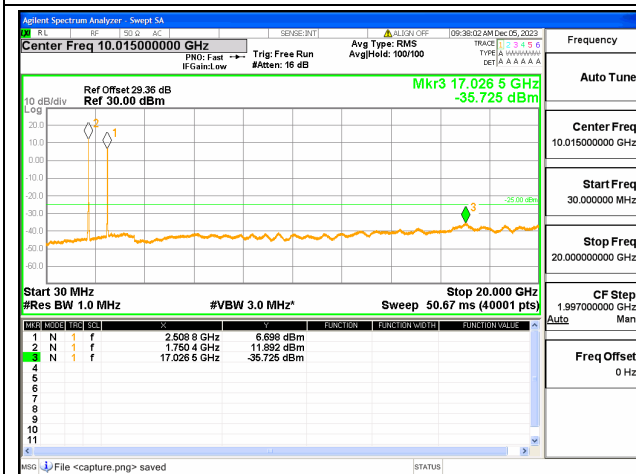
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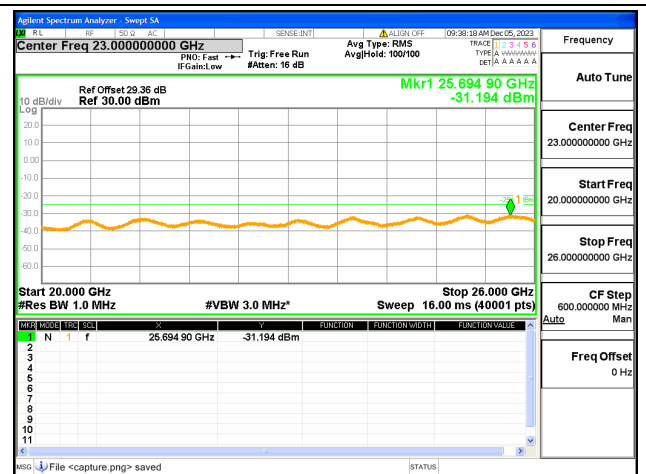
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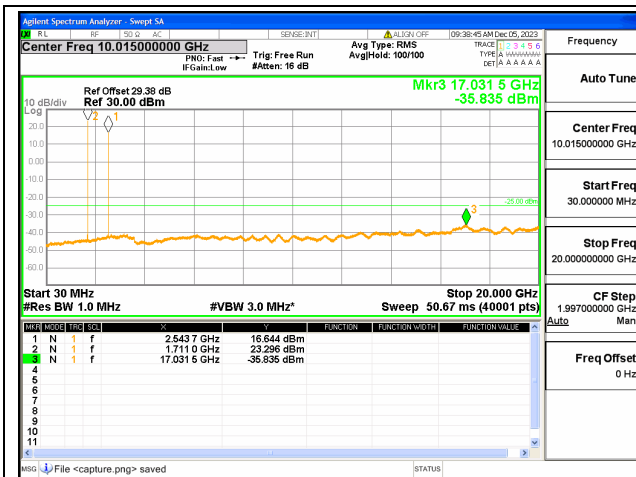
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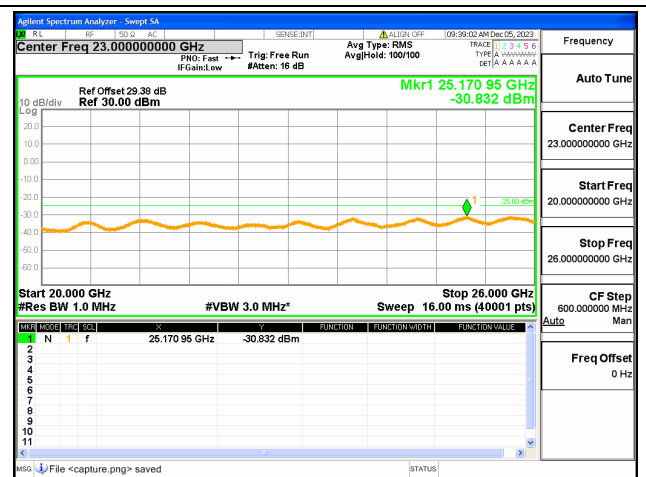
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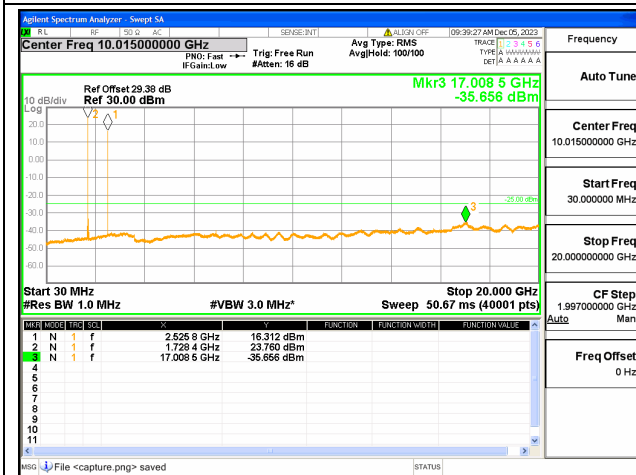
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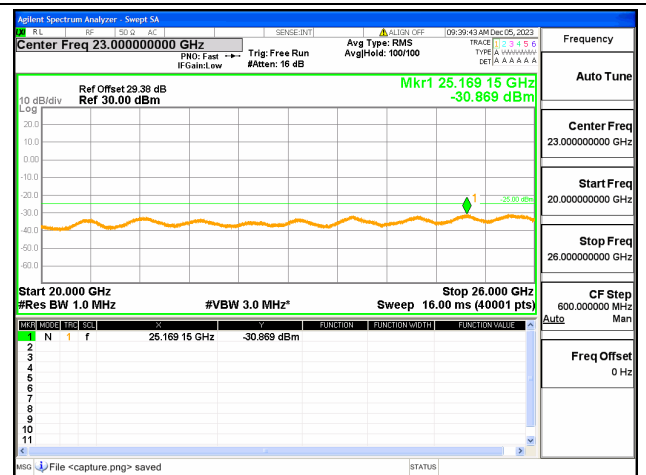
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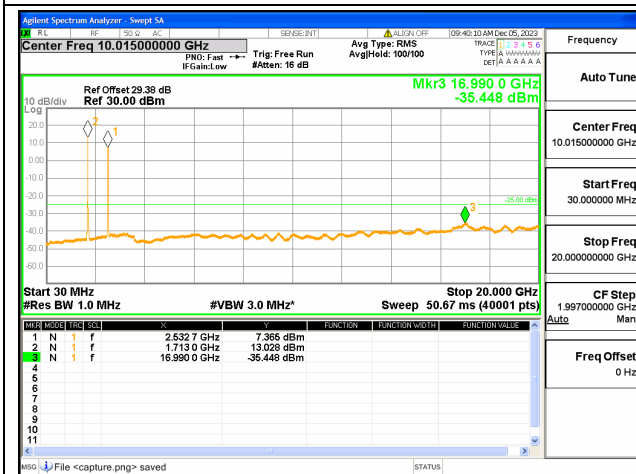
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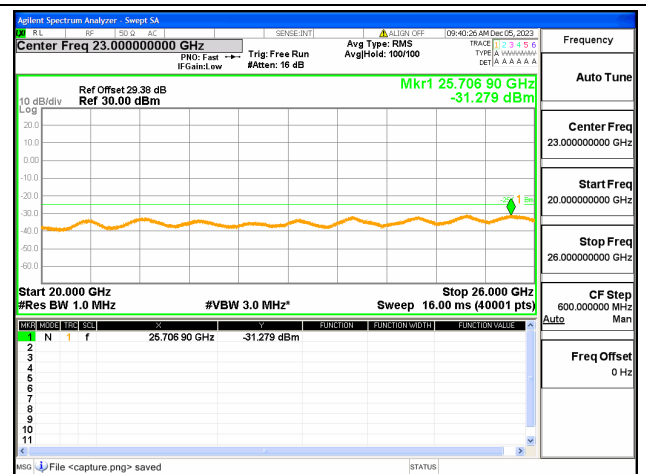
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Low+Mid CH / 1@99-1@0



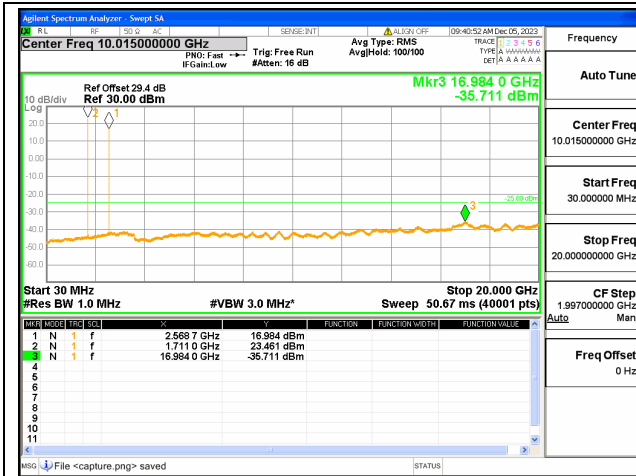
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Low+Mid CH / 1@99-1@0



4A-7A-30M-20G / 20+20MHz / QPSK /
Low+Mid CH / 100@0-100@0



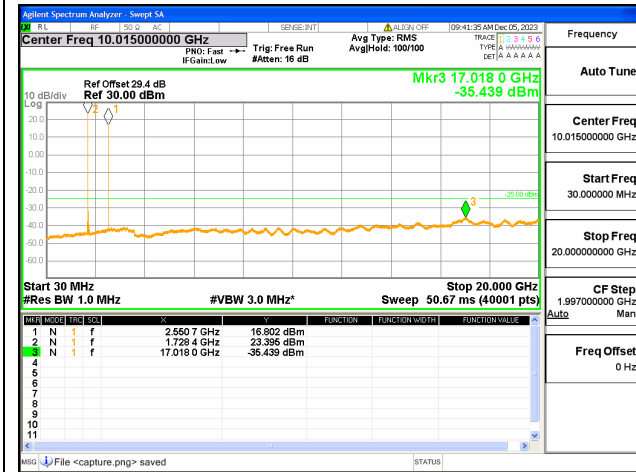
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Low+Mid CH / 100@0-100@0



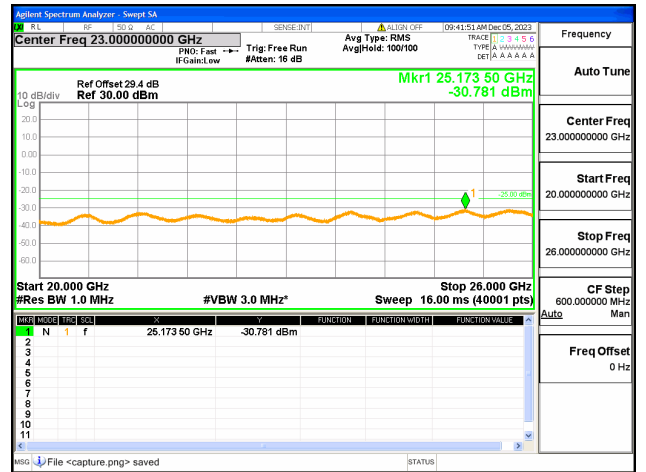
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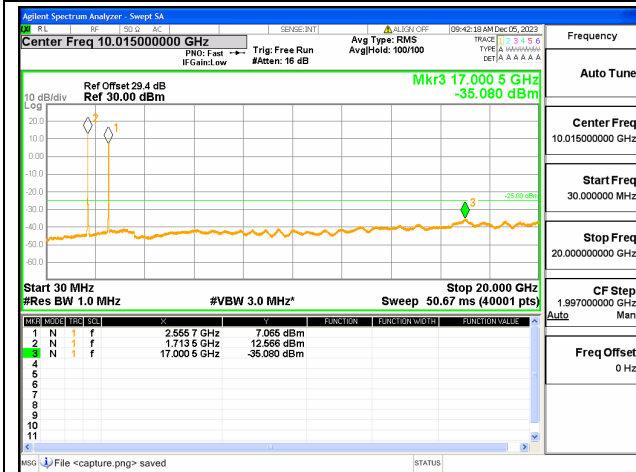
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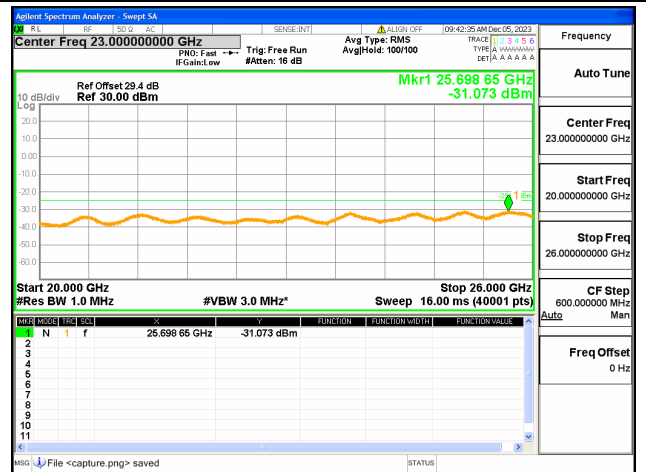
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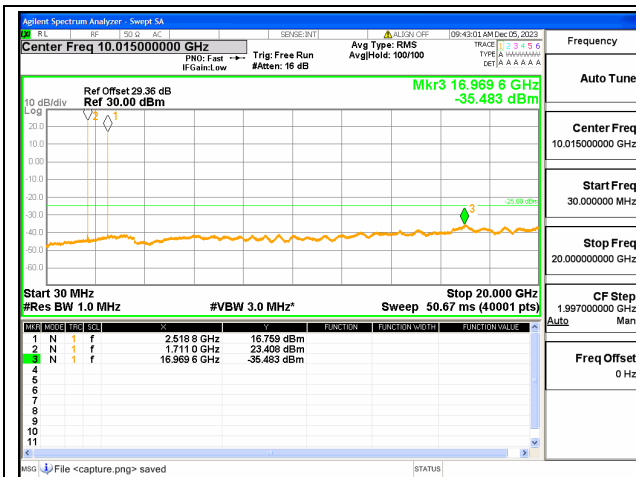
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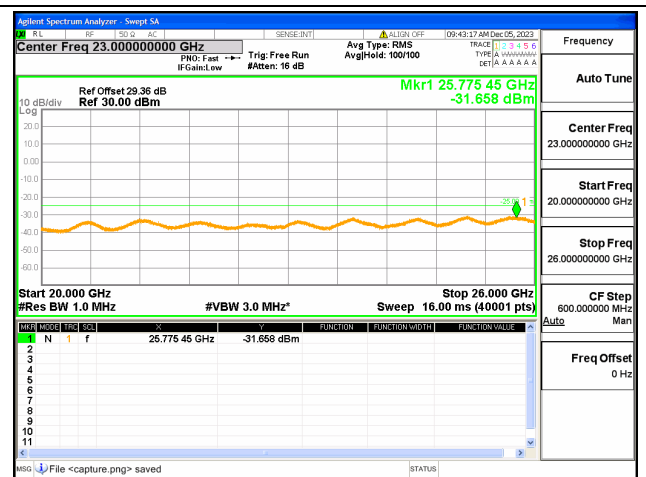
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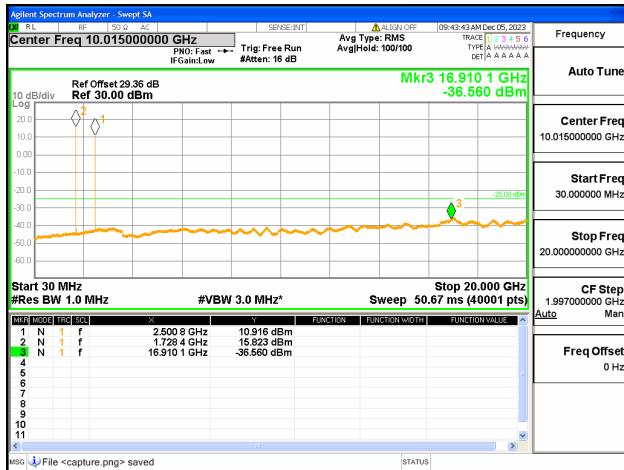
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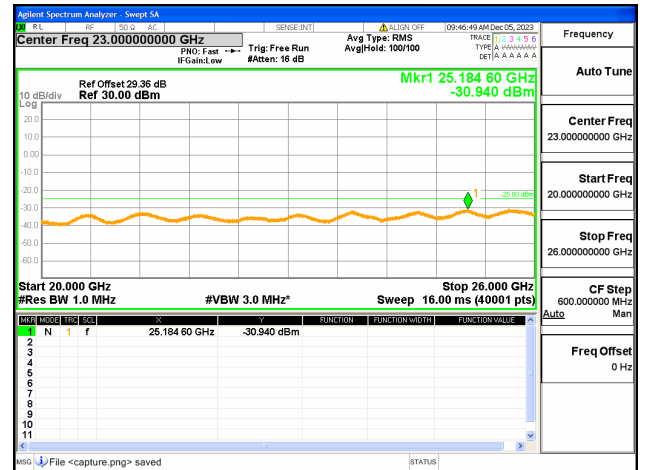
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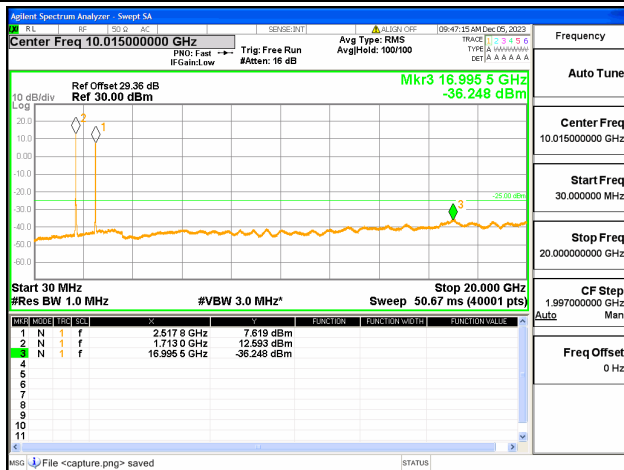
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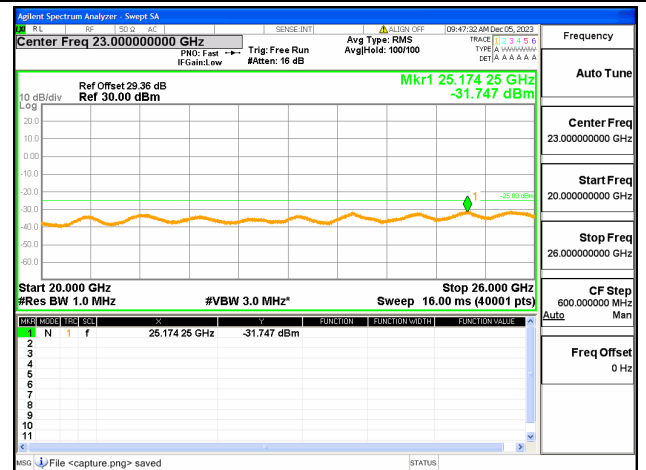
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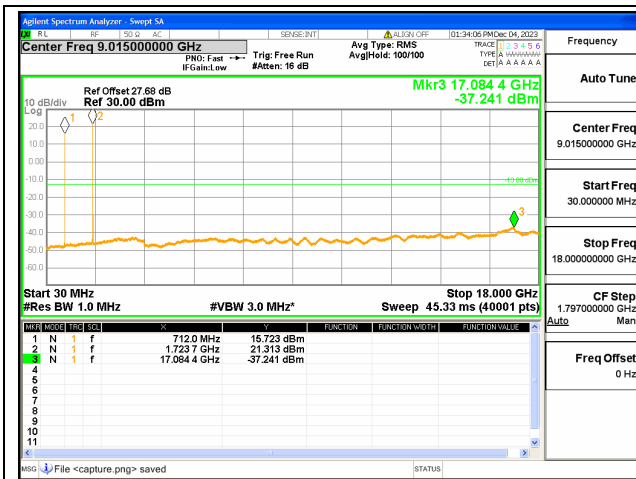
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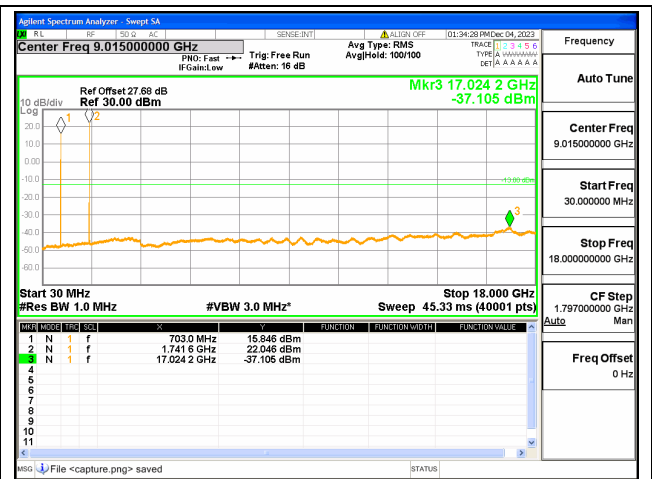
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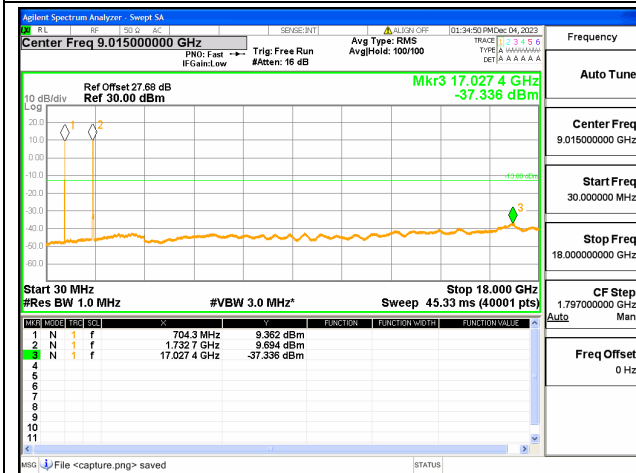
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Low+Low CH / 100@0-100@0



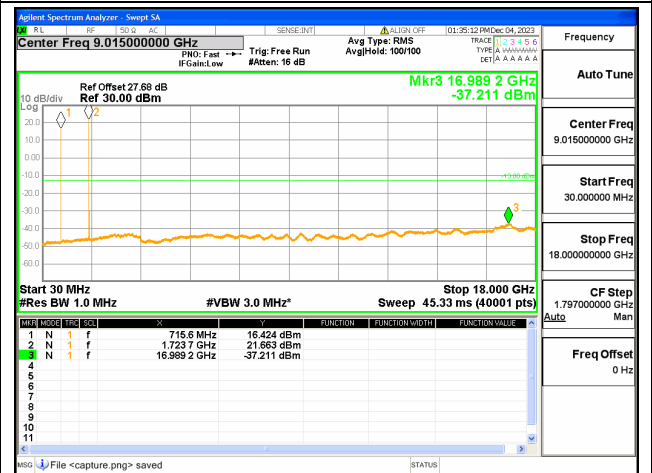
4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@49



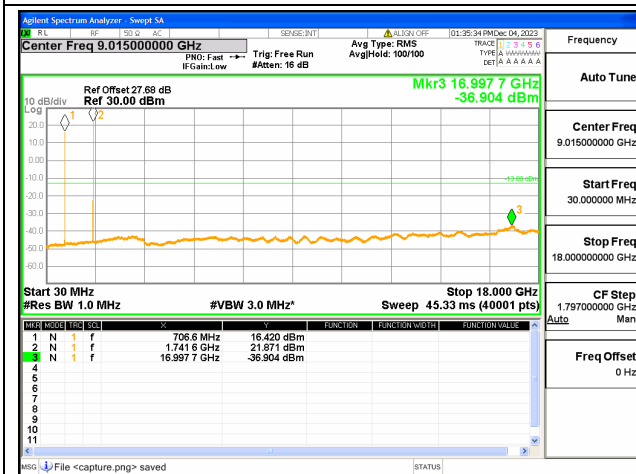
4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@0



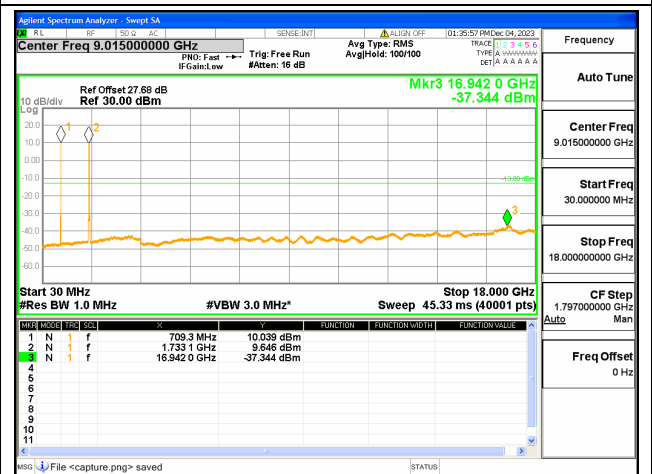
4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
100@0-50@0



4A-12A / 20+10MHz / QPSK / Mid+High CH /
1@0-1@49



4A-12A / 20+10MHz / QPSK / Mid+High CH /
1@99-1@0



4A-12A / 20+10MHz / QPSK / Mid+High CH /
100@0-50@0