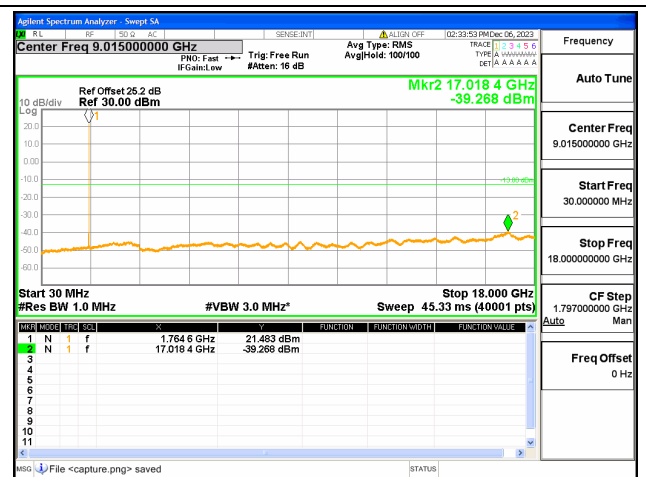
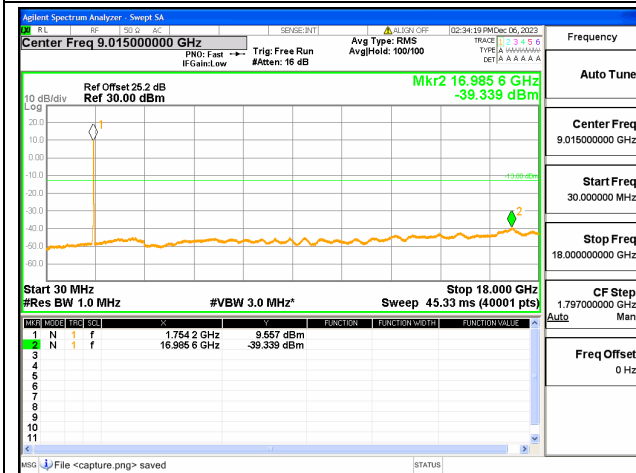


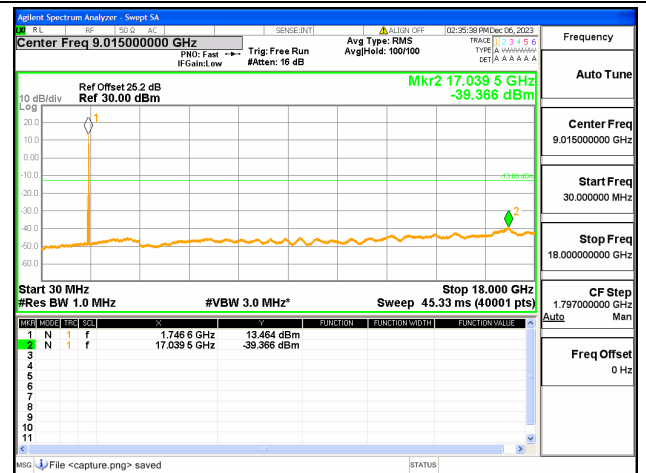
66C / 15+15MHz / QPSK / High CH /  
1@0-1@74



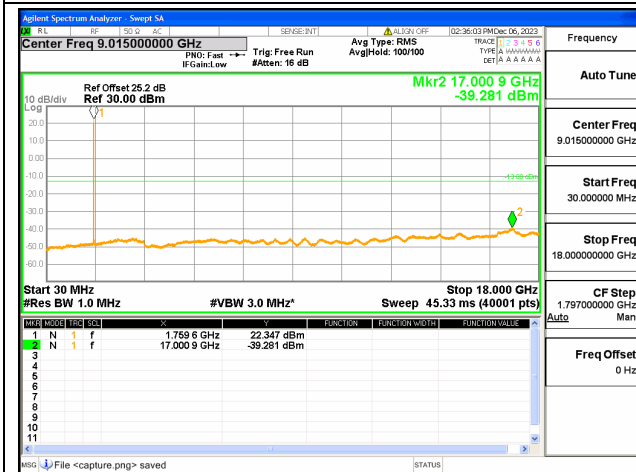
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1@74-1@0



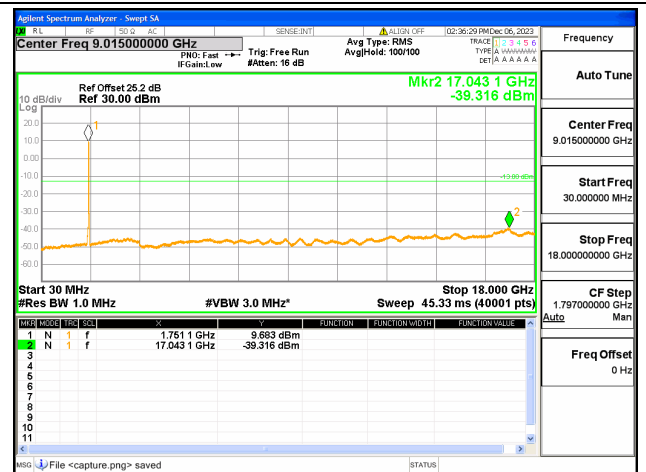
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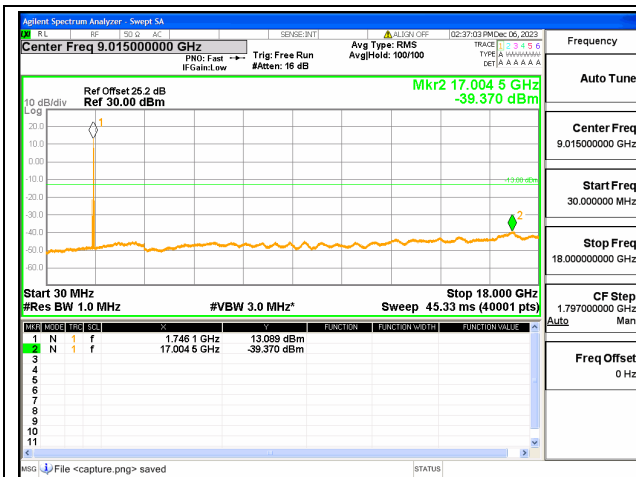
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1@0-1@99



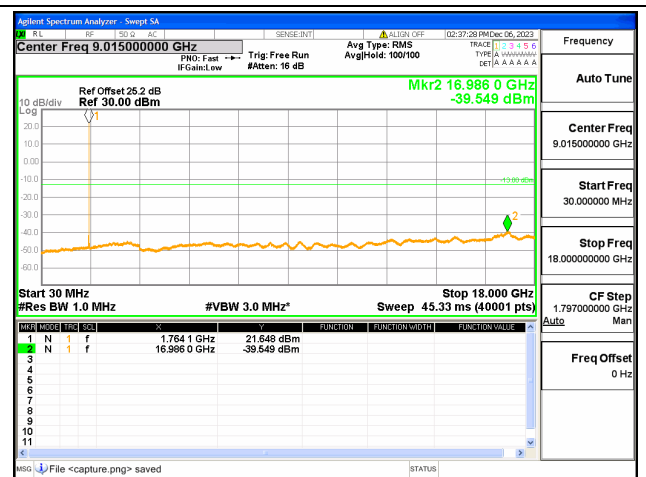
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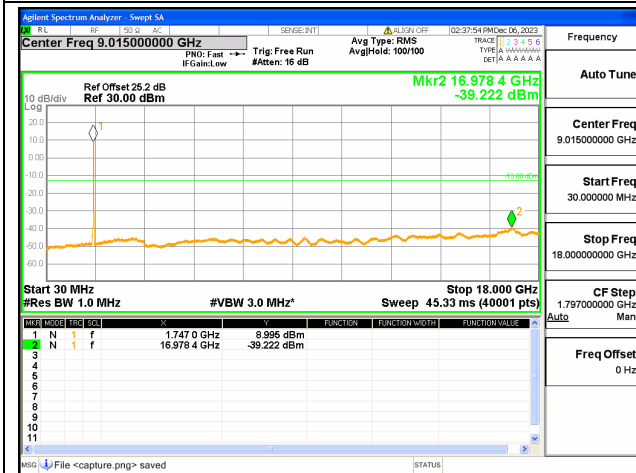
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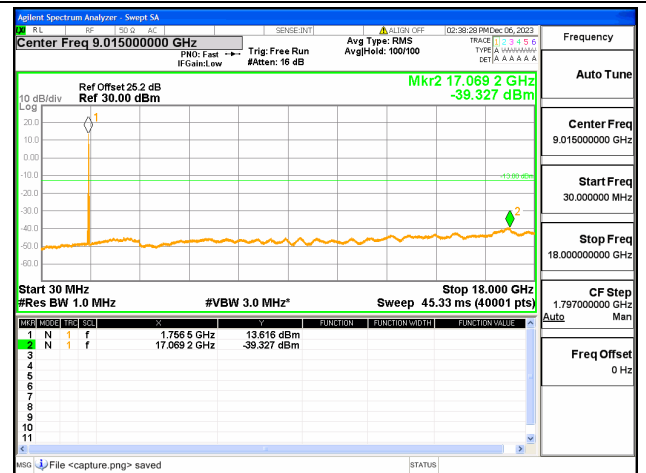
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1@0-1@74



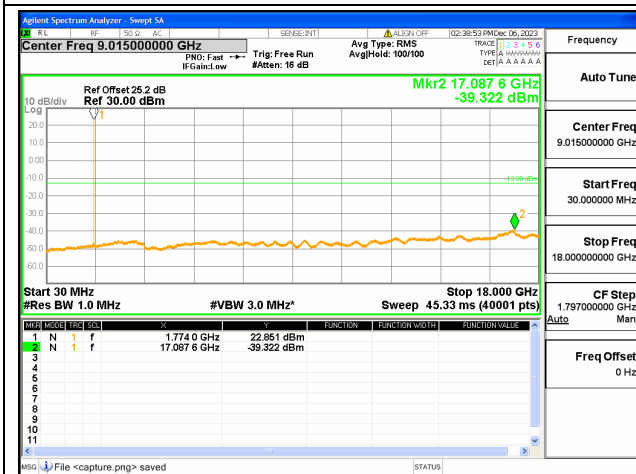
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1@99-1@0



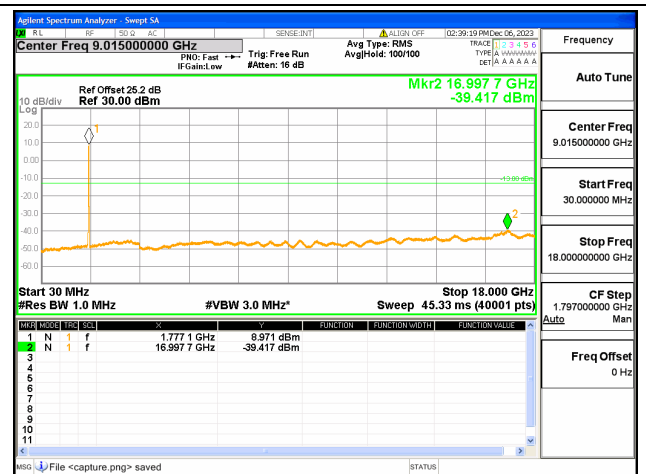
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100@0-75@0



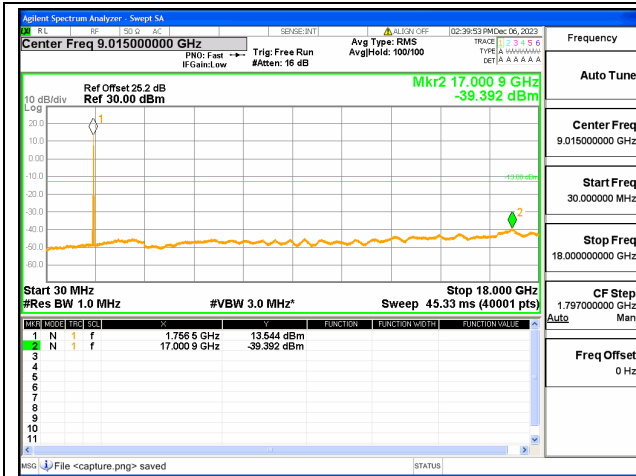
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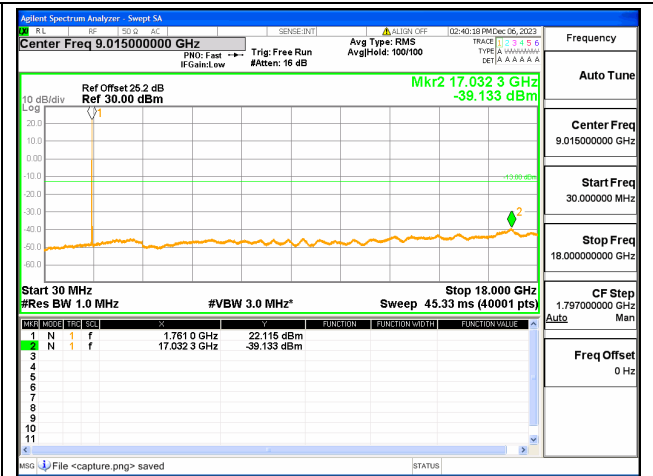
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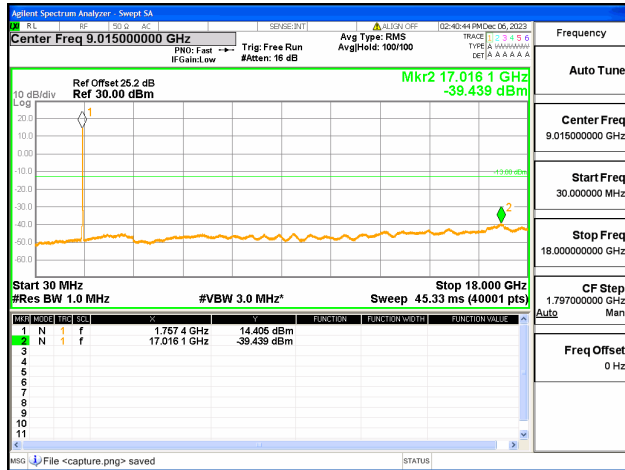
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100@0-25@0



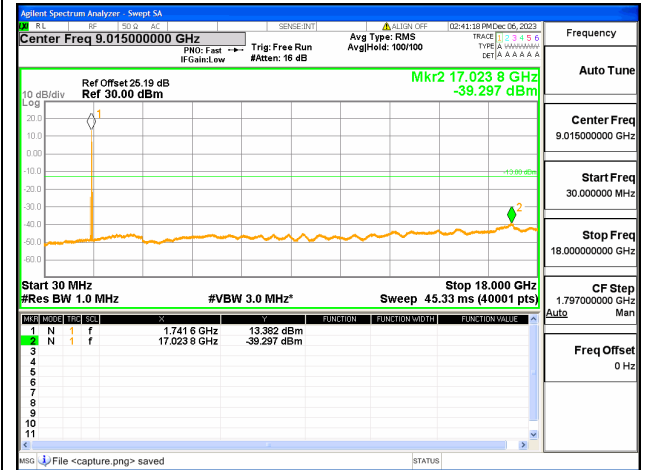
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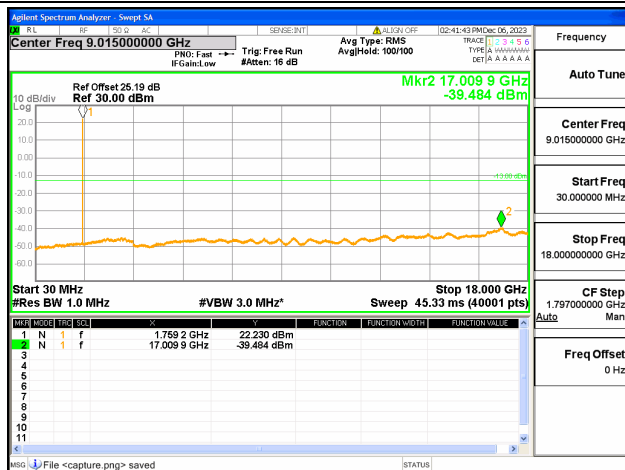
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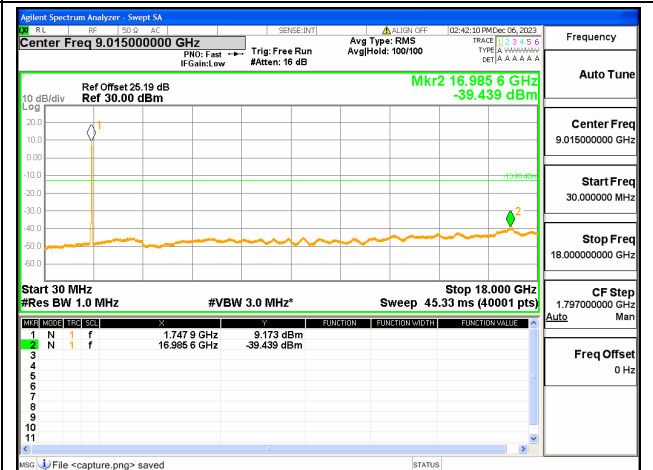
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25@0-100@0



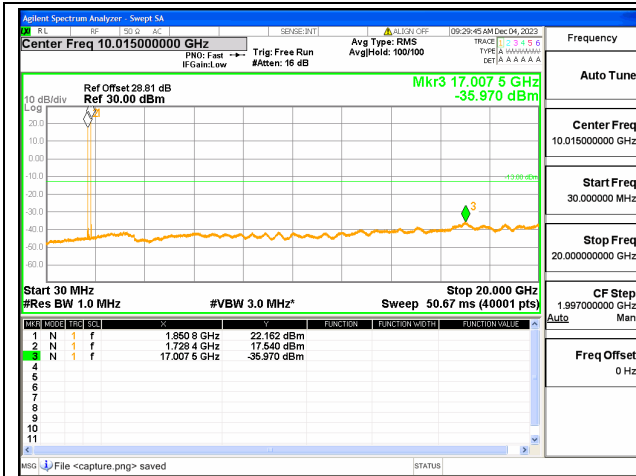
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1@0-1@99



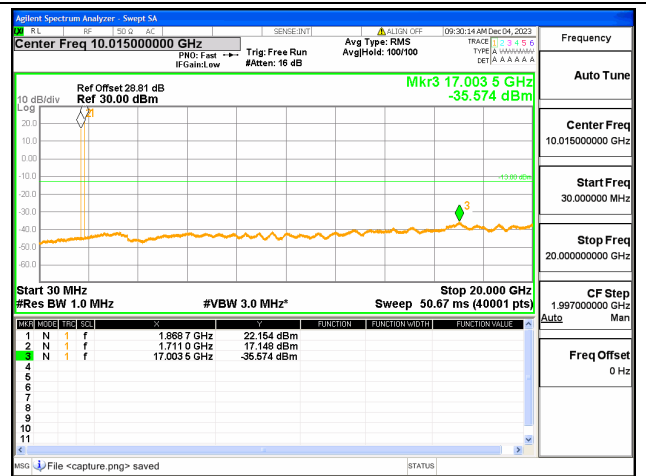
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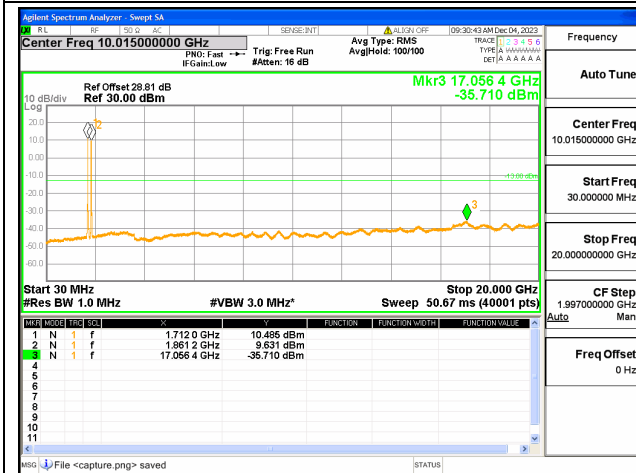
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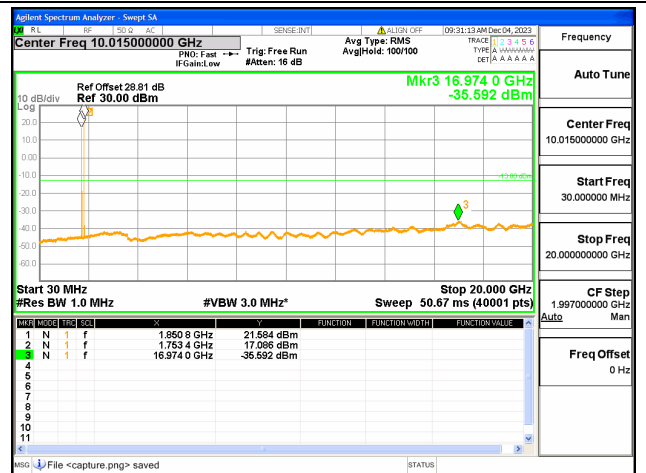
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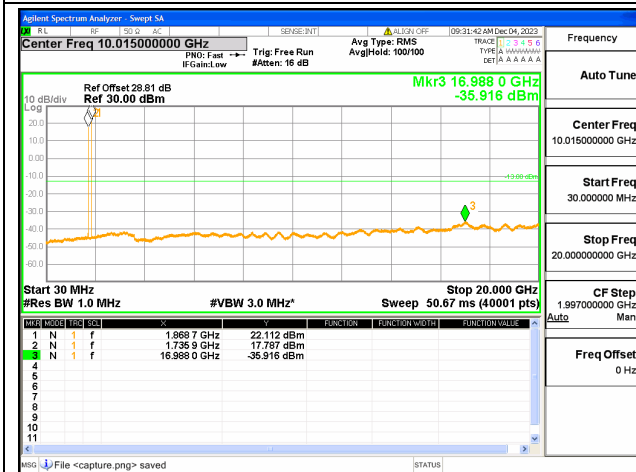
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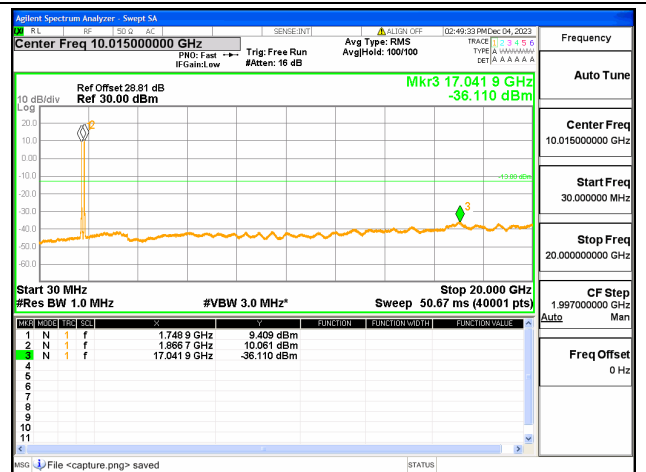
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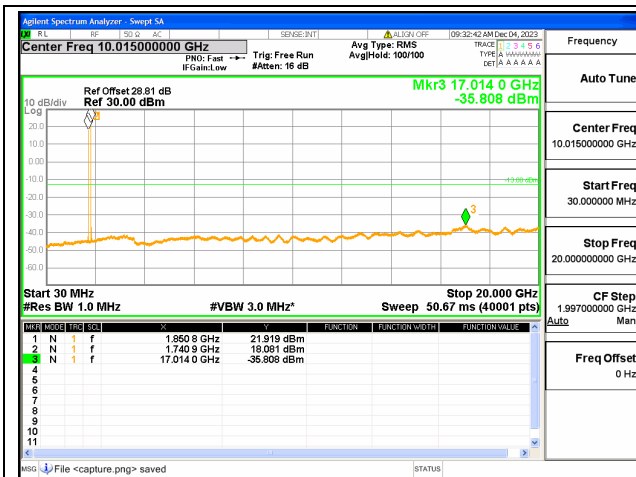
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1@0-1@99



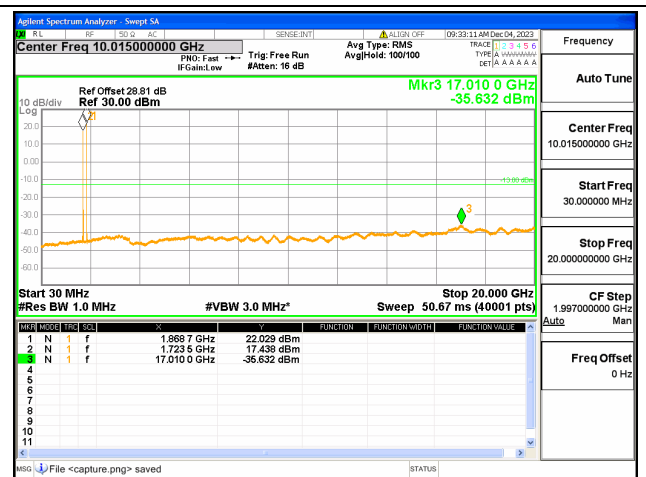
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1@99-1@0



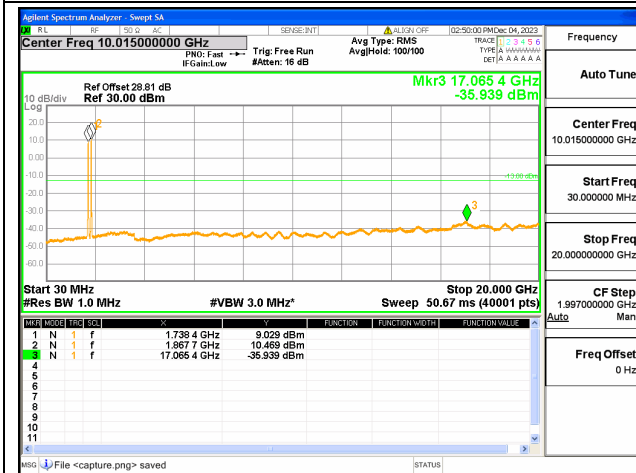
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100@0-100@0



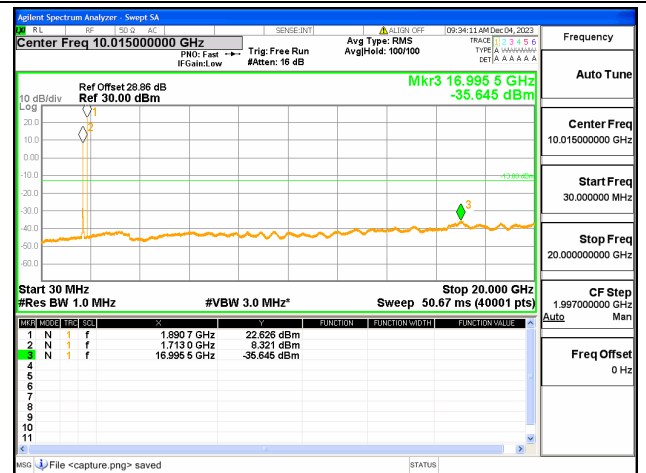
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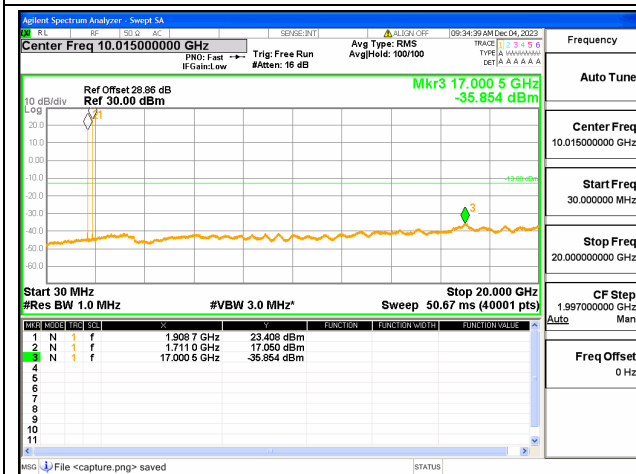
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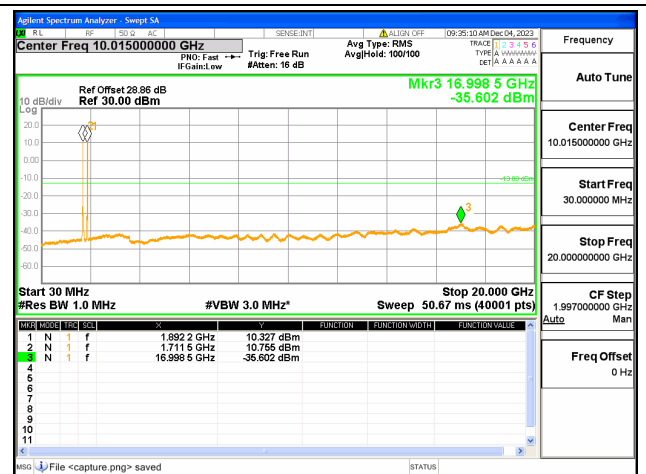
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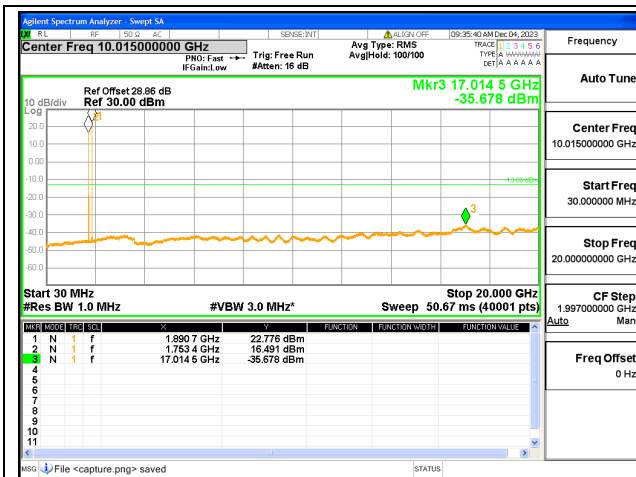
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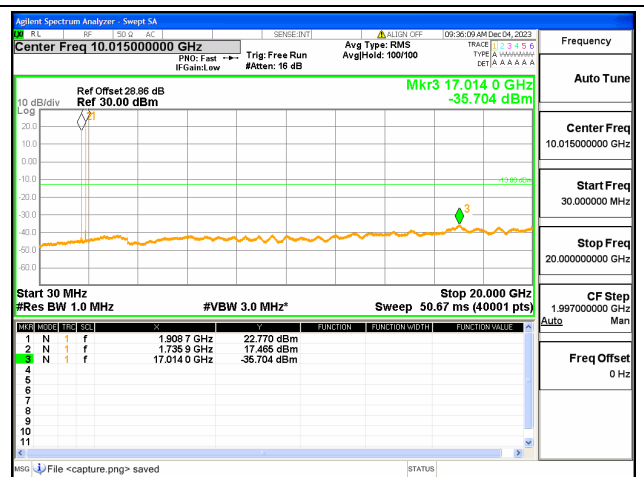
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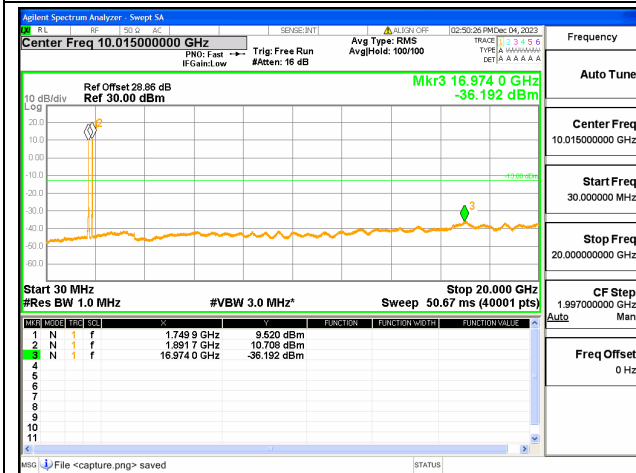
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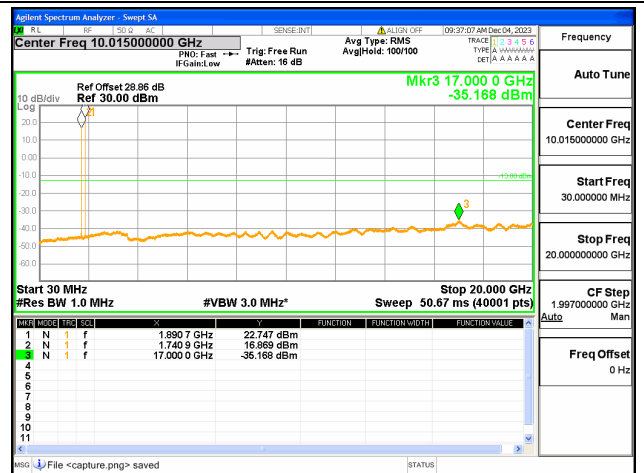
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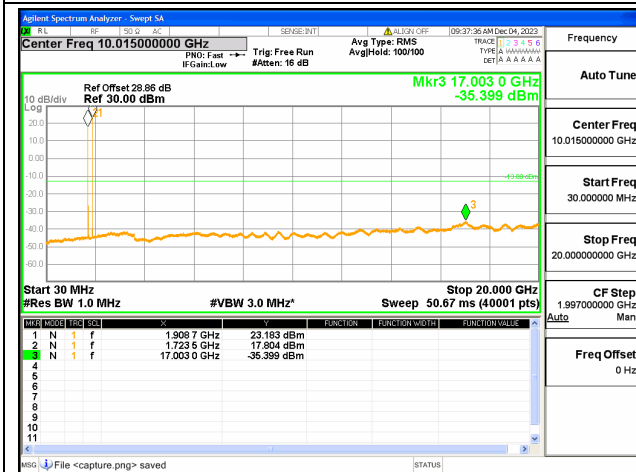
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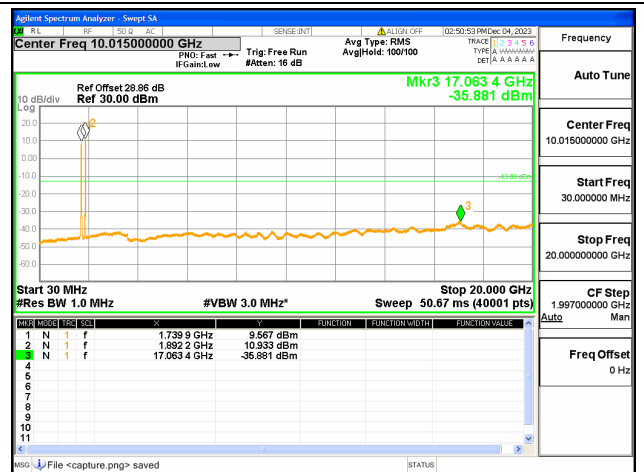
2A-4A / 20+20MHz / QPSK / High+High CH / 100@0-100@0



2A-4A / 20+20MHz / QPSK / High+Mid CH / 1@0-1@99

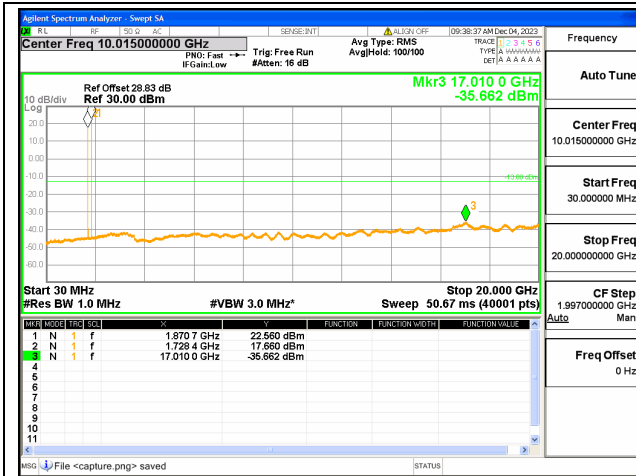


2A-4A / 20+20MHz / QPSK / High+Mid CH / 1@99-1@0

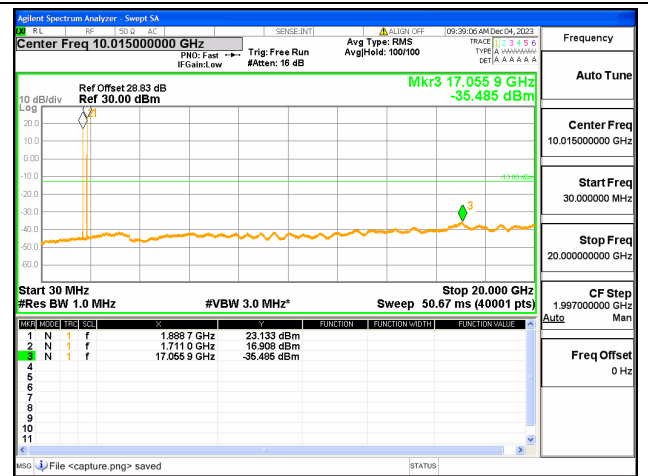


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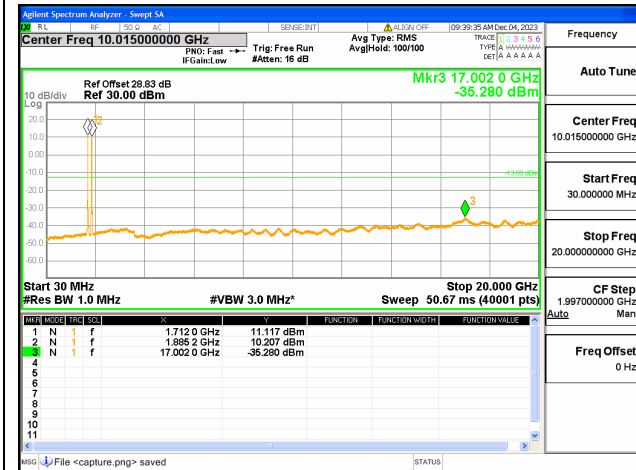




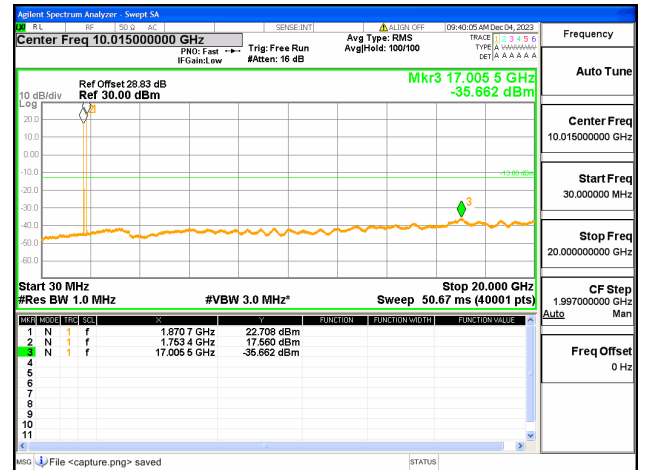
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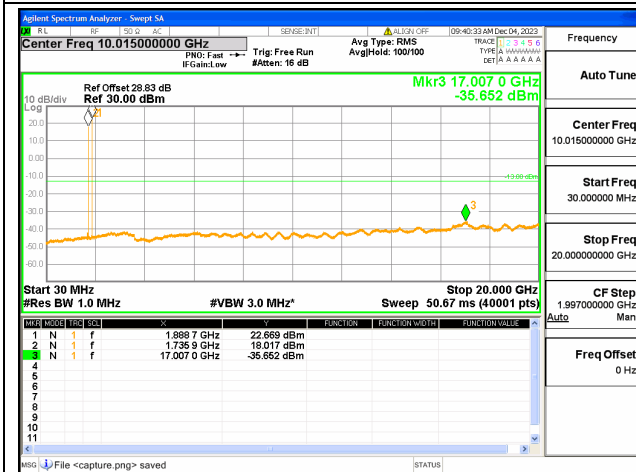
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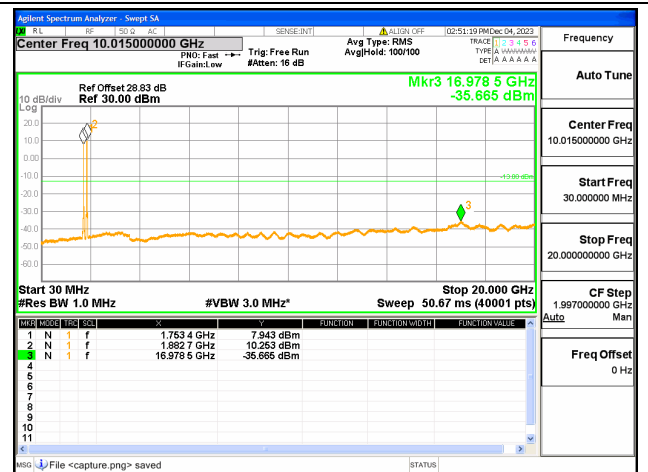
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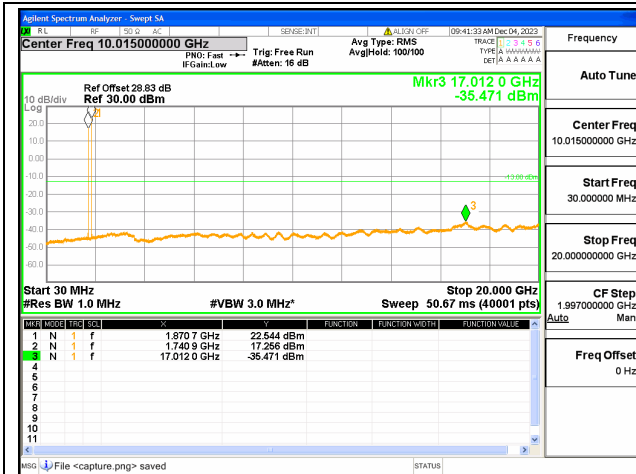
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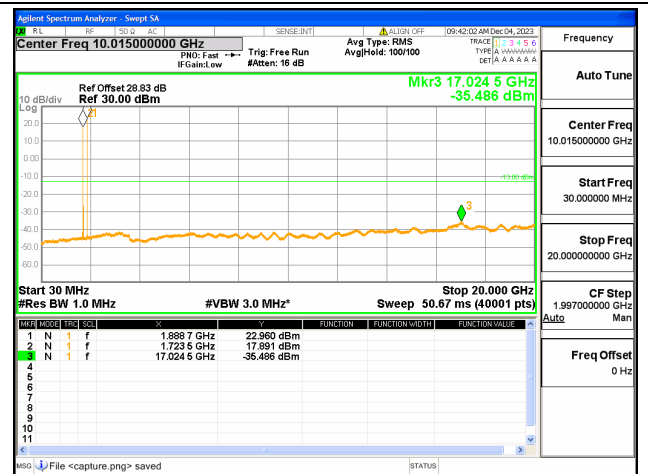
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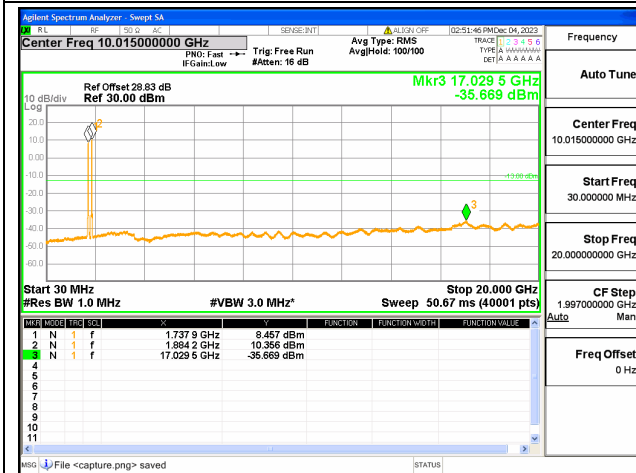
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2A-4A / 20+20MHz / QPSK / Mid+Mid CH /  
1@0-1@99



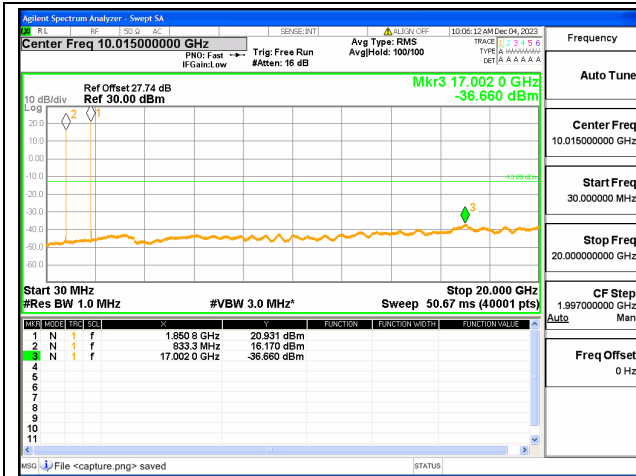
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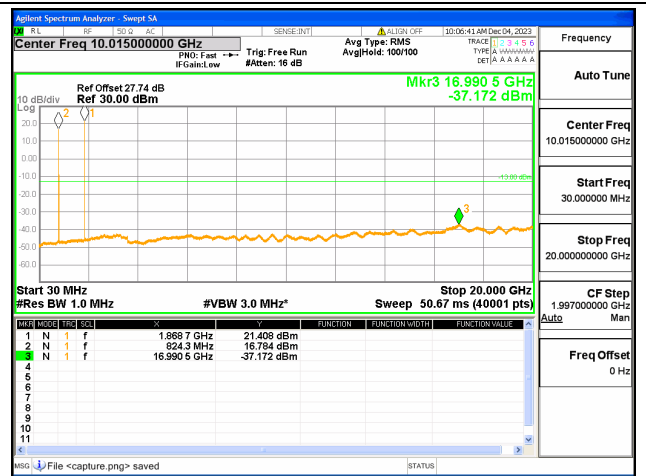
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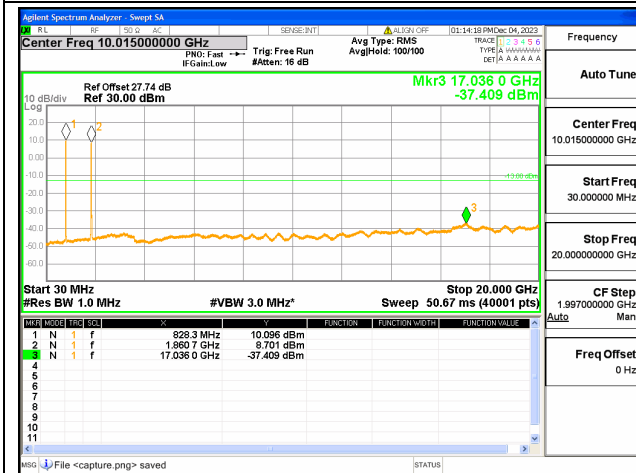




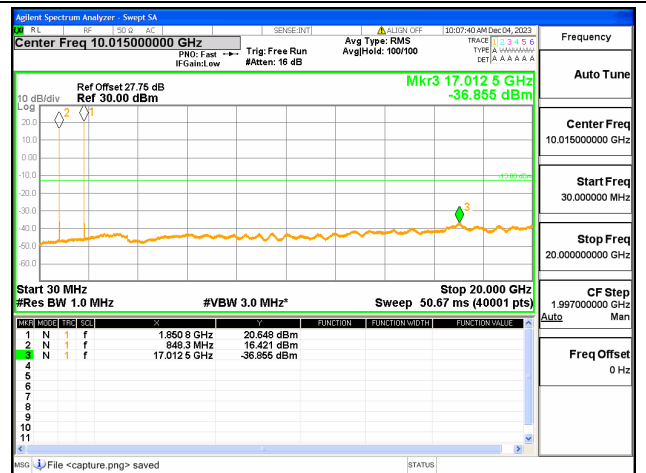
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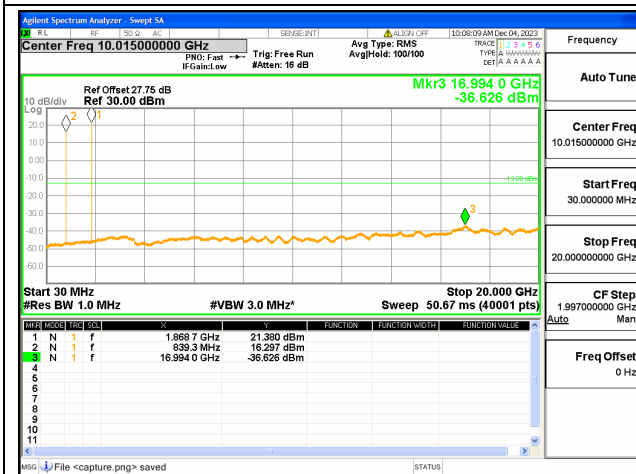
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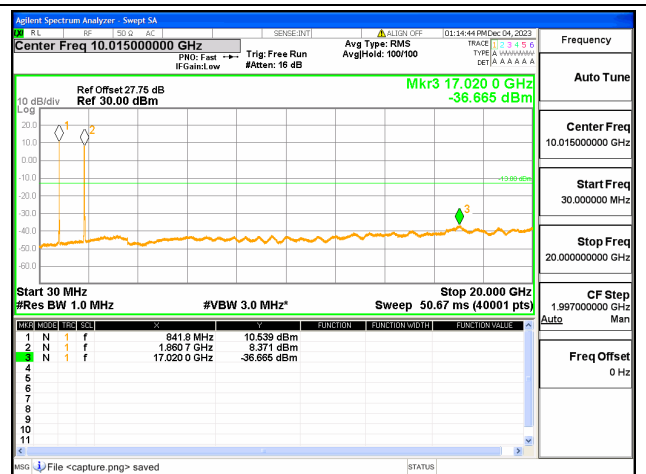
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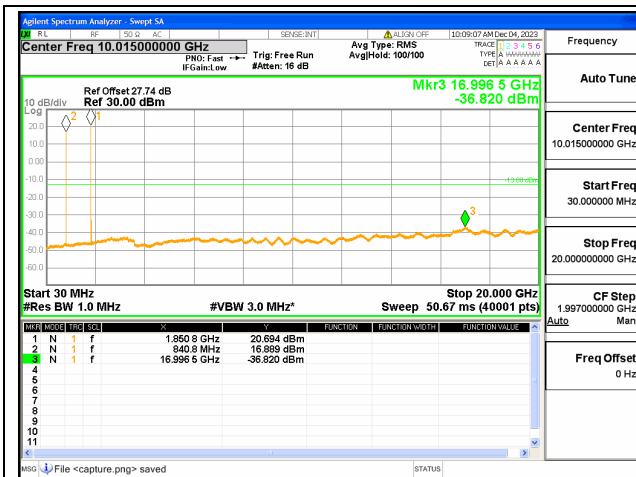
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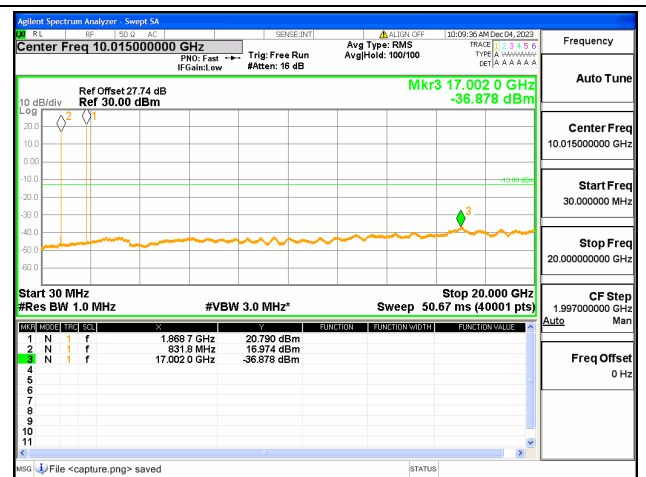
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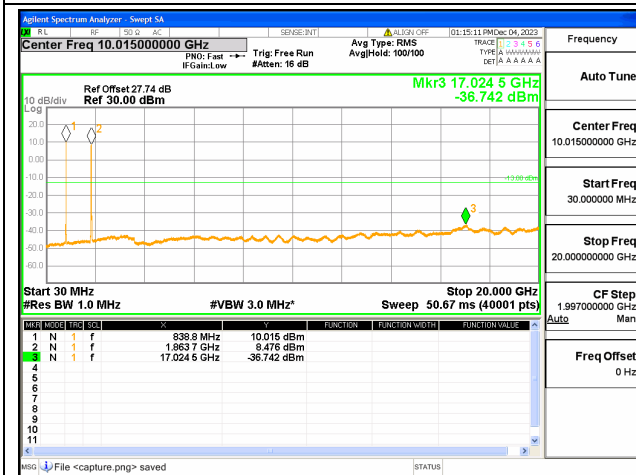
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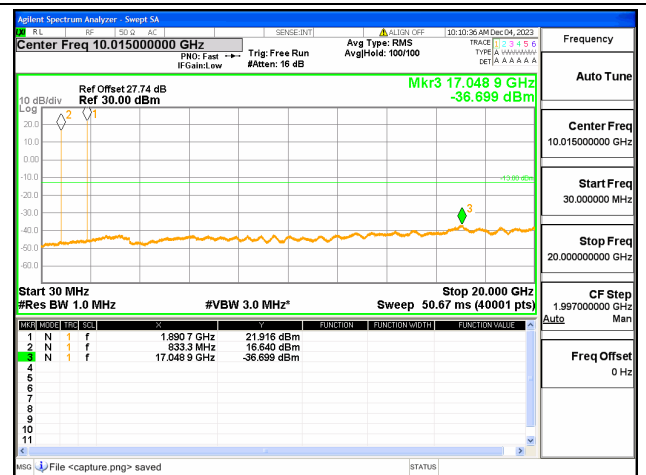
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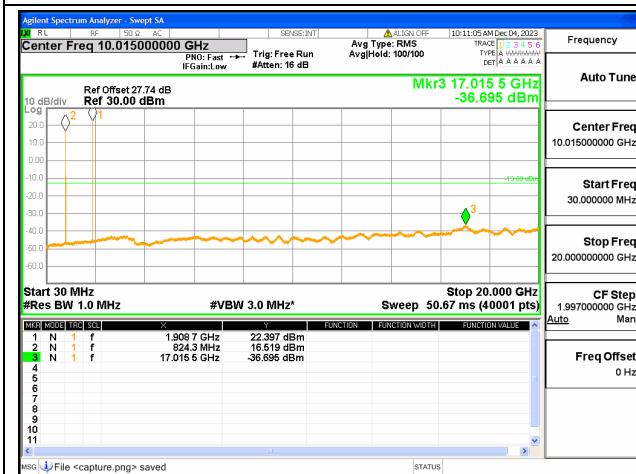
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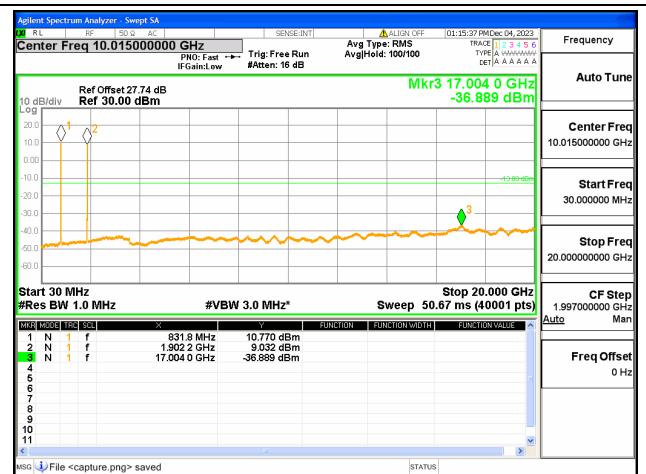
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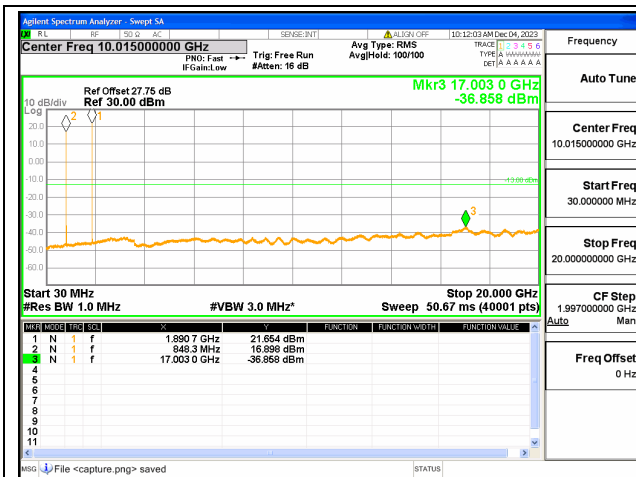
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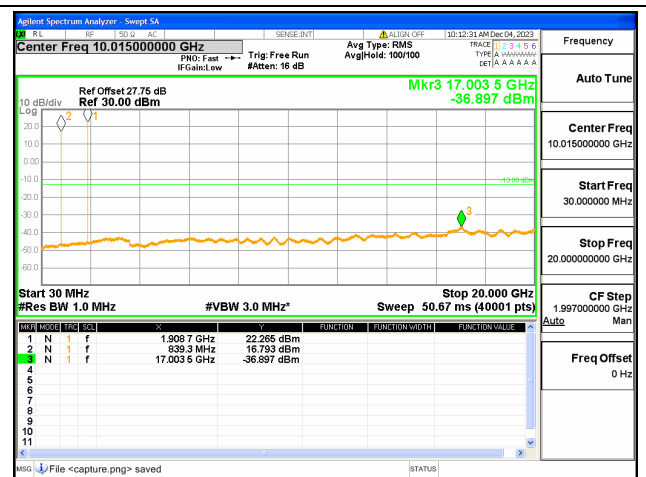
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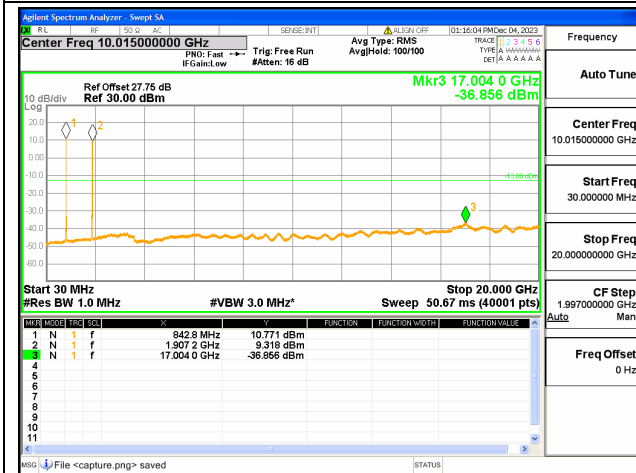
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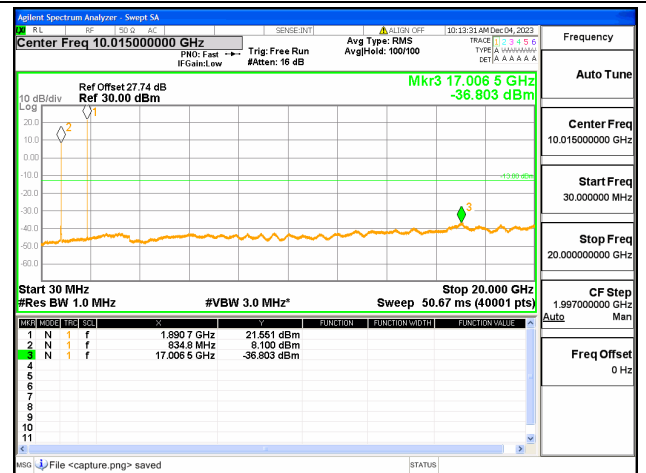
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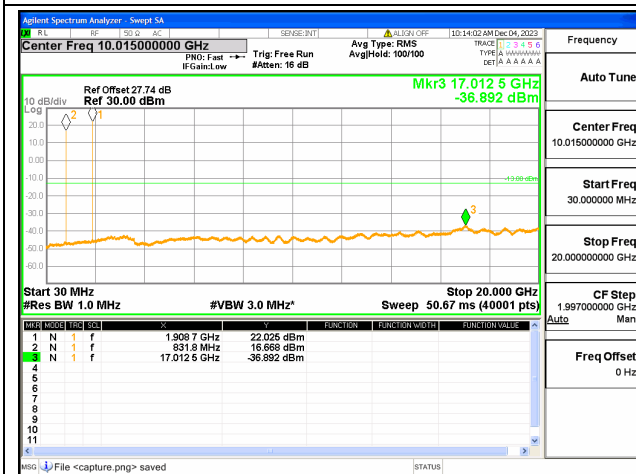
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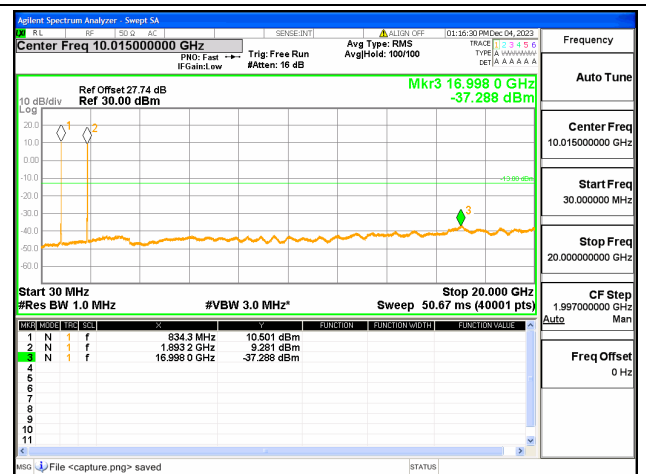
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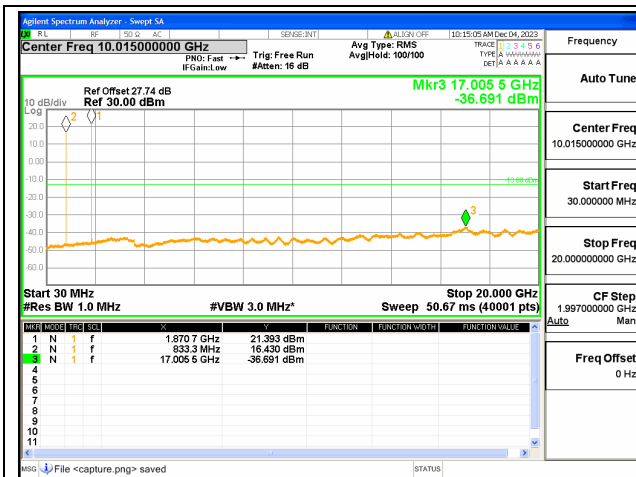
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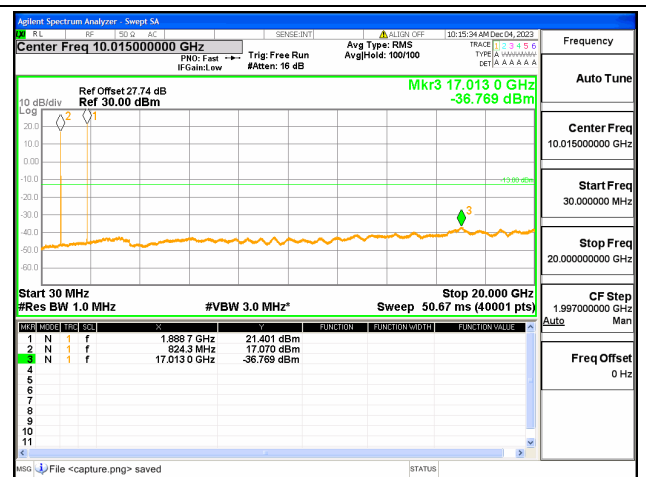
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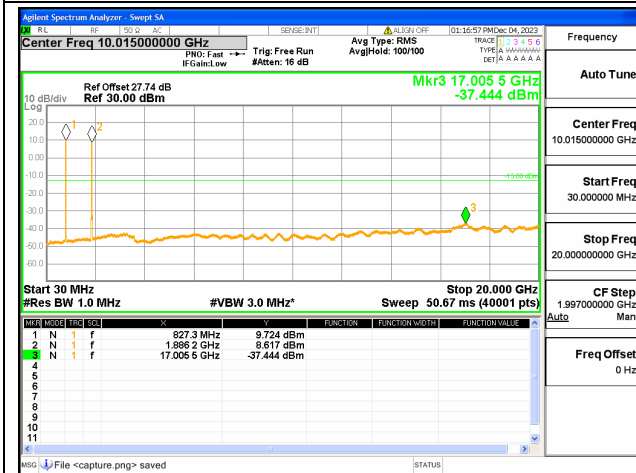
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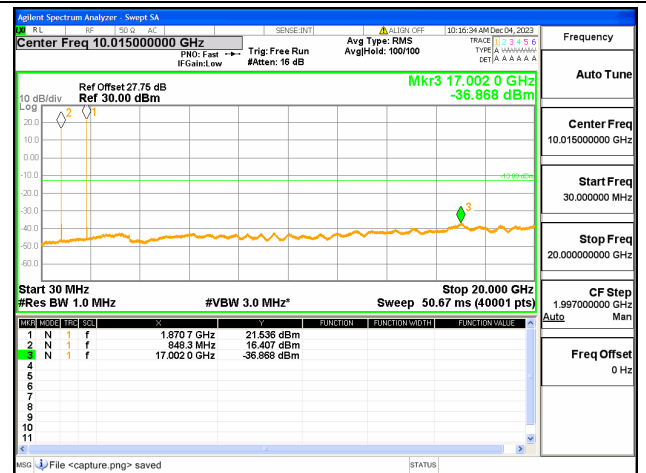
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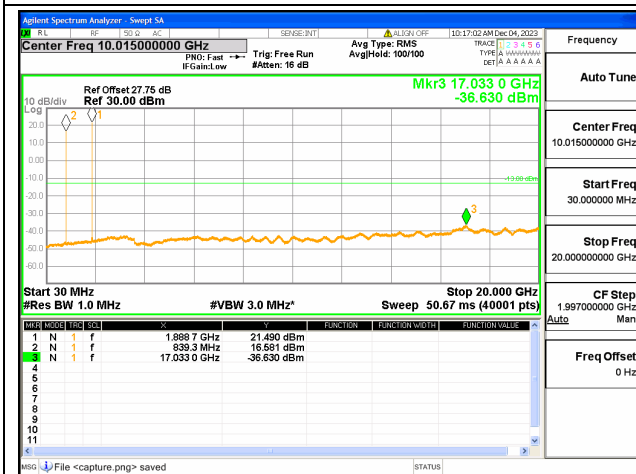
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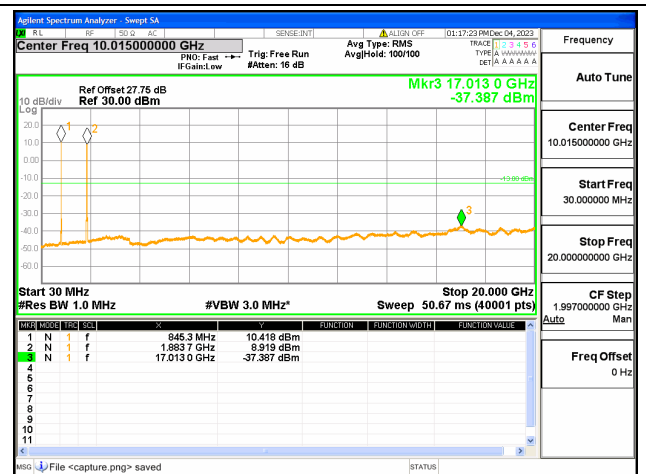
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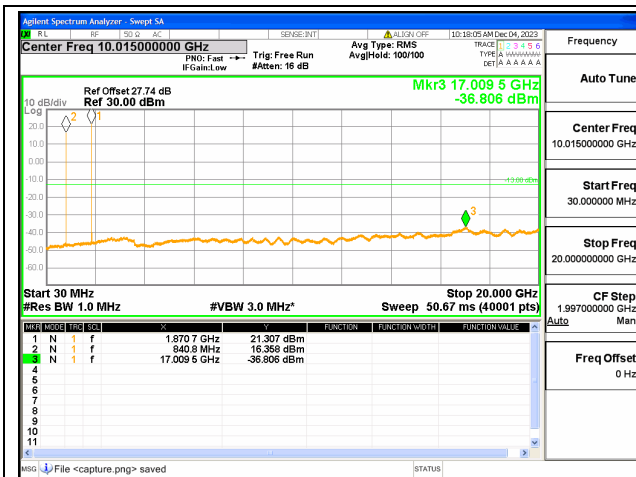
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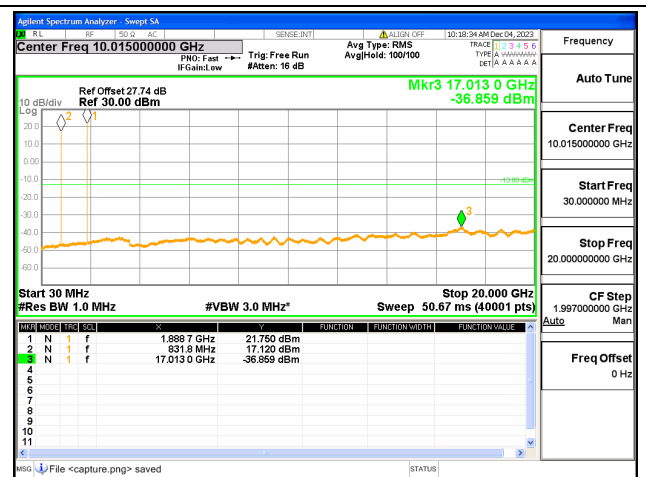
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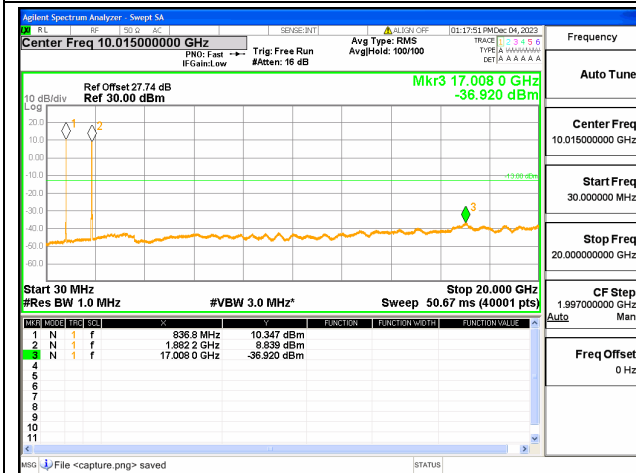
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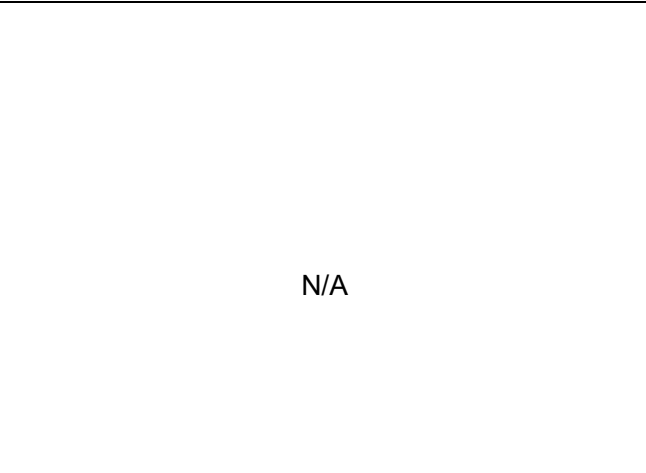
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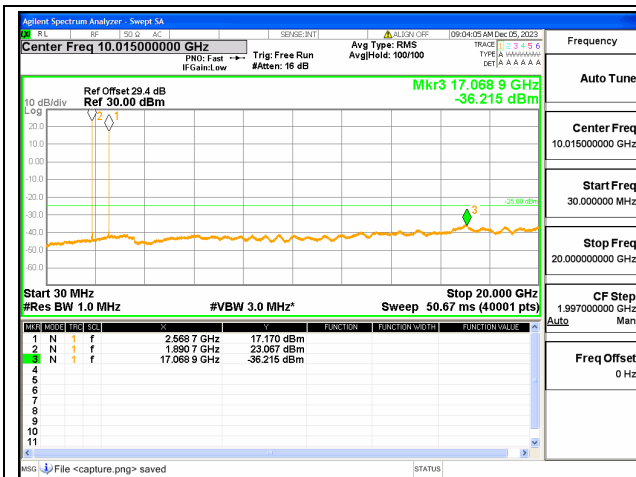


2A-5A / 20+10MHz / QPSK / Mid+Mid CH /  
1@99-1@0

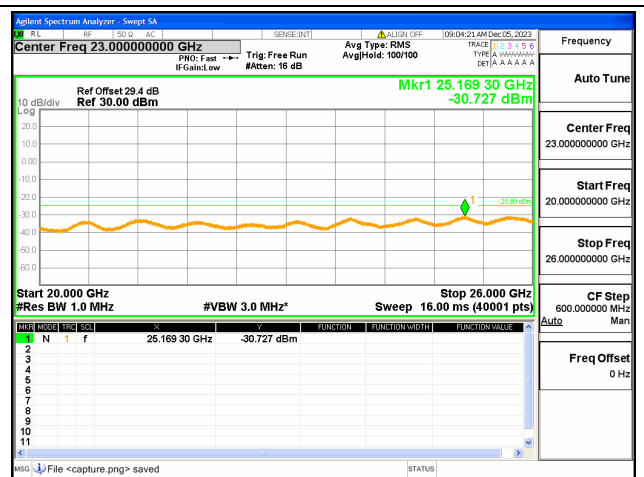


2A-5A / 20+10MHz / QPSK / Mid+Mid CH /  
100@0-50@0

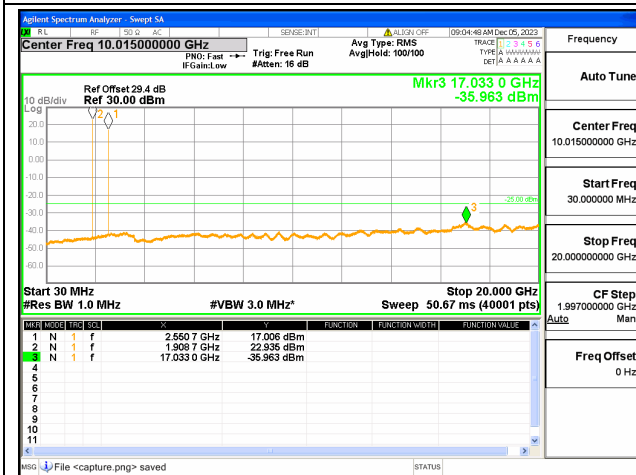




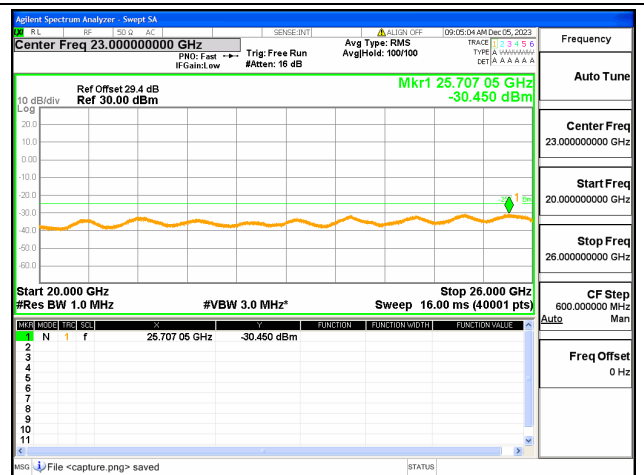
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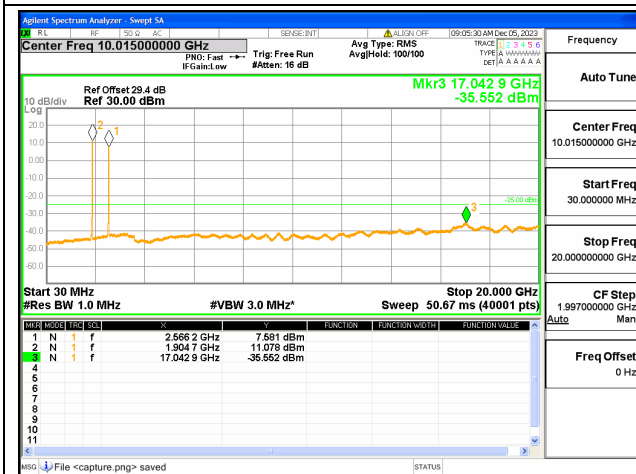
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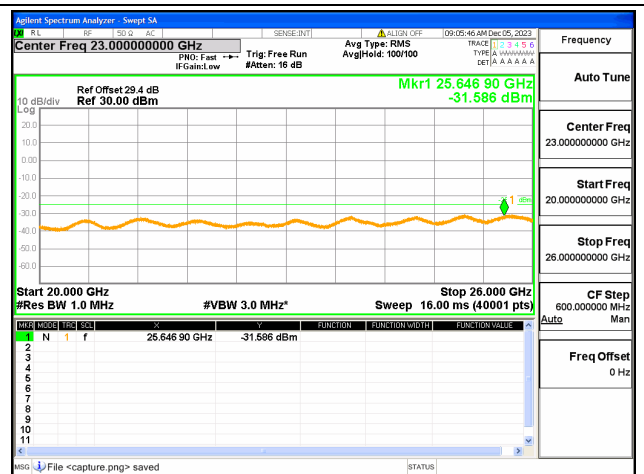
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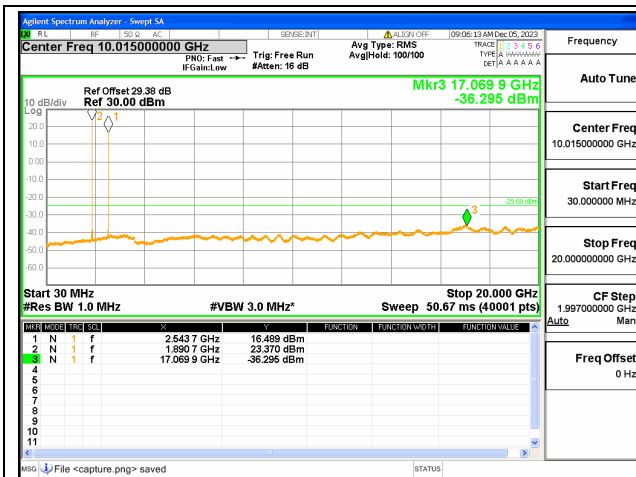


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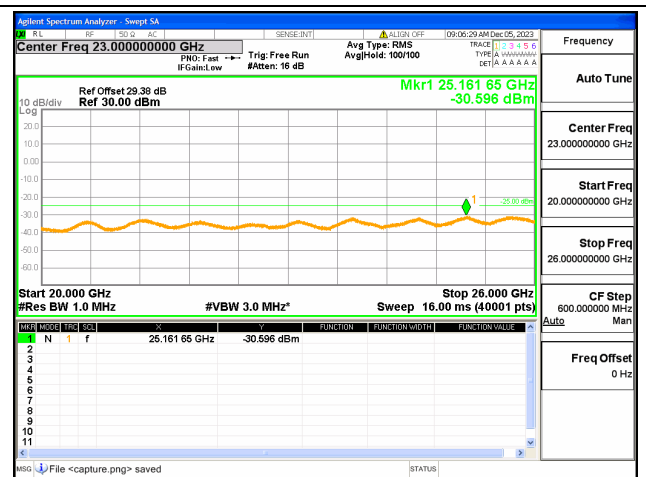


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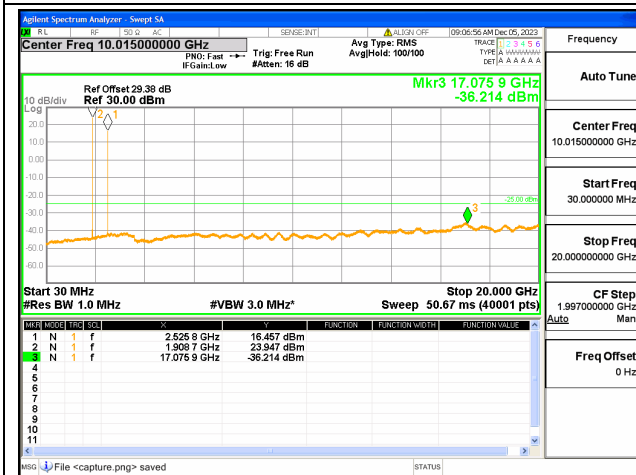




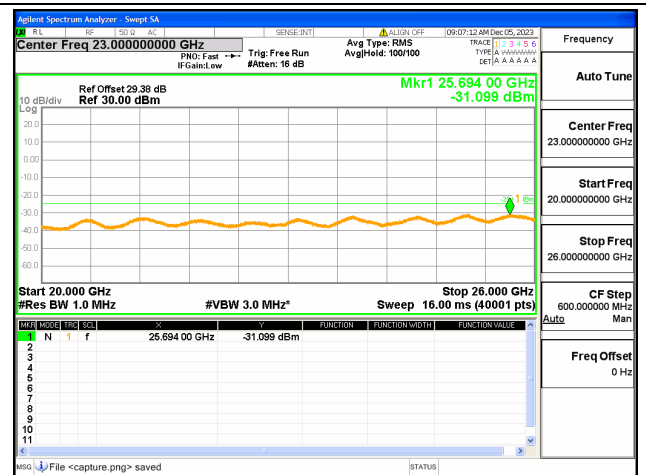
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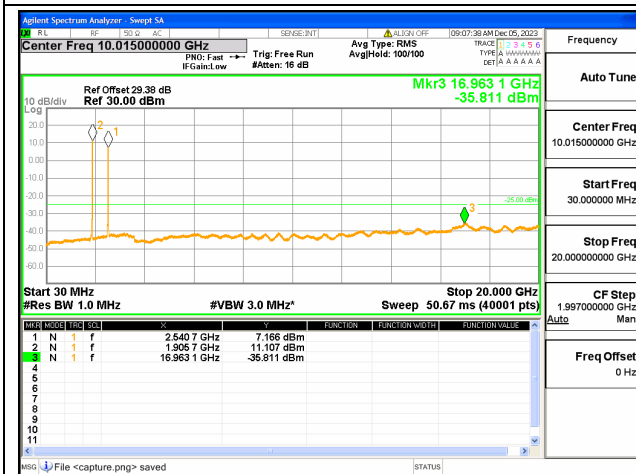
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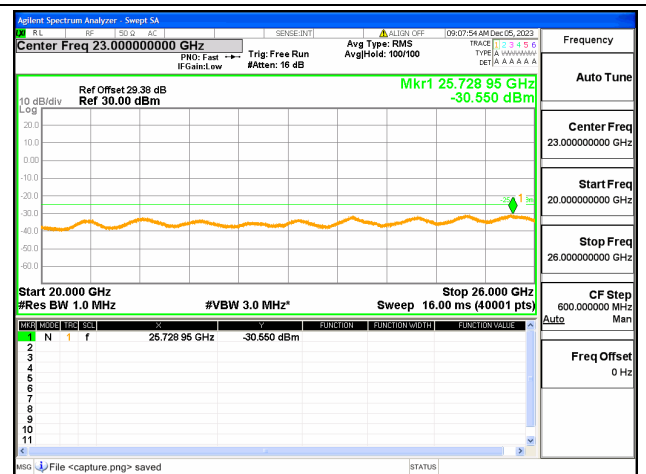
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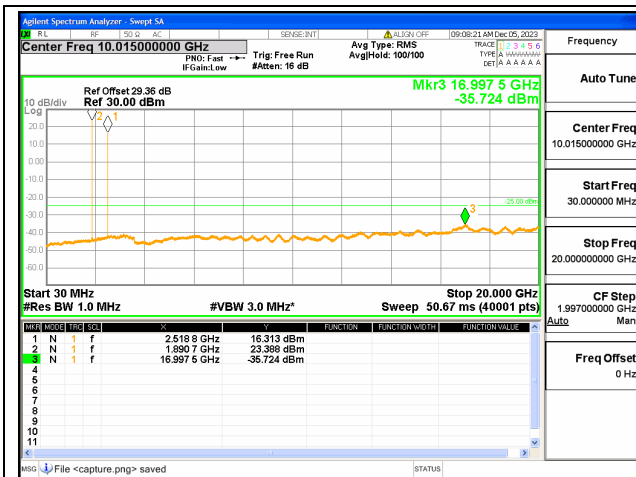
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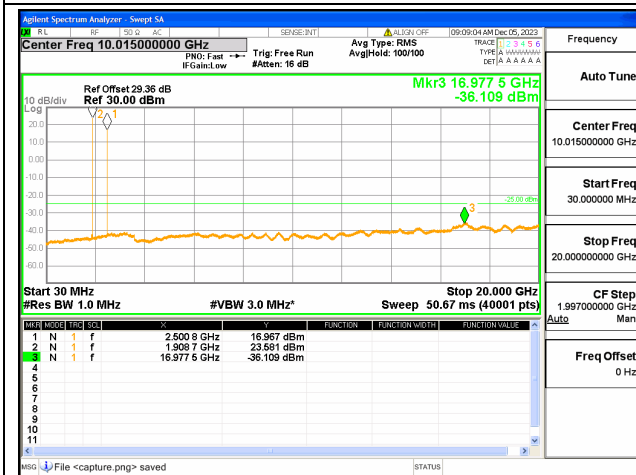
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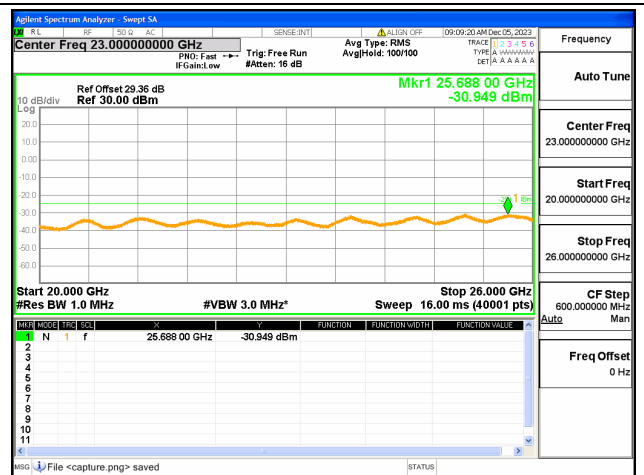
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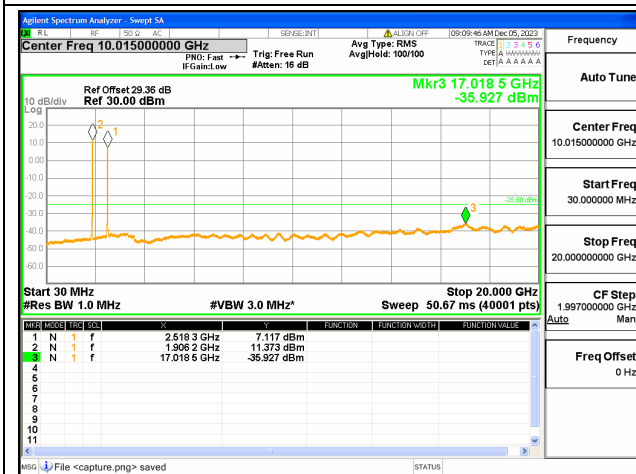
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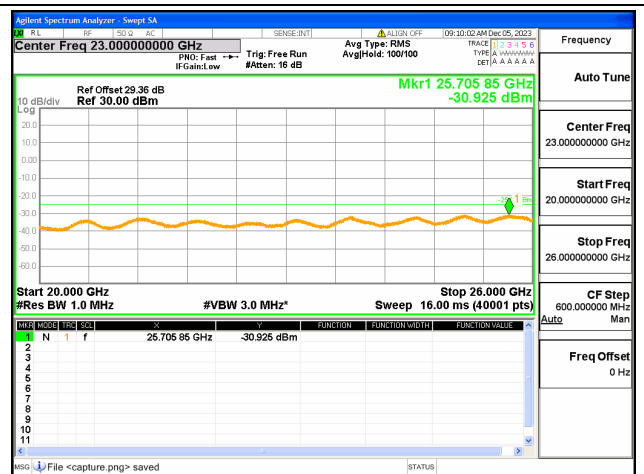
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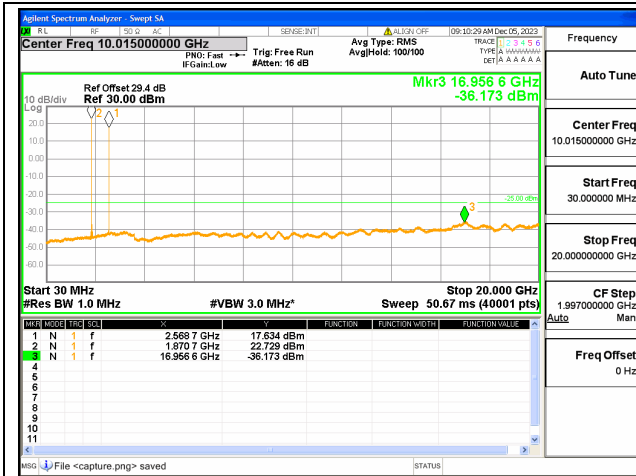
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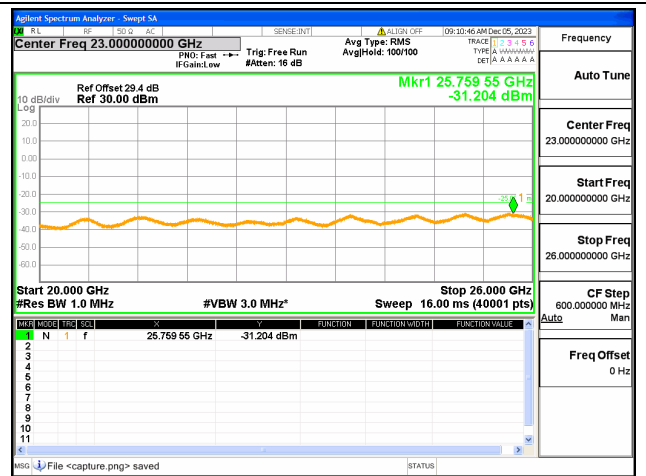
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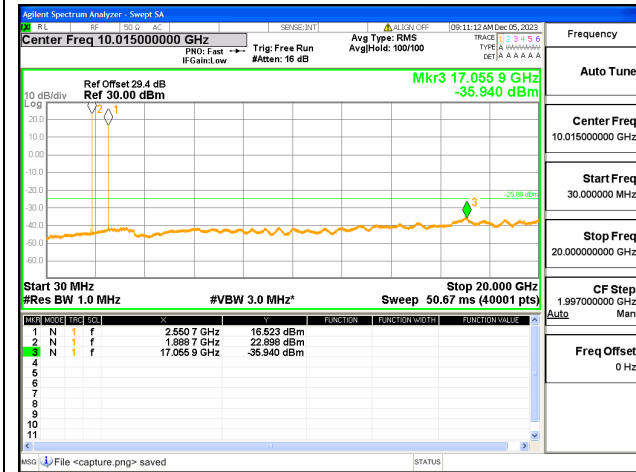
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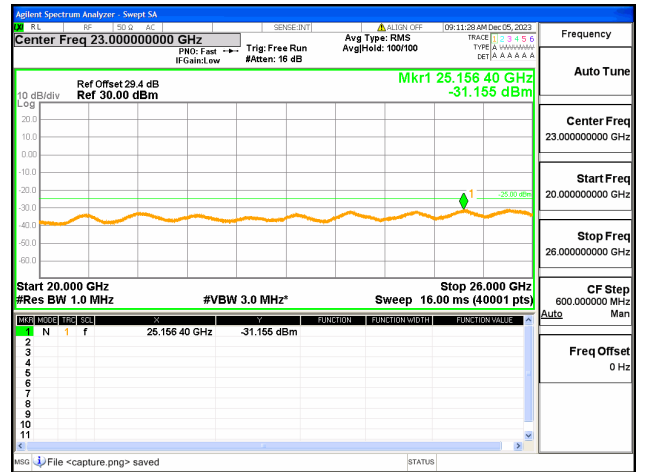
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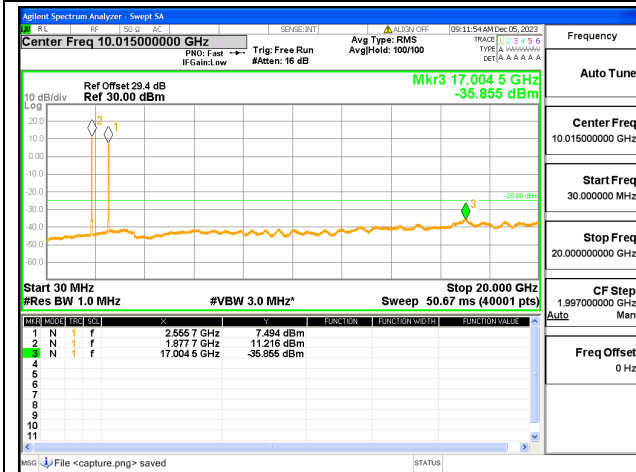
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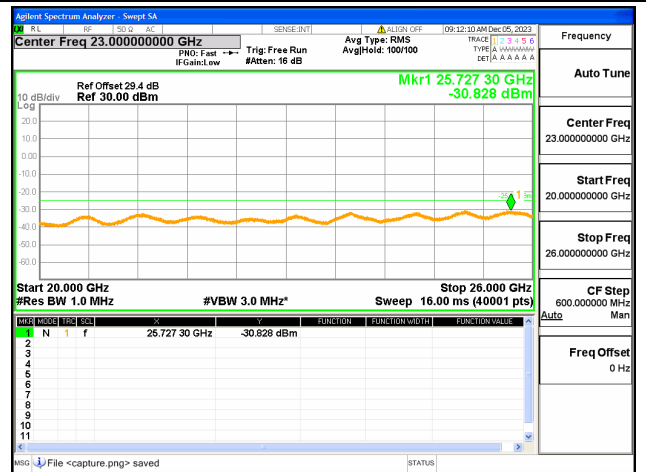
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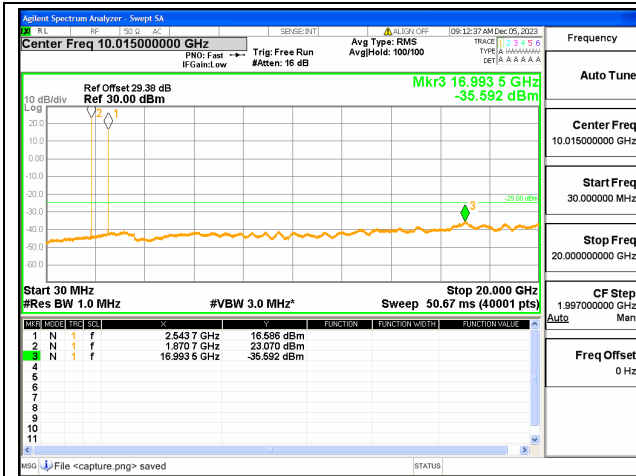
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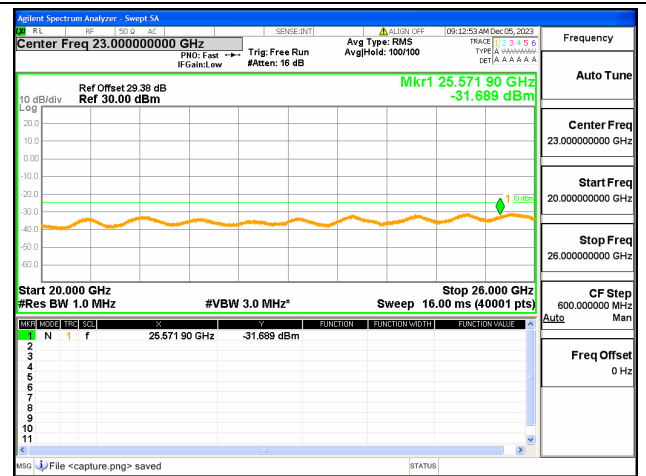
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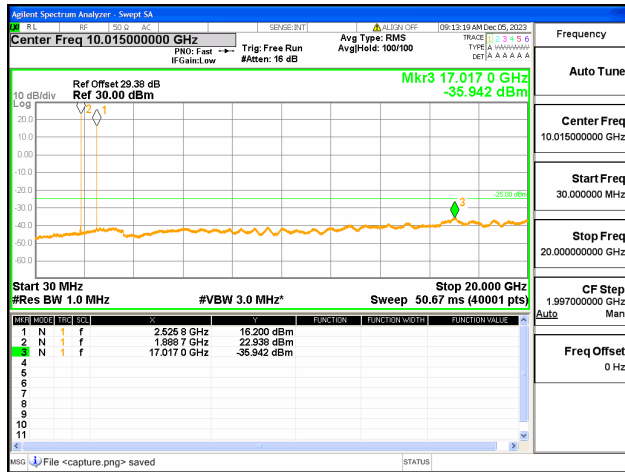
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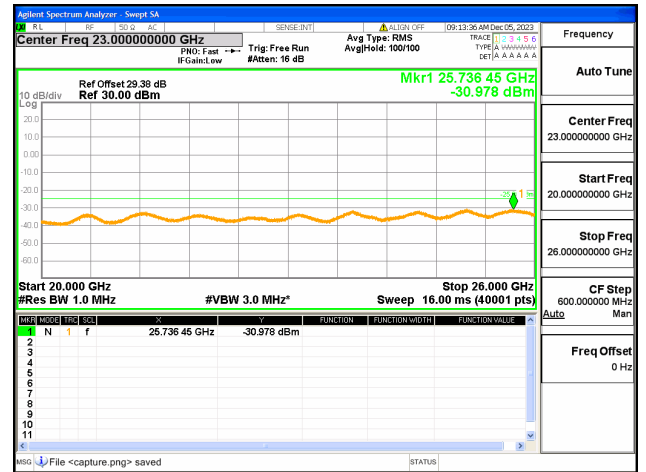
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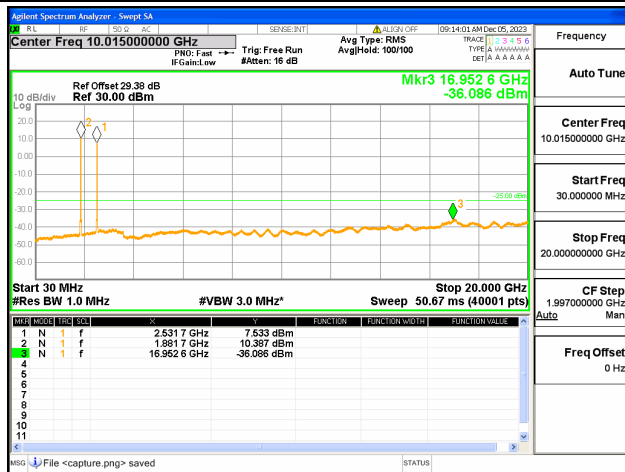
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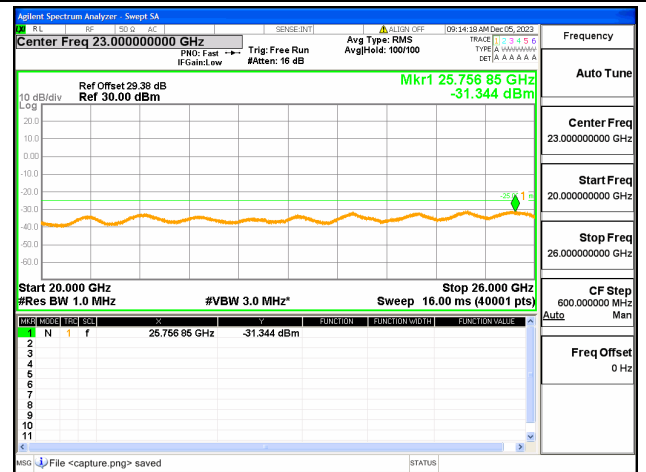
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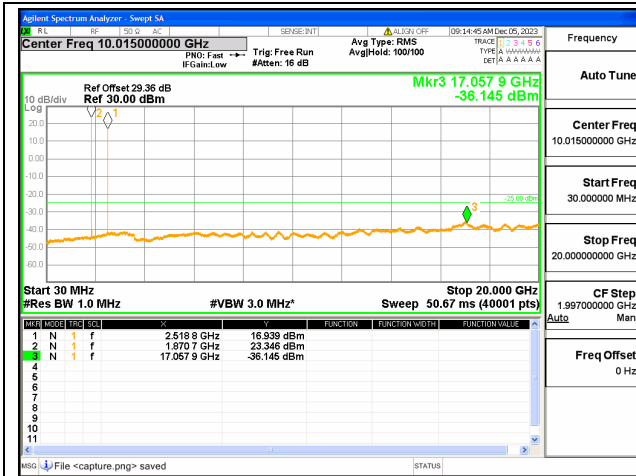
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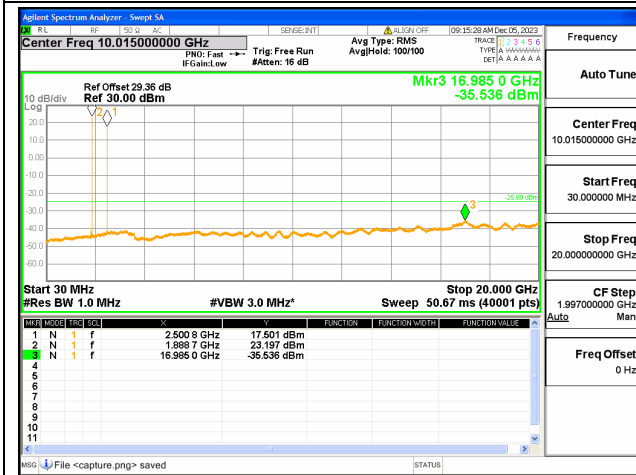
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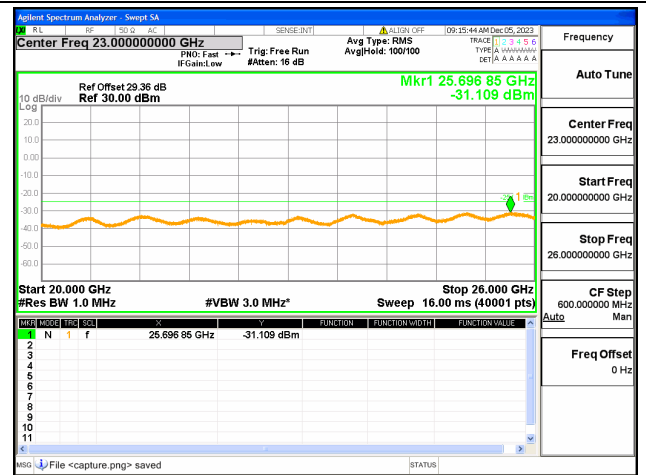
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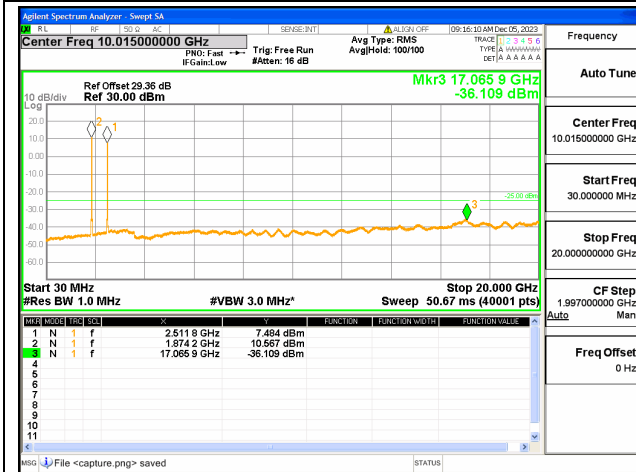
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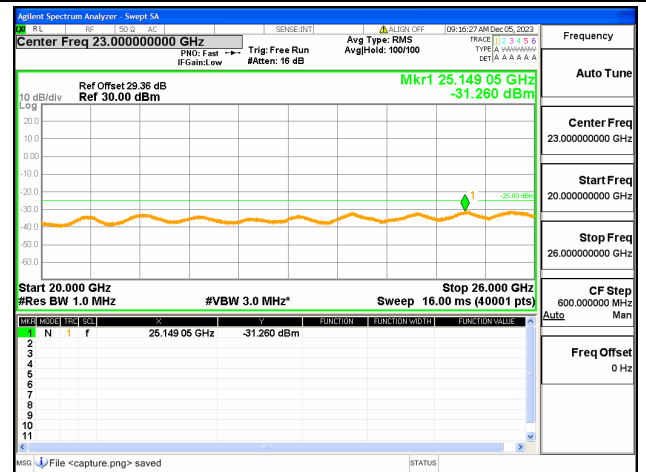
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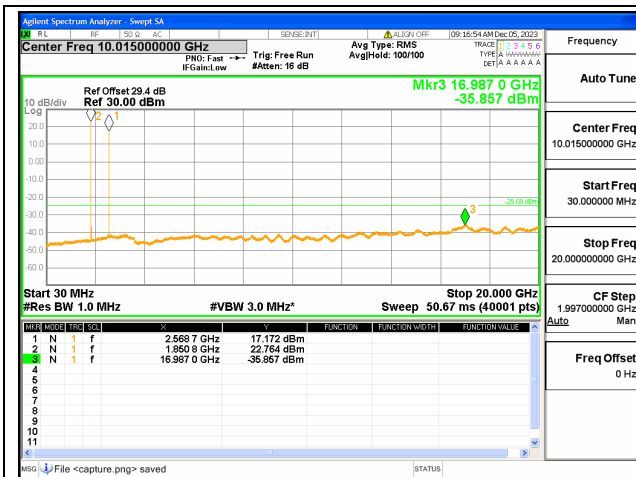


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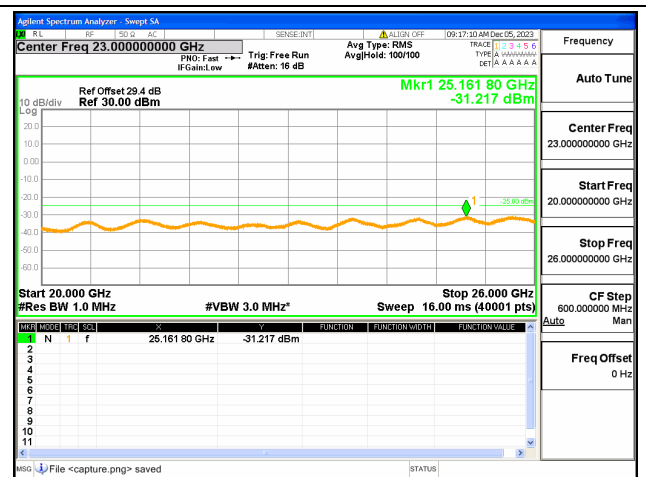


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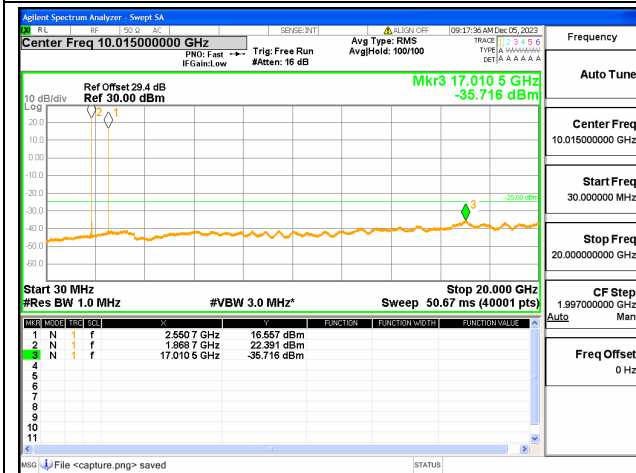




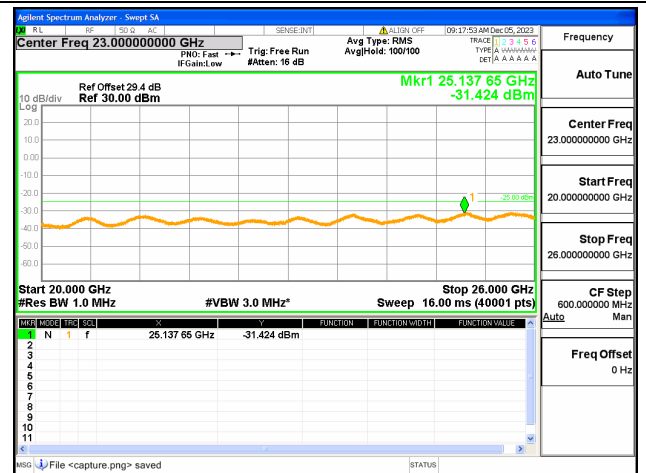
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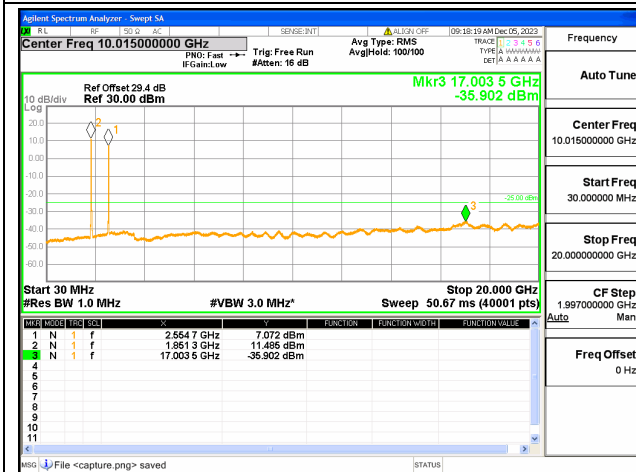
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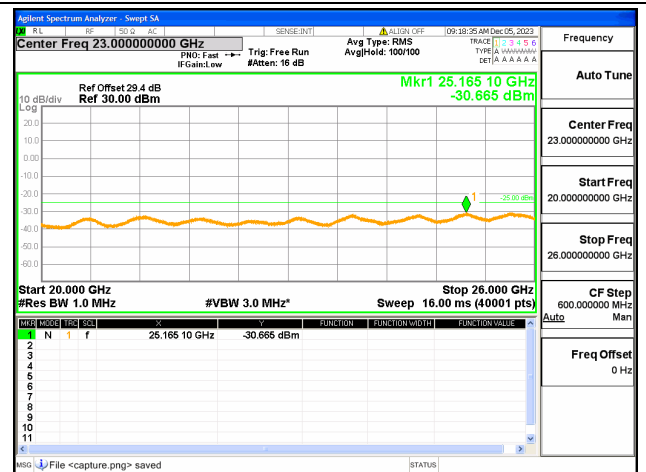
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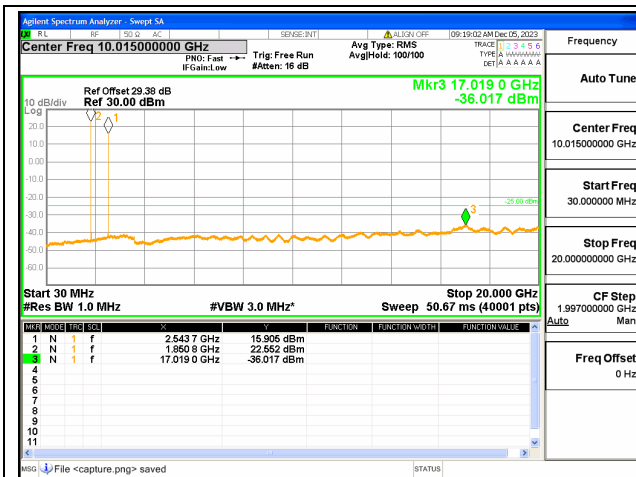


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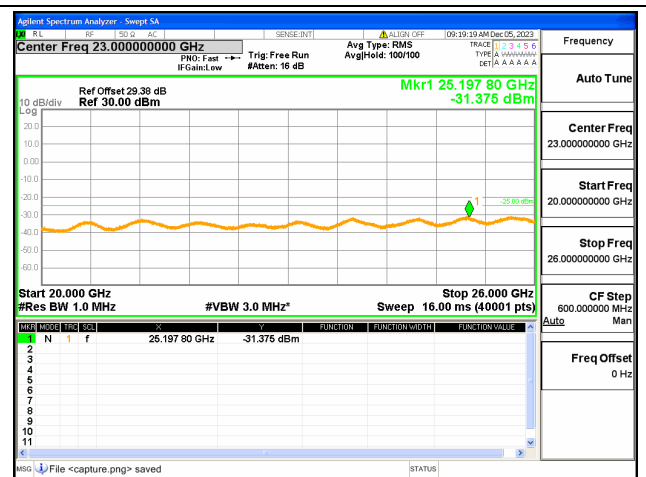


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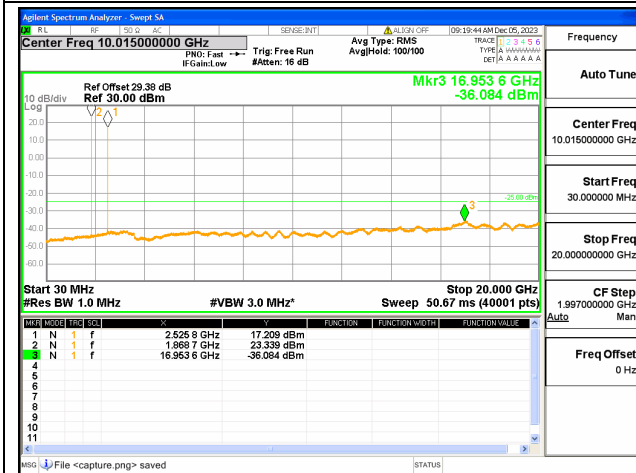




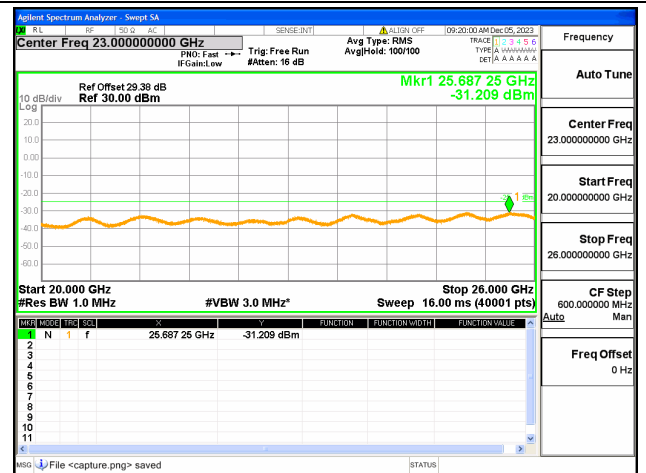
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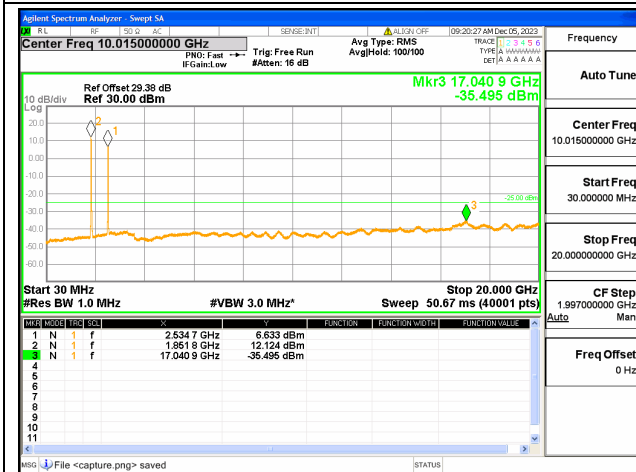
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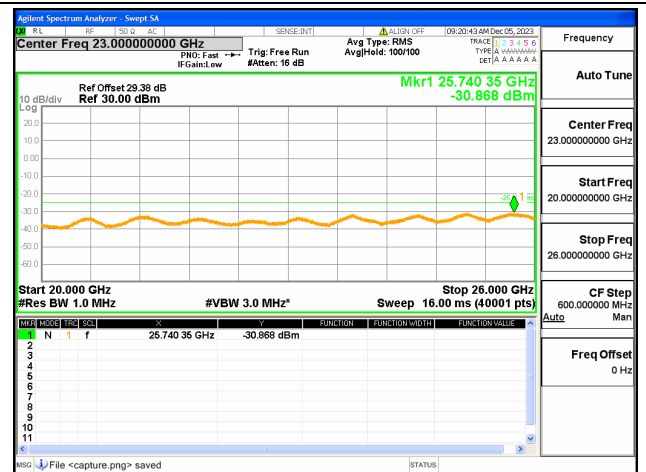
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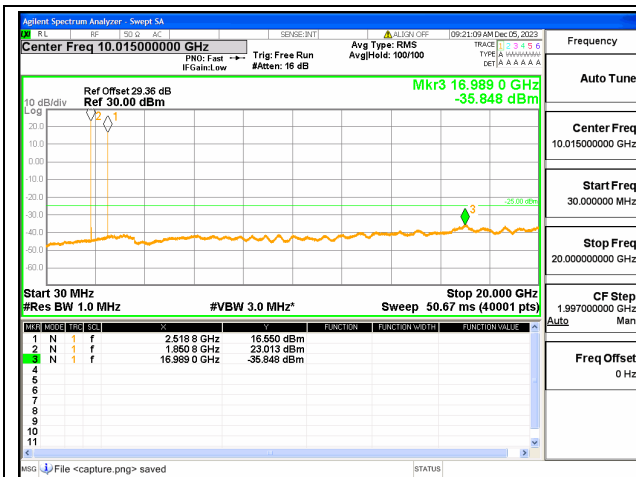
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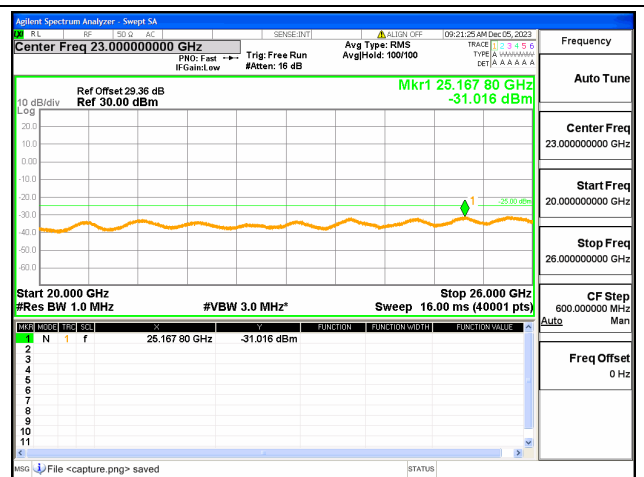
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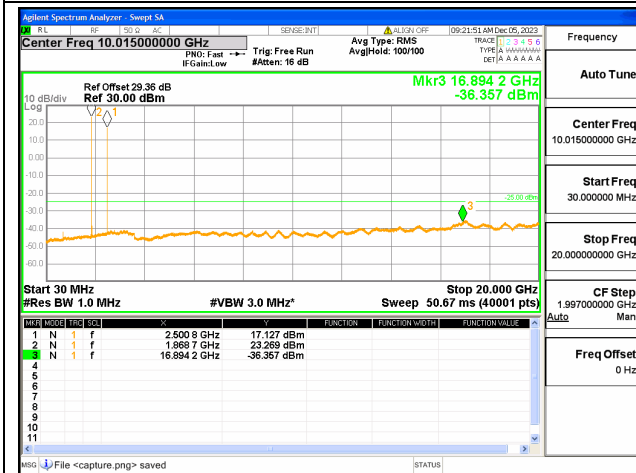
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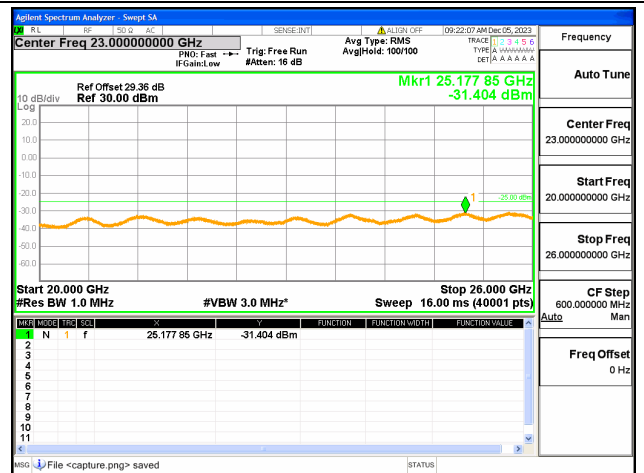
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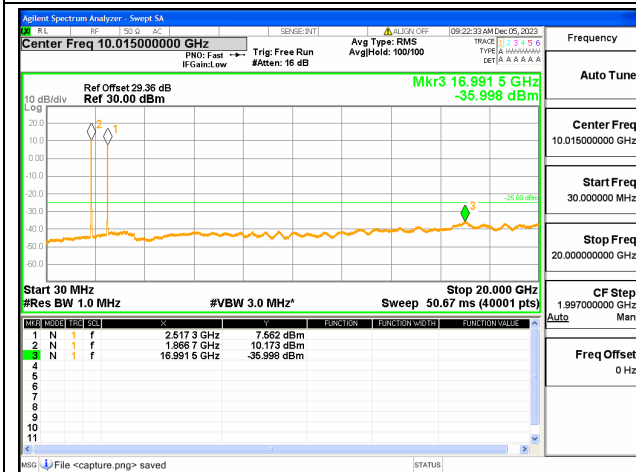
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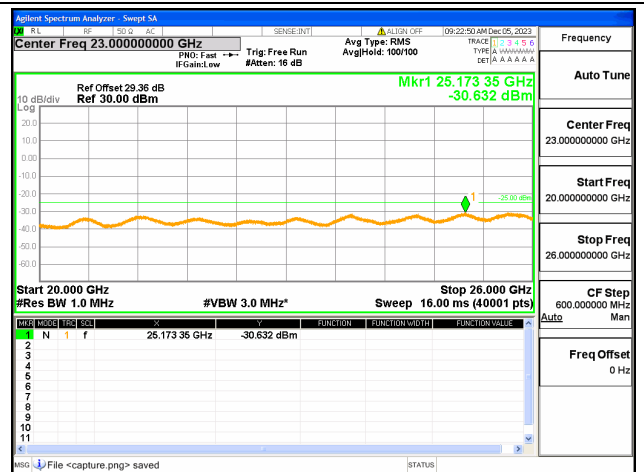
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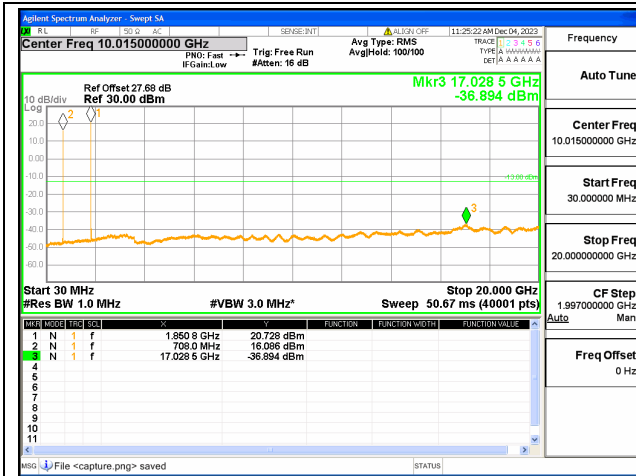
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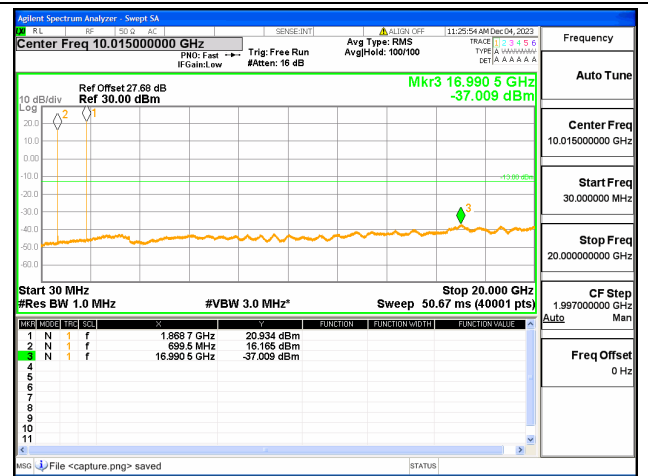
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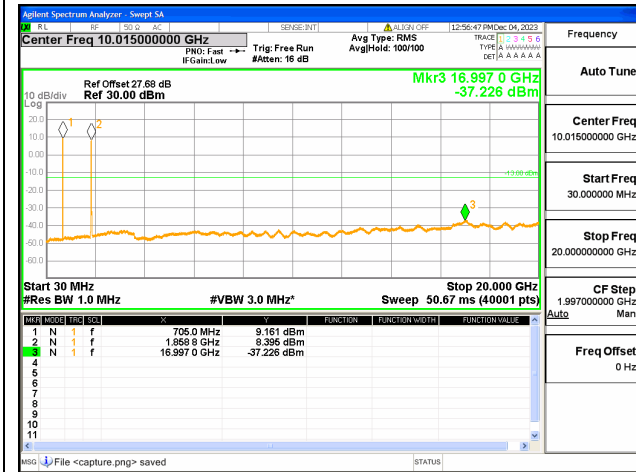
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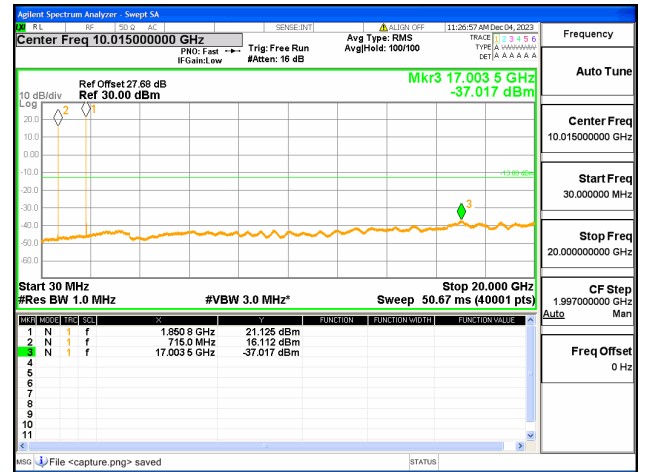
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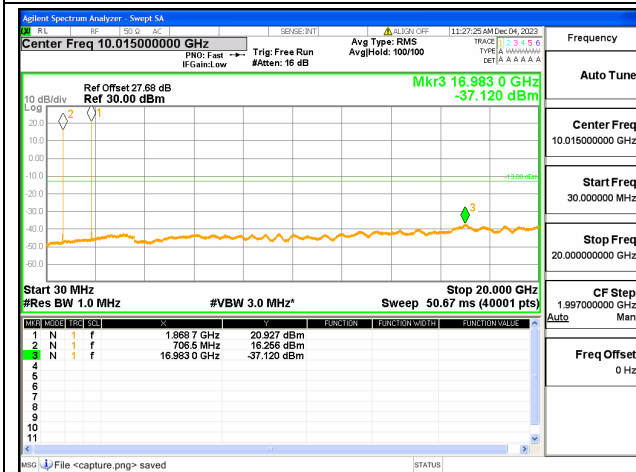
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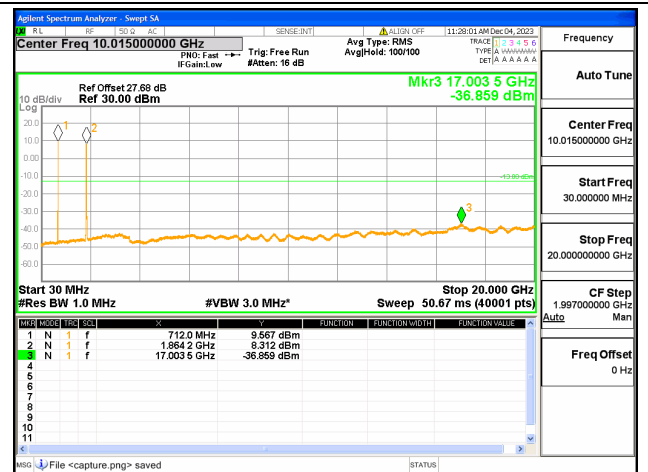
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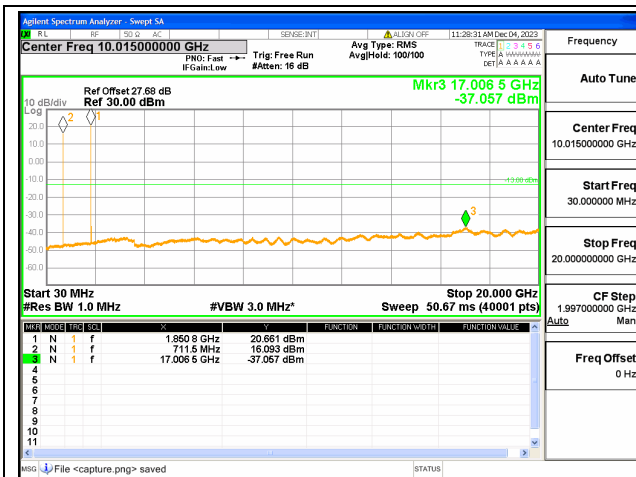
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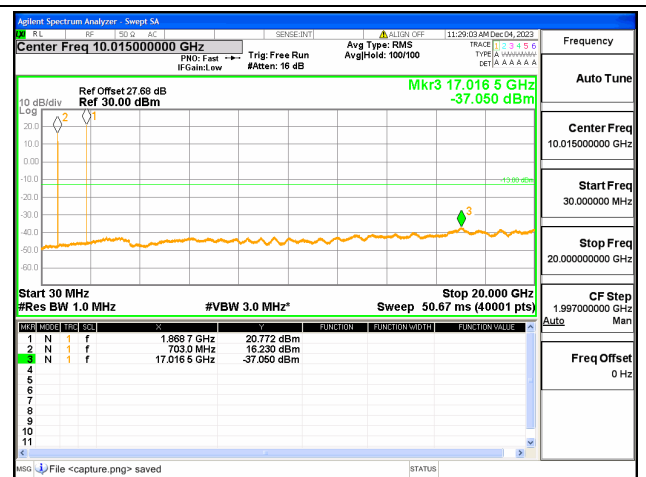
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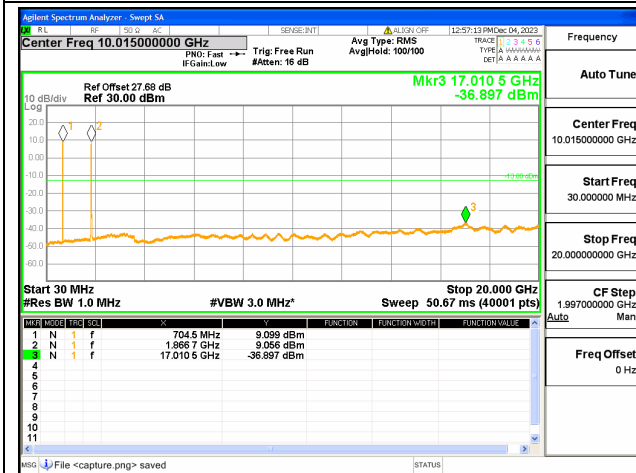
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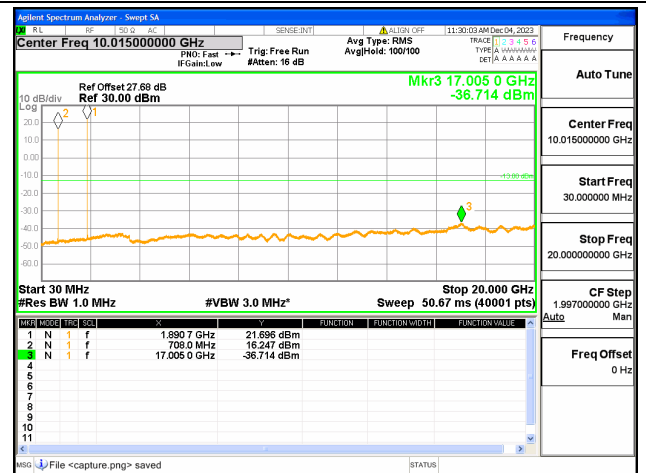
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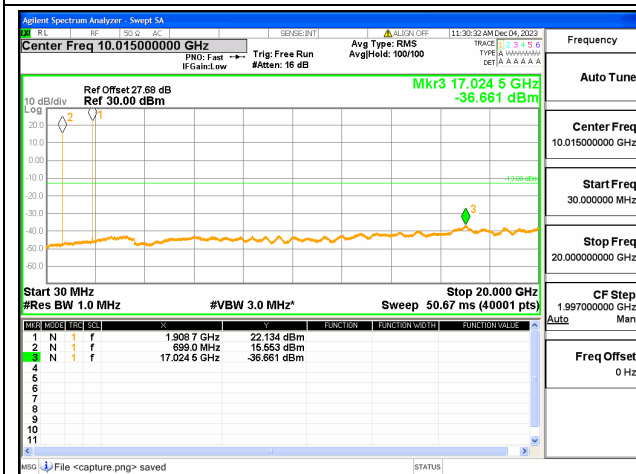
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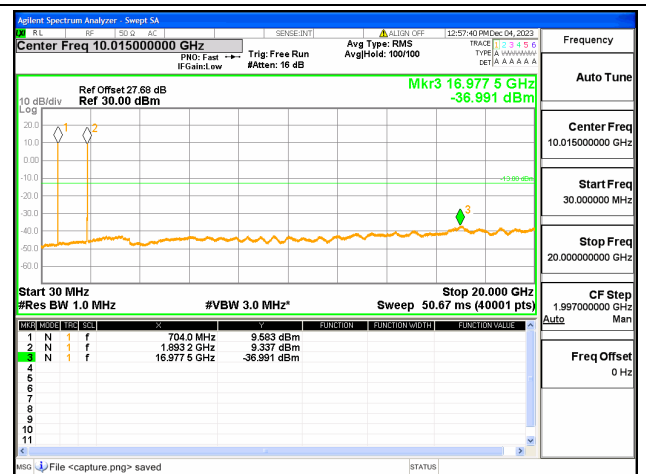
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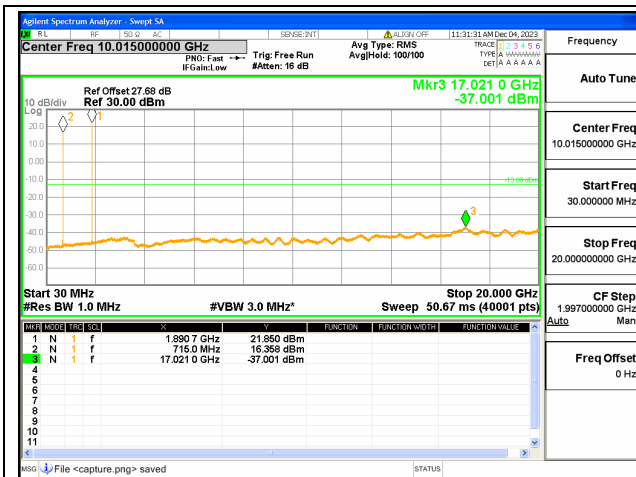
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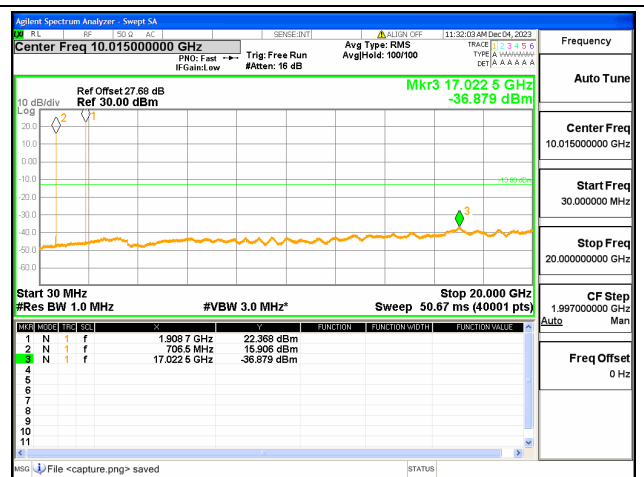
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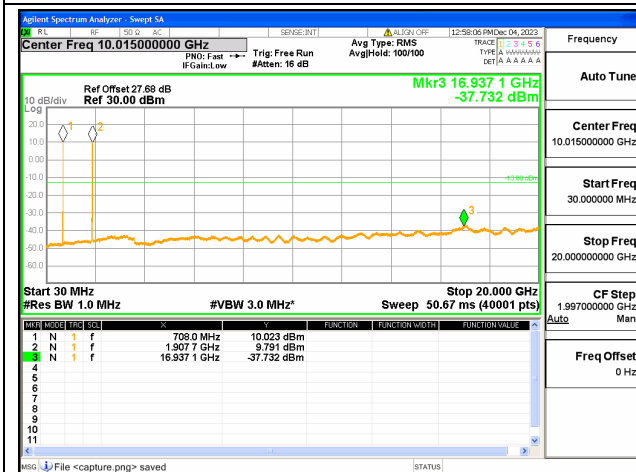
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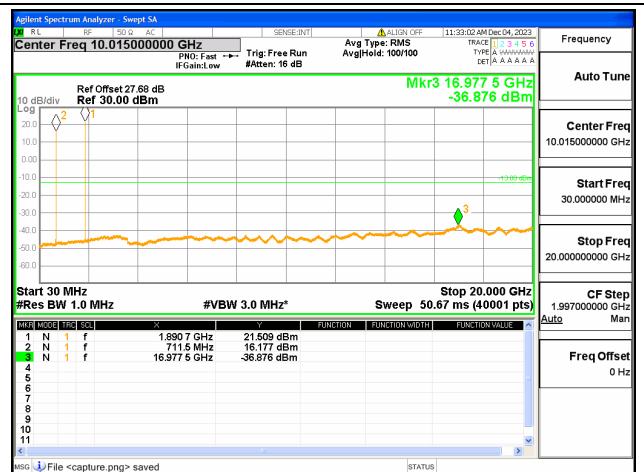
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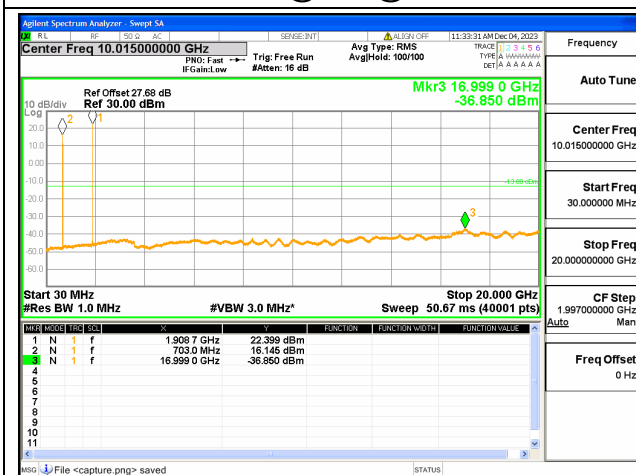
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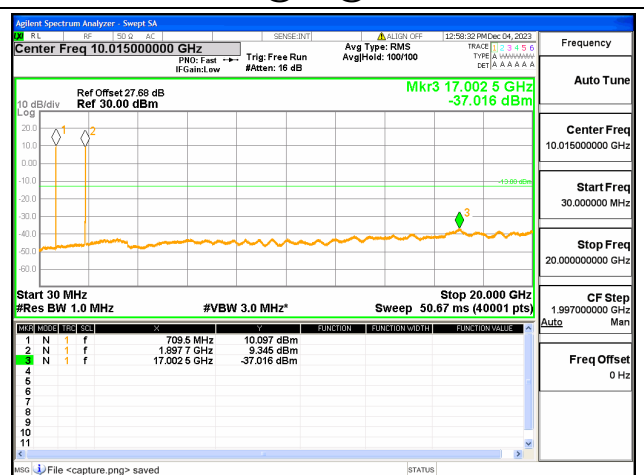
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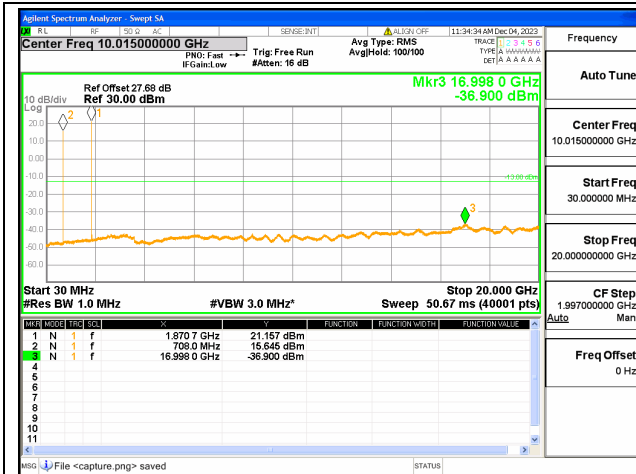


2A-12A / 20+10MHz / QPSK / High+Mid CH / 1@99-1@0

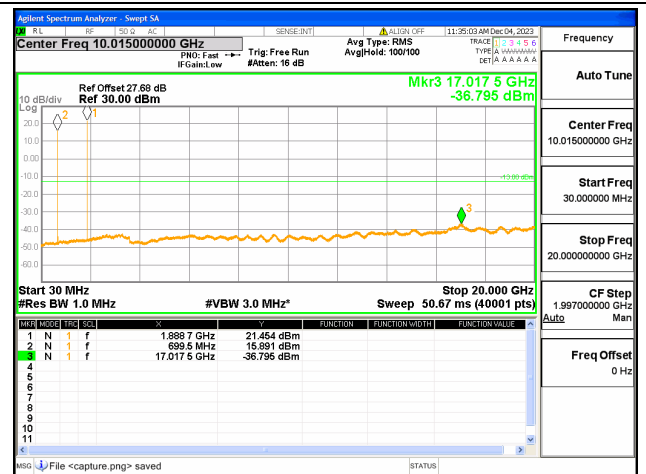


2A-12A / 20+10MHz / QPSK / High+Mid CH / 100@0-50@0

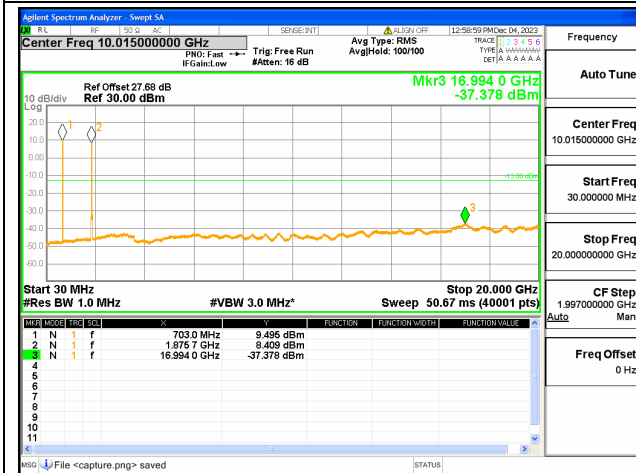




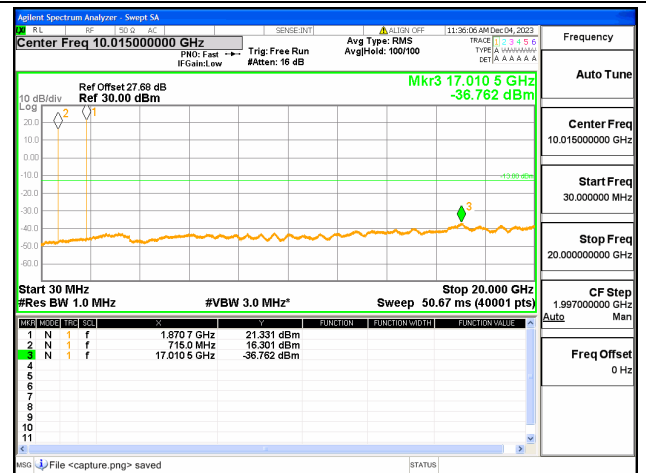
2A-12A / 20+10MHz / QPSK / Mid+Low CH / 1@0-1@49



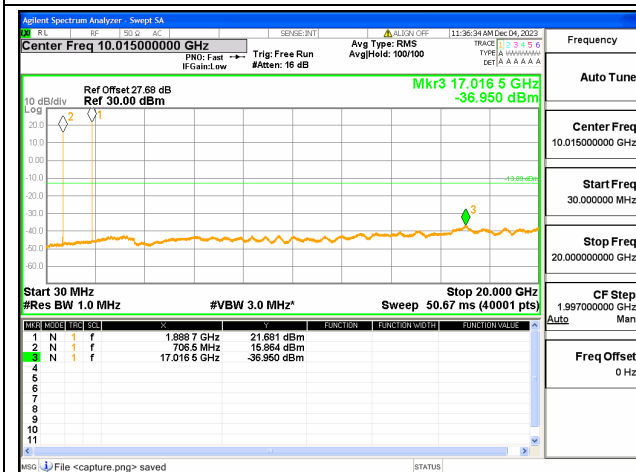
2A-12A / 20+10MHz / QPSK / Mid+Low CH / 1@99-1@0



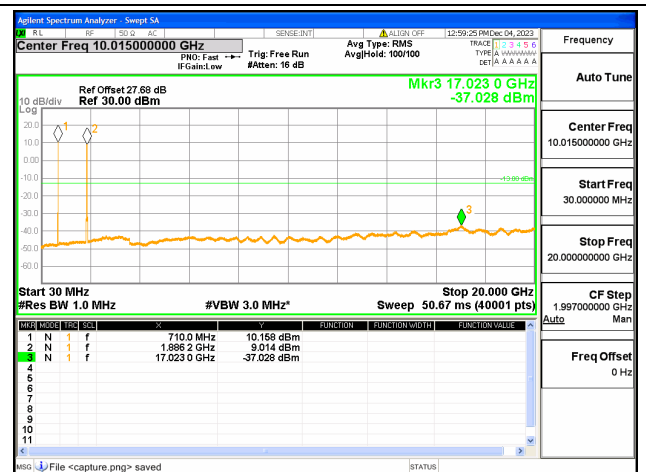
2A-12A / 20+10MHz / QPSK / Mid+Low CH / 100@0-50@0



2A-12A / 20+10MHz / QPSK / Mid+High CH / 1@0-1@49

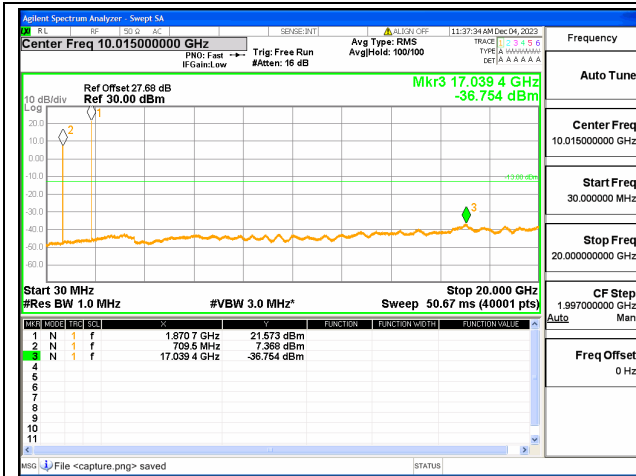


2A-12A / 20+10MHz / QPSK / Mid+High CH / 1@99-1@0

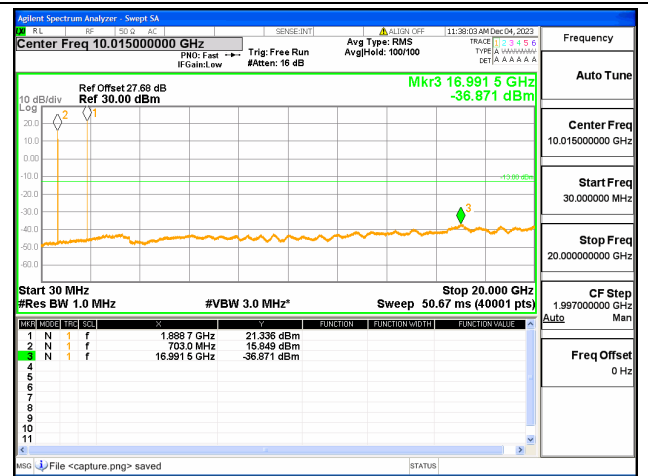


2A-12A / 20+10MHz / QPSK / Mid+High CH / 100@0-50@0

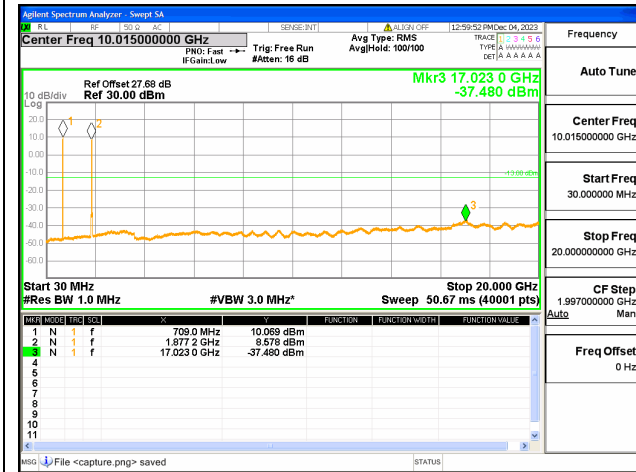




2A-12A / 20+10MHz / QPSK / Mid+Mid CH /  
1@0-1@49

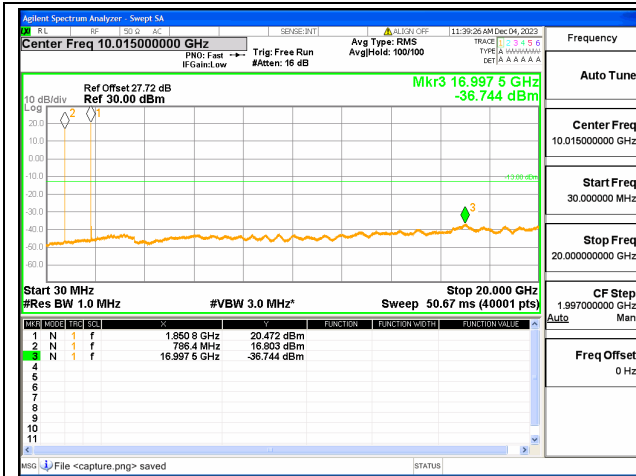


2A-12A / 20+10MHz / QPSK / Mid+Mid CH /  
1@99-1@0

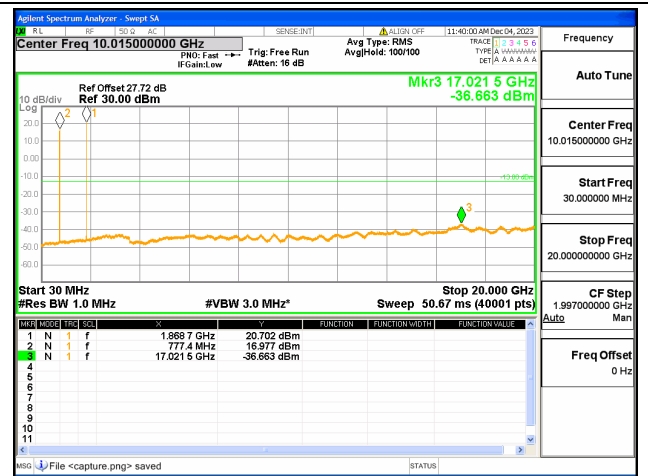


2A-12A / 20+10MHz / QPSK / Mid+Mid CH /  
100@0-50@0

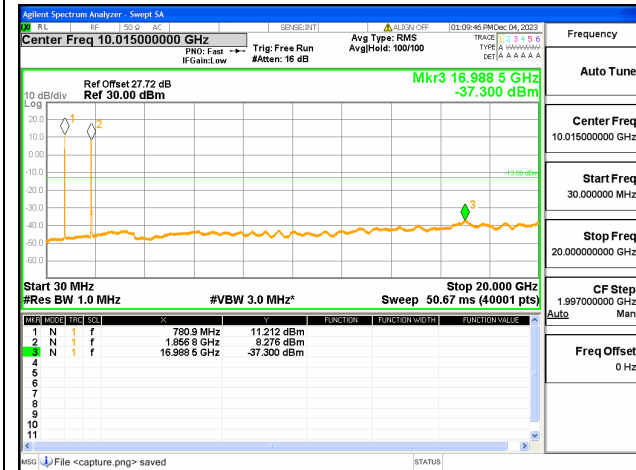
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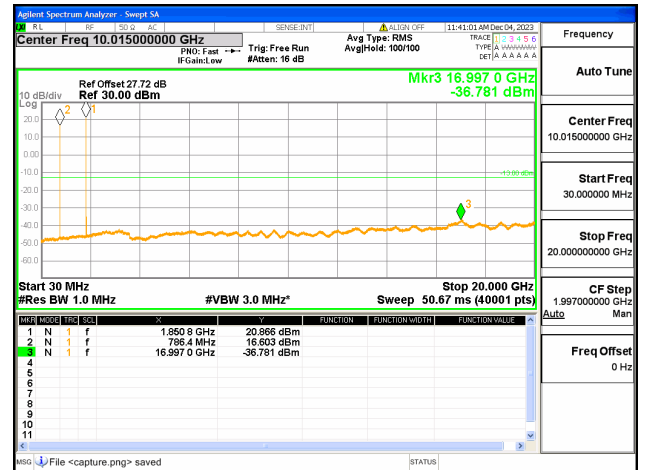
2A-13A / 20+10MHz / QPSK / Low+Low CH / 1@0-1@49



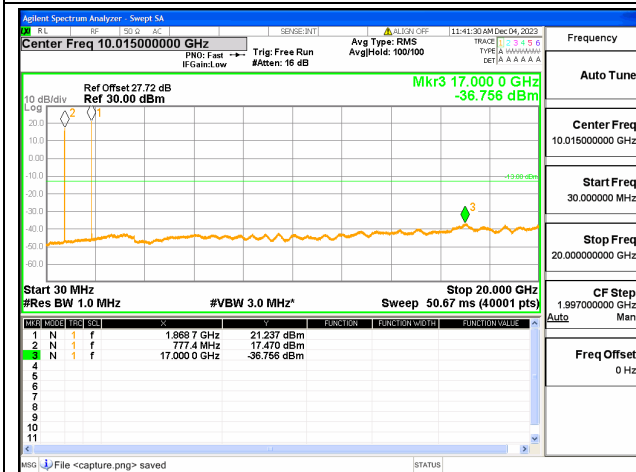
2A-13A / 20+10MHz / QPSK / Low+Low CH / 1@99-1@0



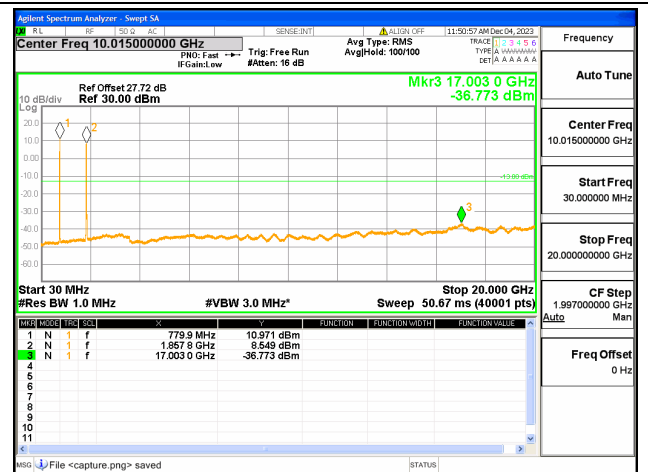
2A-13A / 20+10MHz / QPSK / Low+Low CH / 100@0-50@0



2A-13A / 20+10MHz / QPSK / Low+High CH / 1@0-1@49



2A-13A / 20+10MHz / QPSK / Low+High CH / 1@99-1@0



2A-13A / 20+10MHz / QPSK / Low+High CH / 100@0-50@0