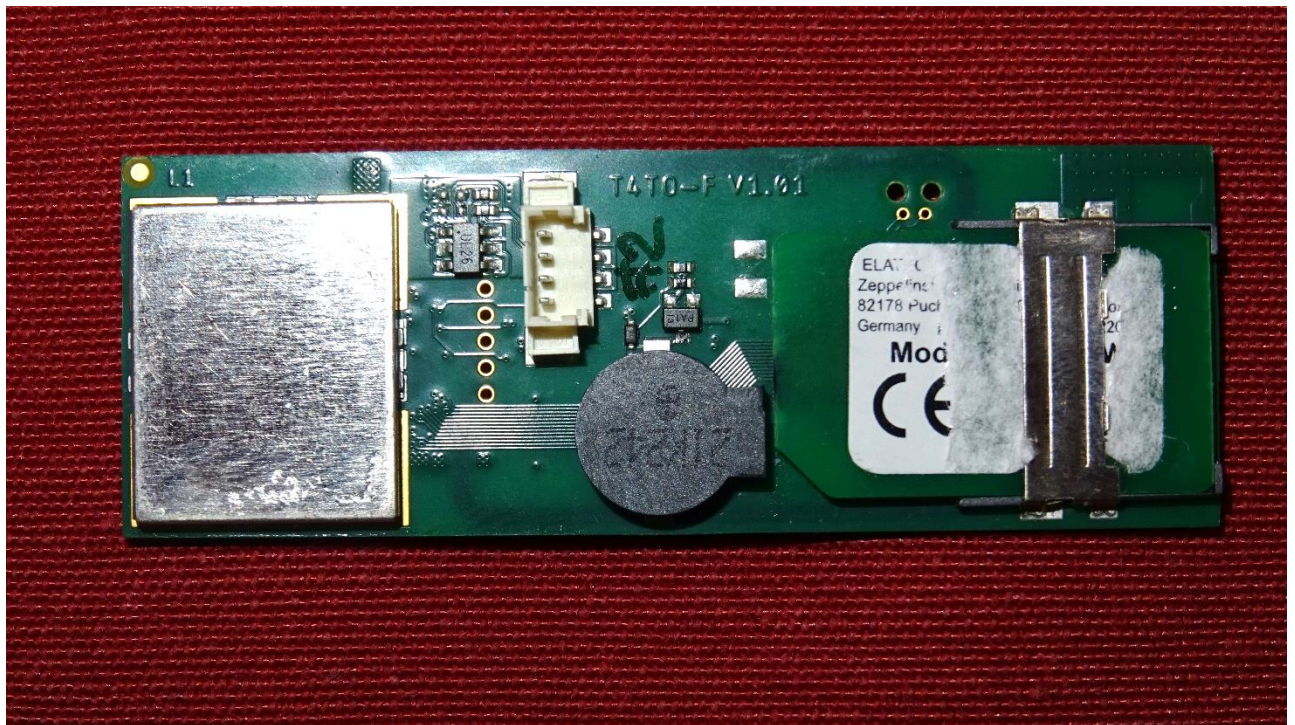


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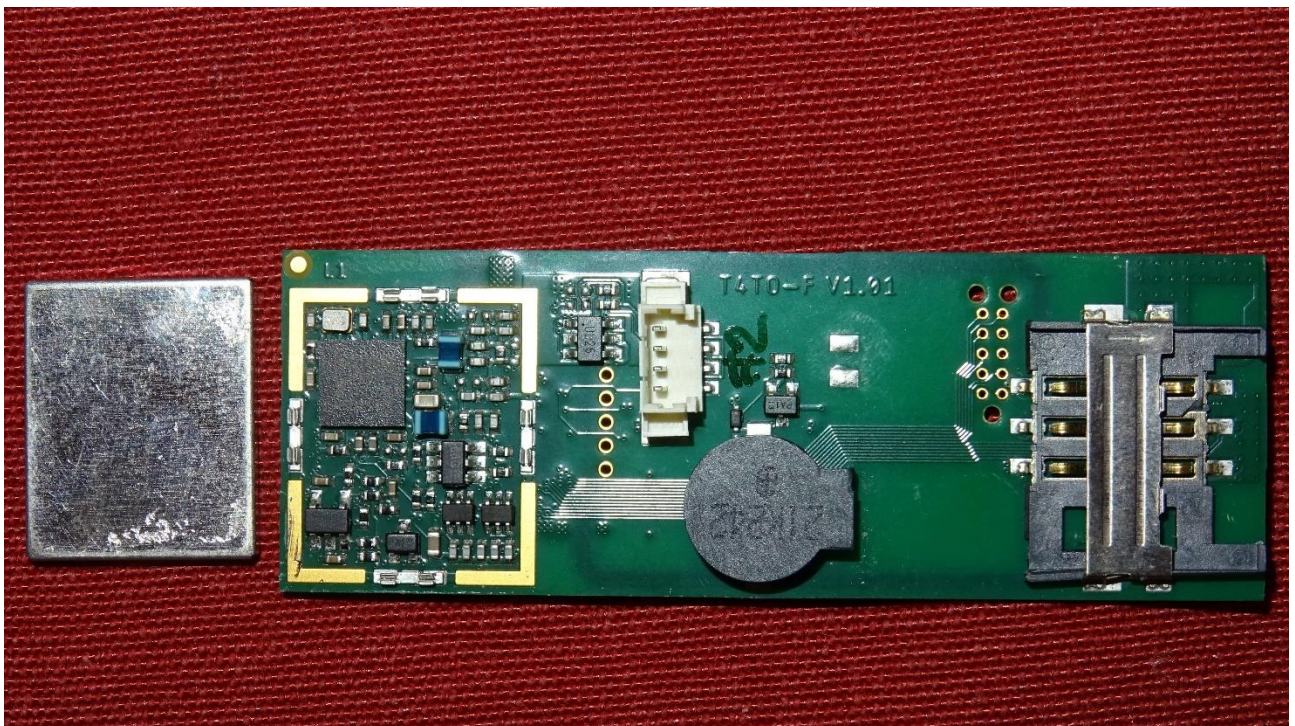
## 1 Annex C – EUT pictures internal

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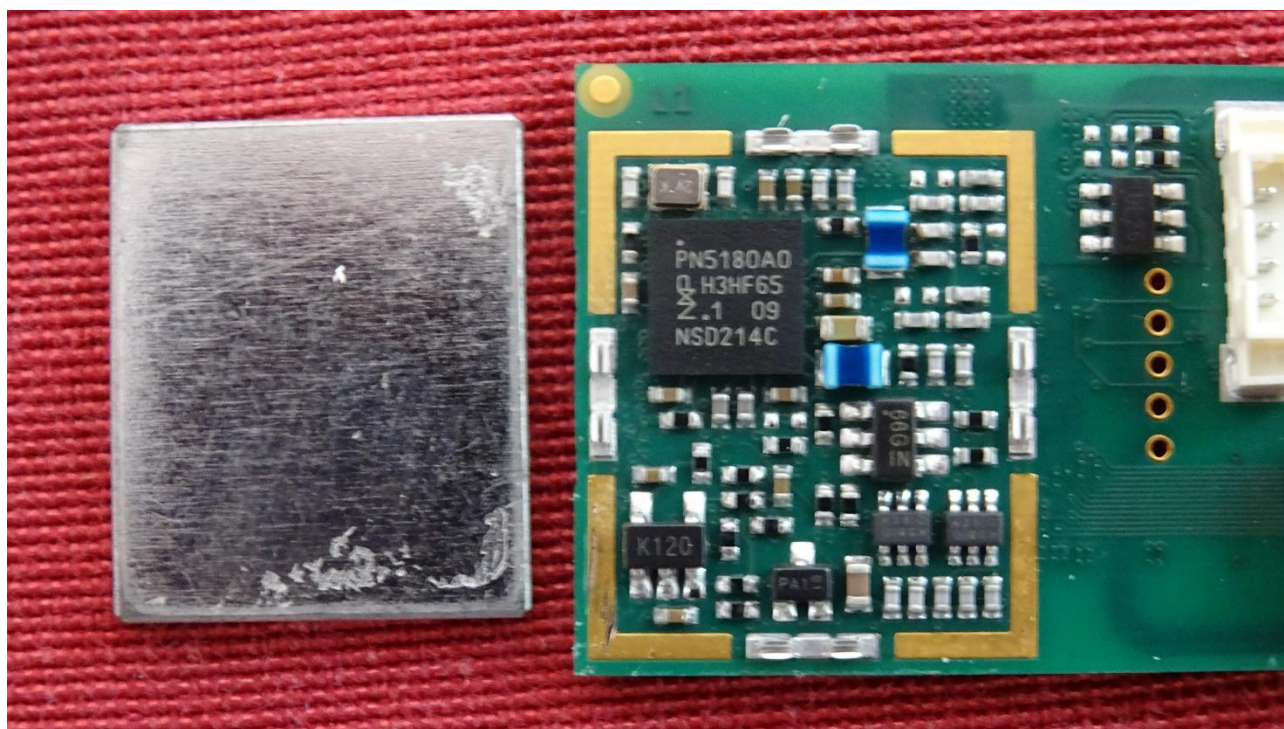


Picture 1: Front view of EUT with shielding and SIO card

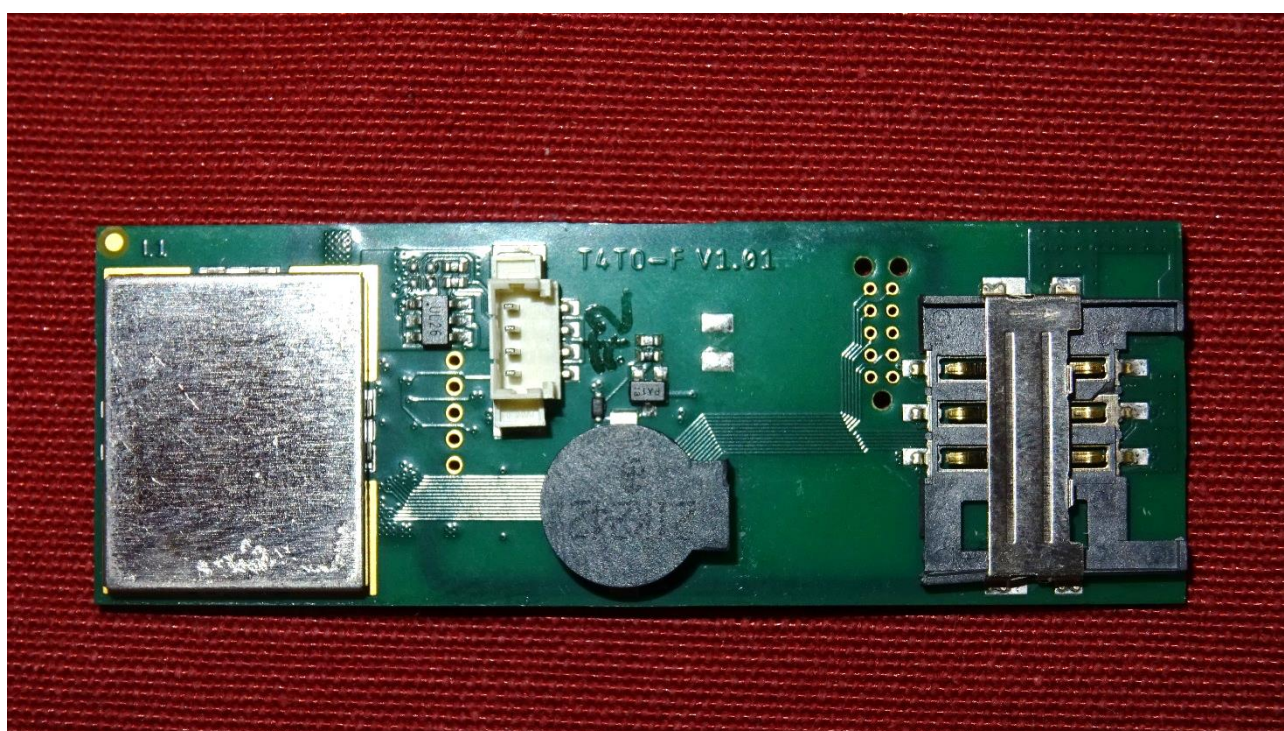


Picture 2: Front view of EUT without shielding



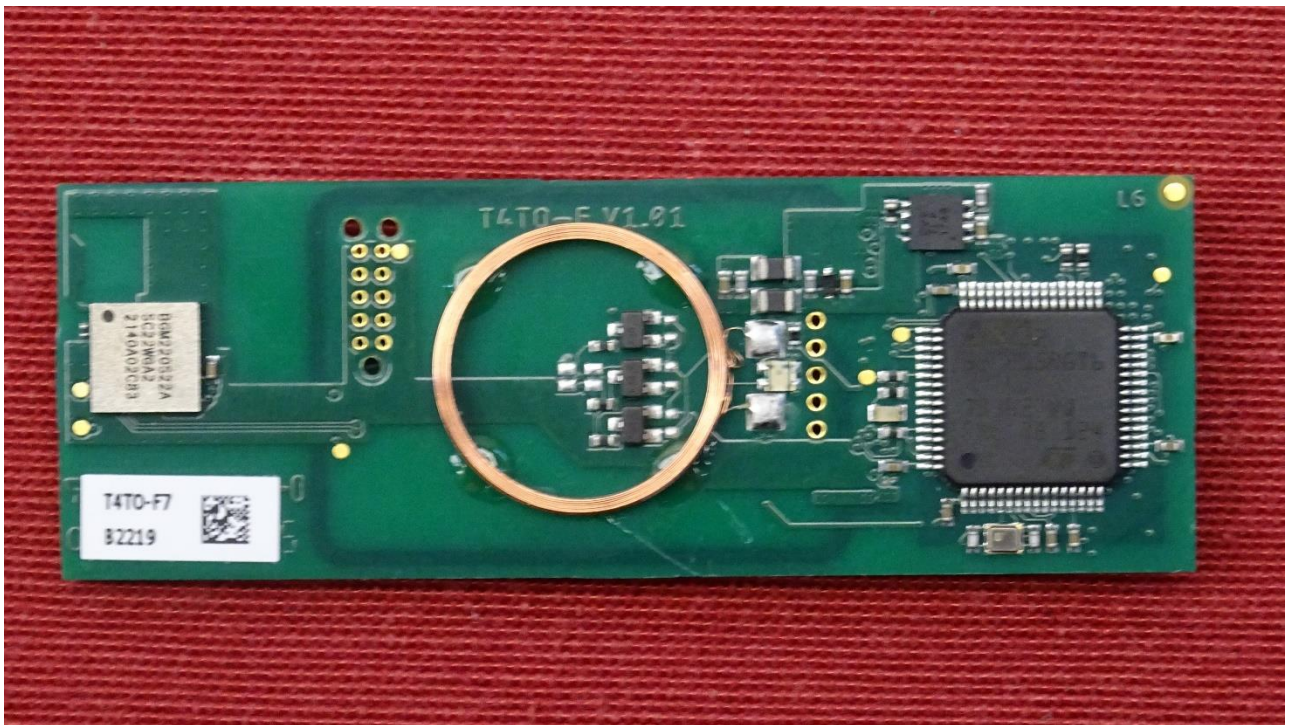


Picture 3: detailed front view of EUT without shielding



Picture 4: Front view of EUT without SIO card





Picture 5: Rear view of EUT

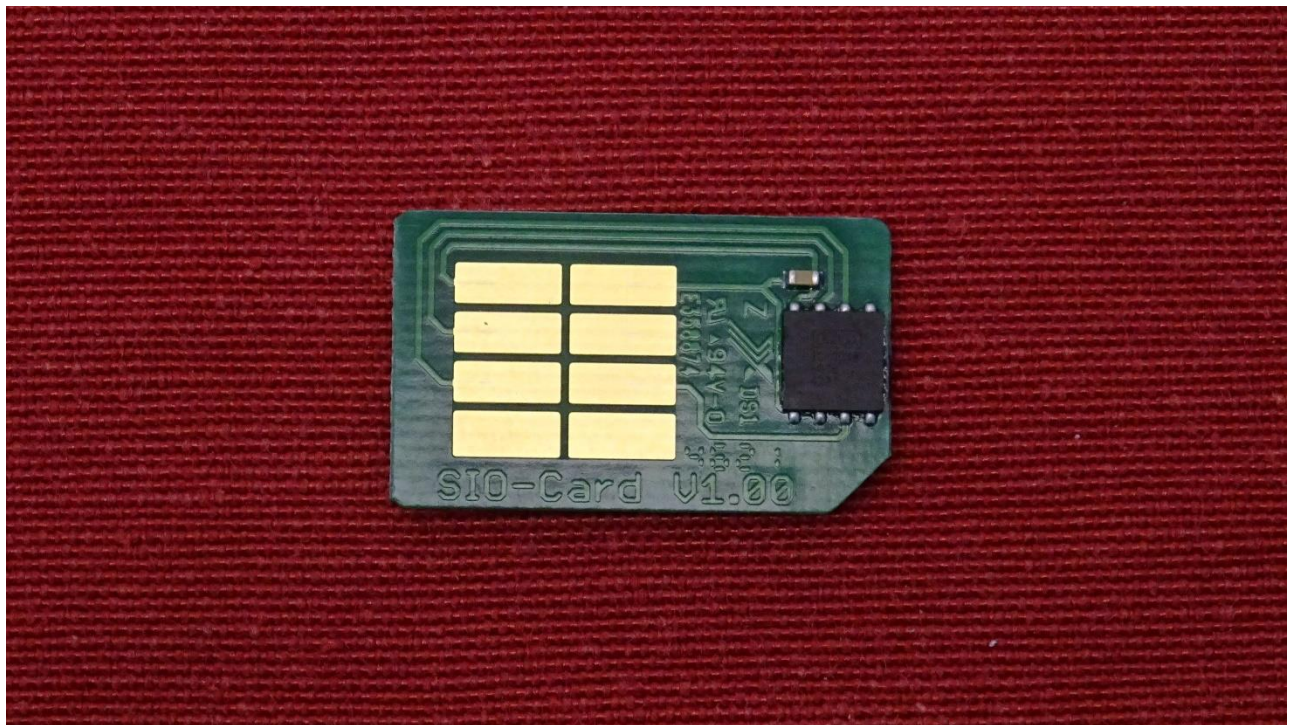


Picture 6: Name plate of BLE-chip

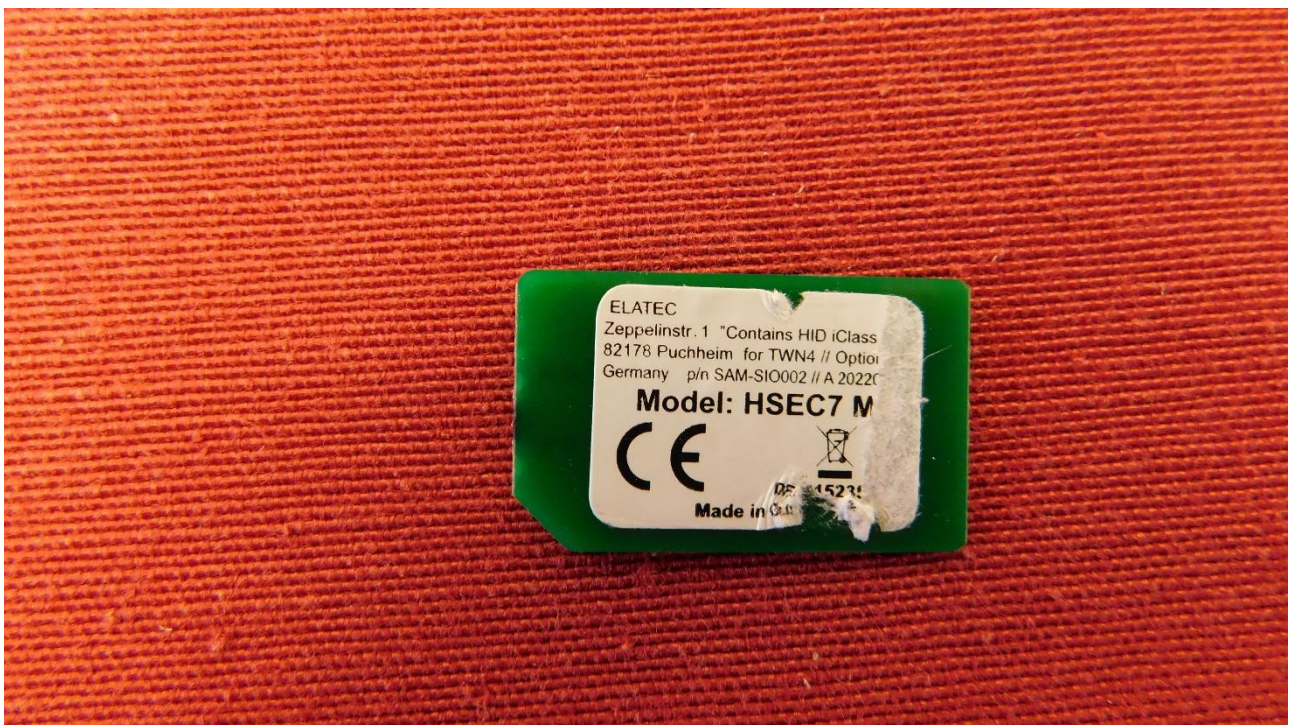


Picture 7: Name plate of EUT

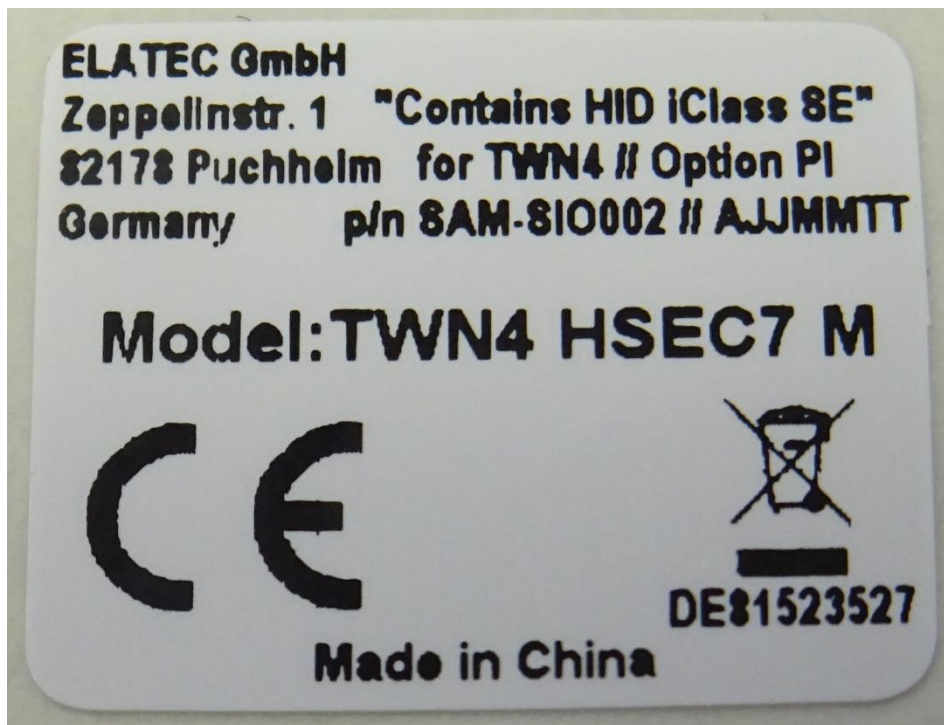




Picture 8: Front view of SIO card



Picture 9: Rear view of SIO card



Picture 10: Name plate of SIO card