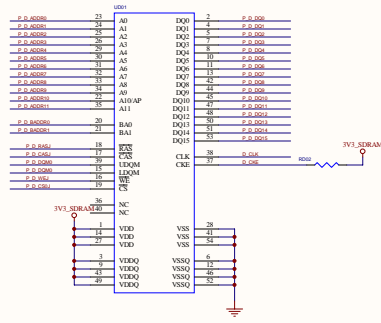
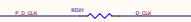
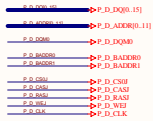
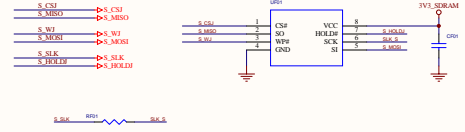


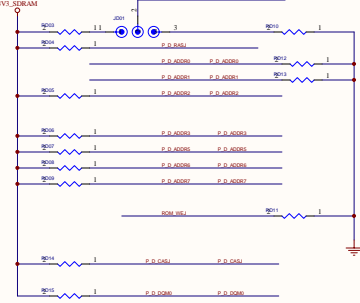
SDRAM



Serial FLASH



M3329C Hardware Config Setting



1. CPU_FLASH_EN[0] = 0; CPU_FLASH_SIZE[0] = 1; CPU_FLASH_ADDR[0] = 0;
2. CPU_FLASH_SIZE[1] = P.D.A0[15]; CPU_FLASH_SIZE[2] = P.D.A0[14]; CPU_FLASH_SIZE[3] = P.D.A0[13]; CPU_FLASH_SIZE[4] = P.D.A0[12]; CPU_FLASH_SIZE[5] = P.D.A0[11]; CPU_FLASH_SIZE[6] = P.D.A0[10]; CPU_FLASH_SIZE[7] = P.D.A0[9]; CPU_FLASH_SIZE[8] = P.D.A0[8]; CPU_FLASH_SIZE[9] = P.D.A0[7]; CPU_FLASH_SIZE[10] = P.D.A0[6]; CPU_FLASH_SIZE[11] = P.D.A0[5]; CPU_FLASH_SIZE[12] = P.D.A0[4]; CPU_FLASH_SIZE[13] = P.D.A0[3]; CPU_FLASH_SIZE[14] = P.D.A0[2]; CPU_FLASH_SIZE[15] = P.D.A0[1]; CPU_FLASH_SIZE[16] = P.D.A0[0];
3. CPU_FLASH_SIZE[16] = P.D.A0[15]; CPU_FLASH_SIZE[17] = P.D.A0[14]; CPU_FLASH_SIZE[18] = P.D.A0[13]; CPU_FLASH_SIZE[19] = P.D.A0[12]; CPU_FLASH_SIZE[20] = P.D.A0[11]; CPU_FLASH_SIZE[21] = P.D.A0[10]; CPU_FLASH_SIZE[22] = P.D.A0[9]; CPU_FLASH_SIZE[23] = P.D.A0[8]; CPU_FLASH_SIZE[24] = P.D.A0[7]; CPU_FLASH_SIZE[25] = P.D.A0[6]; CPU_FLASH_SIZE[26] = P.D.A0[5]; CPU_FLASH_SIZE[27] = P.D.A0[4]; CPU_FLASH_SIZE[28] = P.D.A0[3]; CPU_FLASH_SIZE[29] = P.D.A0[2]; CPU_FLASH_SIZE[30] = P.D.A0[1]; CPU_FLASH_SIZE[31] = P.D.A0[0];
4. CPU_FLASH_SIZE[31] = P.D.A0[15]; CPU_FLASH_SIZE[32] = P.D.A0[14]; CPU_FLASH_SIZE[33] = P.D.A0[13]; CPU_FLASH_SIZE[34] = P.D.A0[12]; CPU_FLASH_SIZE[35] = P.D.A0[11]; CPU_FLASH_SIZE[36] = P.D.A0[10]; CPU_FLASH_SIZE[37] = P.D.A0[9]; CPU_FLASH_SIZE[38] = P.D.A0[8]; CPU_FLASH_SIZE[39] = P.D.A0[7]; CPU_FLASH_SIZE[40] = P.D.A0[6]; CPU_FLASH_SIZE[41] = P.D.A0[5]; CPU_FLASH_SIZE[42] = P.D.A0[4]; CPU_FLASH_SIZE[43] = P.D.A0[3]; CPU_FLASH_SIZE[44] = P.D.A0[2]; CPU_FLASH_SIZE[45] = P.D.A0[1]; CPU_FLASH_SIZE[46] = P.D.A0[0];
5. CPU_FLASH_SIZE[46] = P.D.A0[15]; CPU_FLASH_SIZE[47] = P.D.A0[14]; CPU_FLASH_SIZE[48] = P.D.A0[13]; CPU_FLASH_SIZE[49] = P.D.A0[12]; CPU_FLASH_SIZE[50] = P.D.A0[11]; CPU_FLASH_SIZE[51] = P.D.A0[10]; CPU_FLASH_SIZE[52] = P.D.A0[9]; CPU_FLASH_SIZE[53] = P.D.A0[8]; CPU_FLASH_SIZE[54] = P.D.A0[7]; CPU_FLASH_SIZE[55] = P.D.A0[6]; CPU_FLASH_SIZE[56] = P.D.A0[5]; CPU_FLASH_SIZE[57] = P.D.A0[4]; CPU_FLASH_SIZE[58] = P.D.A0[3]; CPU_FLASH_SIZE[59] = P.D.A0[2]; CPU_FLASH_SIZE[60] = P.D.A0[1]; CPU_FLASH_SIZE[61] = P.D.A0[0];
6. CPU_FLASH_SIZE[61] = P.D.A0[15]; CPU_FLASH_SIZE[62] = P.D.A0[14]; CPU_FLASH_SIZE[63] = P.D.A0[13]; CPU_FLASH_SIZE[64] = P.D.A0[12]; CPU_FLASH_SIZE[65] = P.D.A0[11]; CPU_FLASH_SIZE[66] = P.D.A0[10]; CPU_FLASH_SIZE[67] = P.D.A0[9]; CPU_FLASH_SIZE[68] = P.D.A0[8]; CPU_FLASH_SIZE[69] = P.D.A0[7]; CPU_FLASH_SIZE[70] = P.D.A0[6]; CPU_FLASH_SIZE[71] = P.D.A0[5]; CPU_FLASH_SIZE[72] = P.D.A0[4]; CPU_FLASH_SIZE[73] = P.D.A0[3]; CPU_FLASH_SIZE[74] = P.D.A0[2]; CPU_FLASH_SIZE[75] = P.D.A0[1]; CPU_FLASH_SIZE[76] = P.D.A0[0];
7. CPU_FLASH_SIZE[76] = P.D.A0[15]; CPU_FLASH_SIZE[77] = P.D.A0[14]; CPU_FLASH_SIZE[78] = P.D.A0[13]; CPU_FLASH_SIZE[79] = P.D.A0[12]; CPU_FLASH_SIZE[80] = P.D.A0[11]; CPU_FLASH_SIZE[81] = P.D.A0[10]; CPU_FLASH_SIZE[82] = P.D.A0[9]; CPU_FLASH_SIZE[83] = P.D.A0[8]; CPU_FLASH_SIZE[84] = P.D.A0[7]; CPU_FLASH_SIZE[85] = P.D.A0[6]; CPU_FLASH_SIZE[86] = P.D.A0[5]; CPU_FLASH_SIZE[87] = P.D.A0[4]; CPU_FLASH_SIZE[88] = P.D.A0[3]; CPU_FLASH_SIZE[89] = P.D.A0[2]; CPU_FLASH_SIZE[90] = P.D.A0[1]; CPU_FLASH_SIZE[91] = P.D.A0[0];

E2PROM

