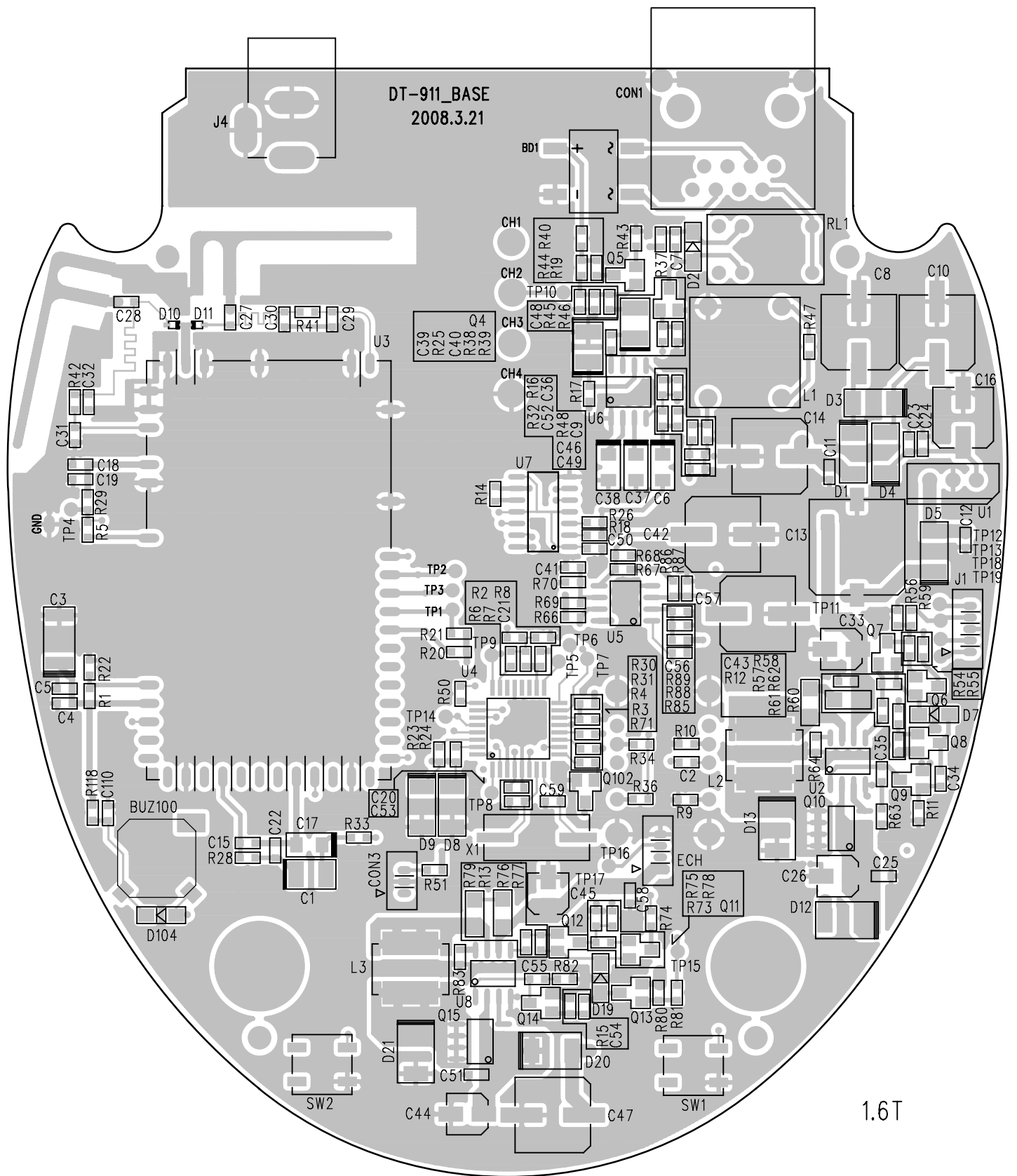
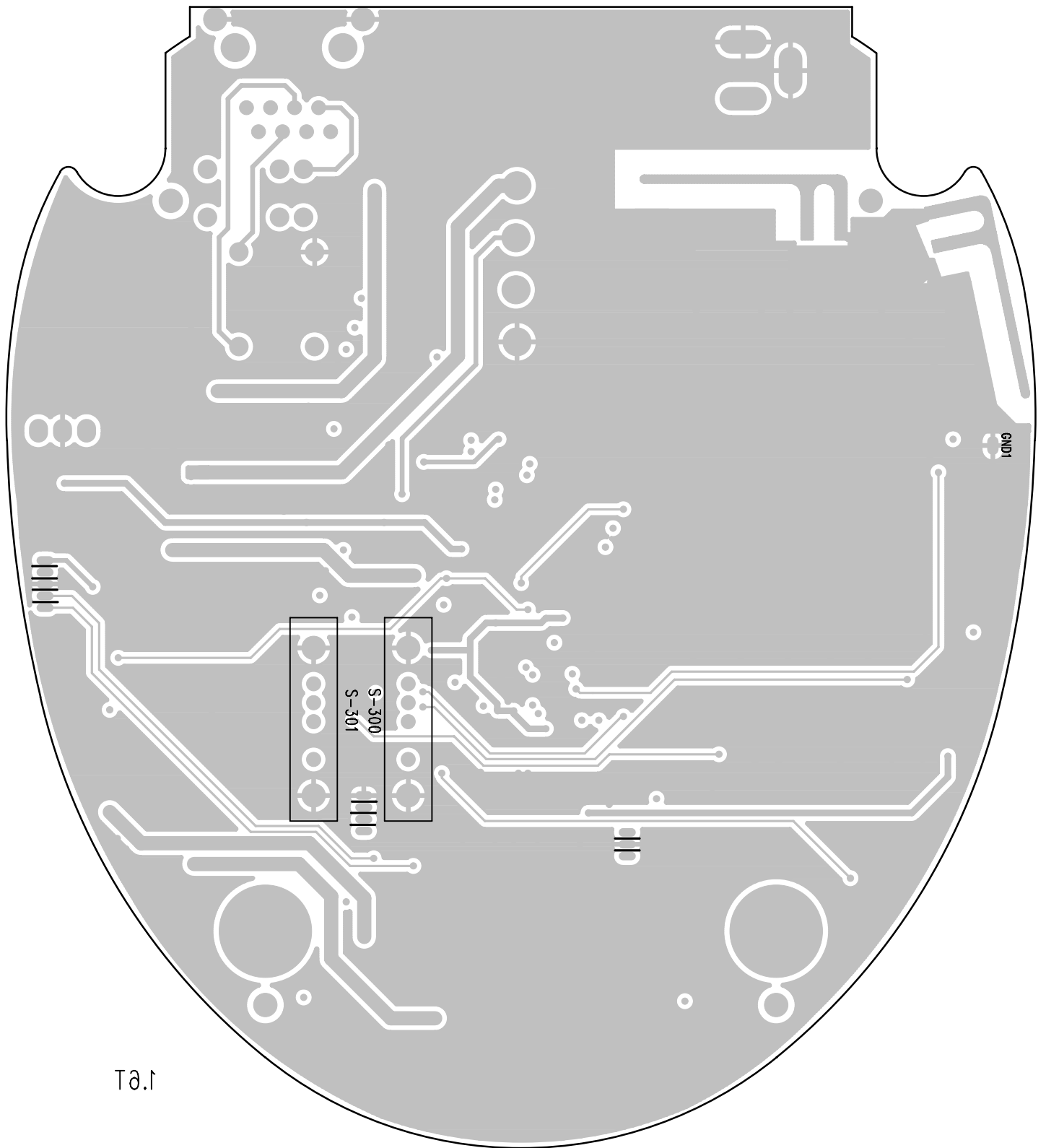


# **PCB Layout**





T&I