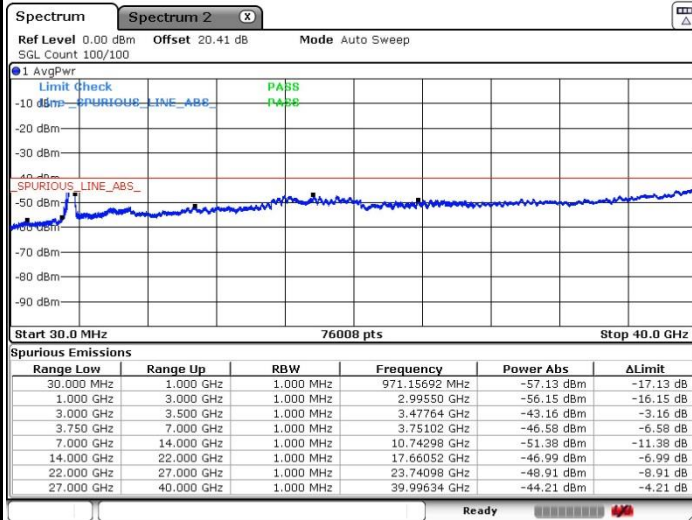




FR1 Part 96 n48 / 90MHz

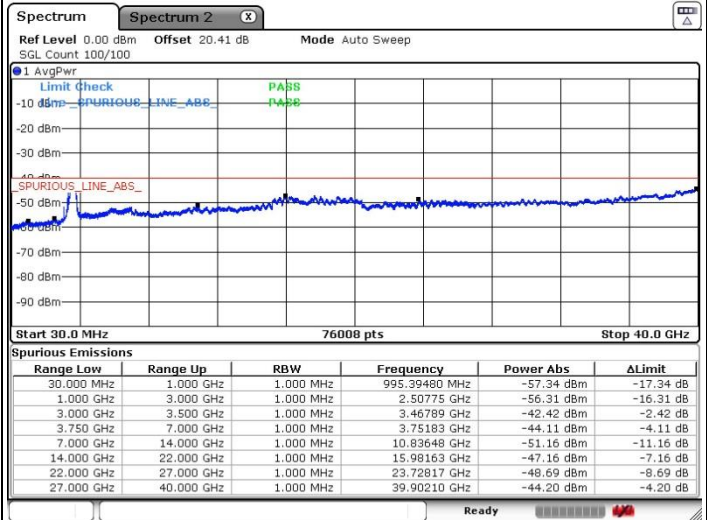
ANT1 256QAM

Lowest Channel / FullRB



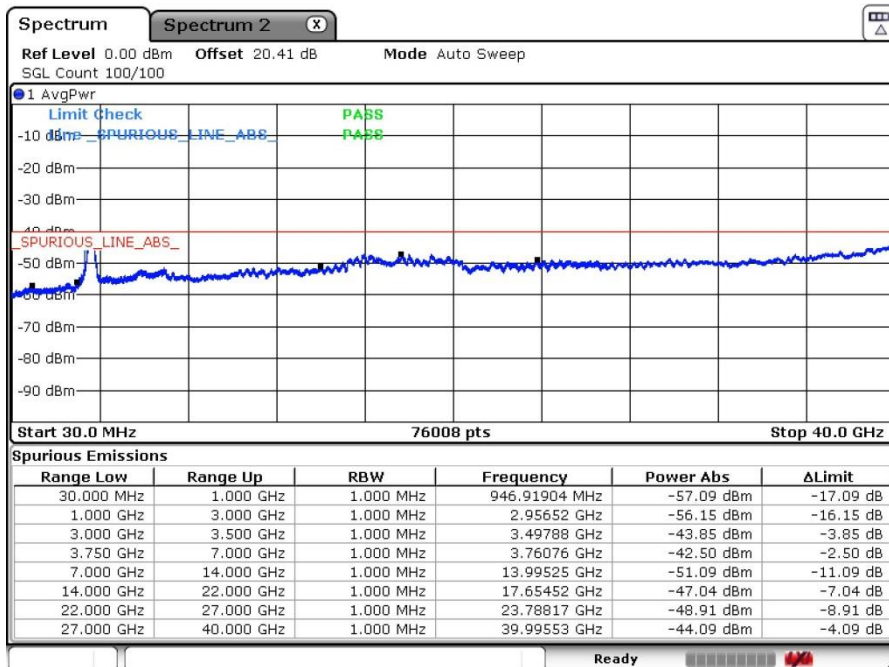
Date: 11.JUL.2023 21:09:47

Middle Channel / FullRB



Date: 11.JUL.2023 21:33:53

Highest Channel / FullRB



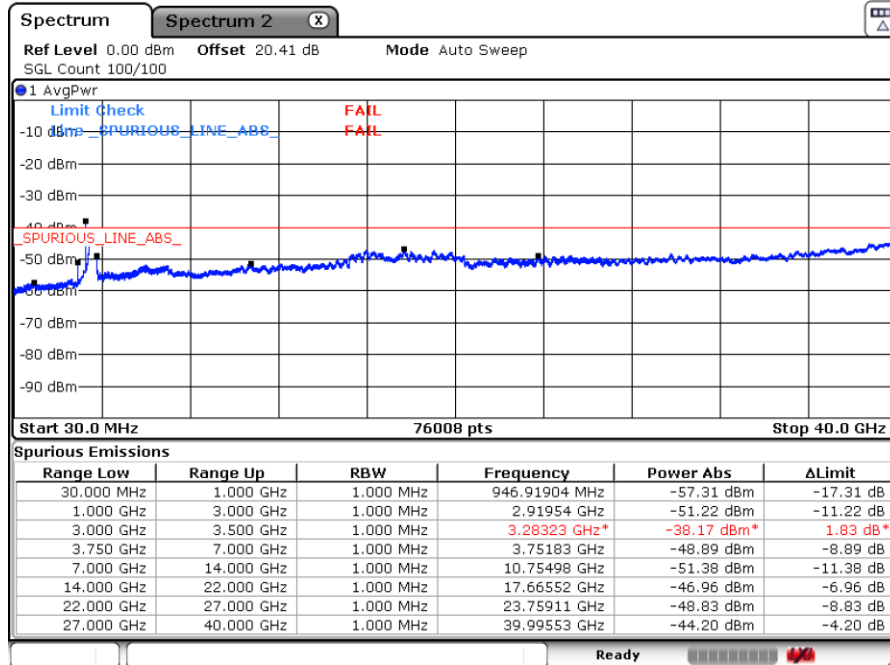
Date: 11.JUL.2023 22:03:04



FR1 Part 96 n48 / 80MHz

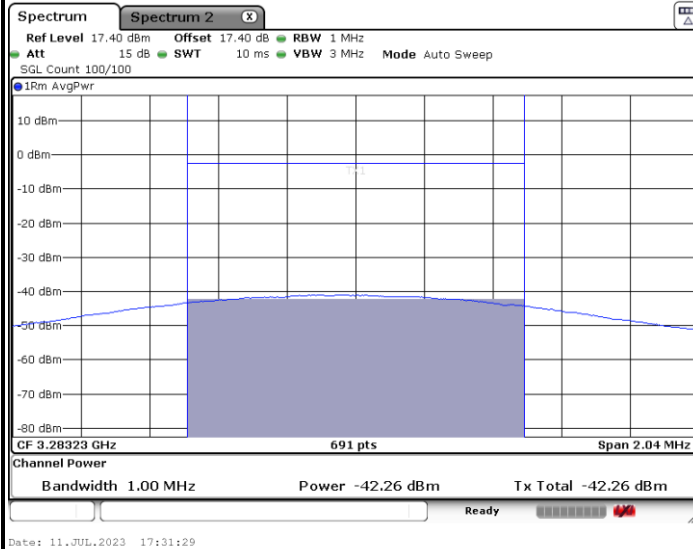
ANT0 64QAM

Lowest Channel / FullIRB



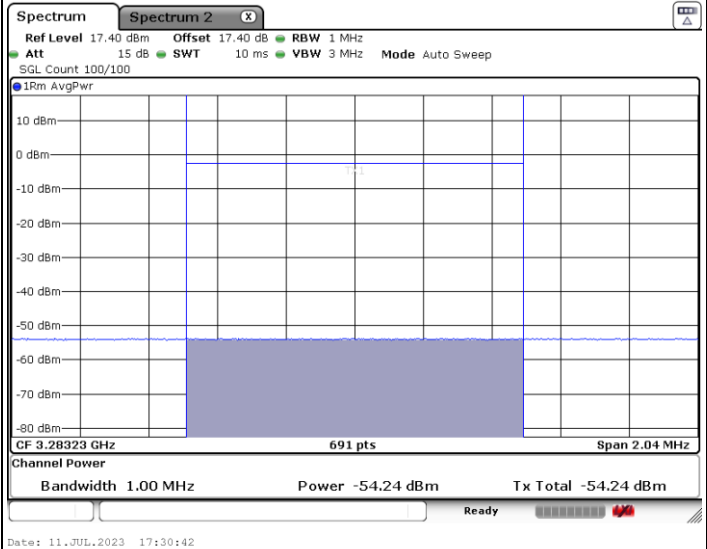
Date: 11.JUL.2023 17:28:21

ANT0



Date: 11.JUL.2023 17:31:29

ANT1

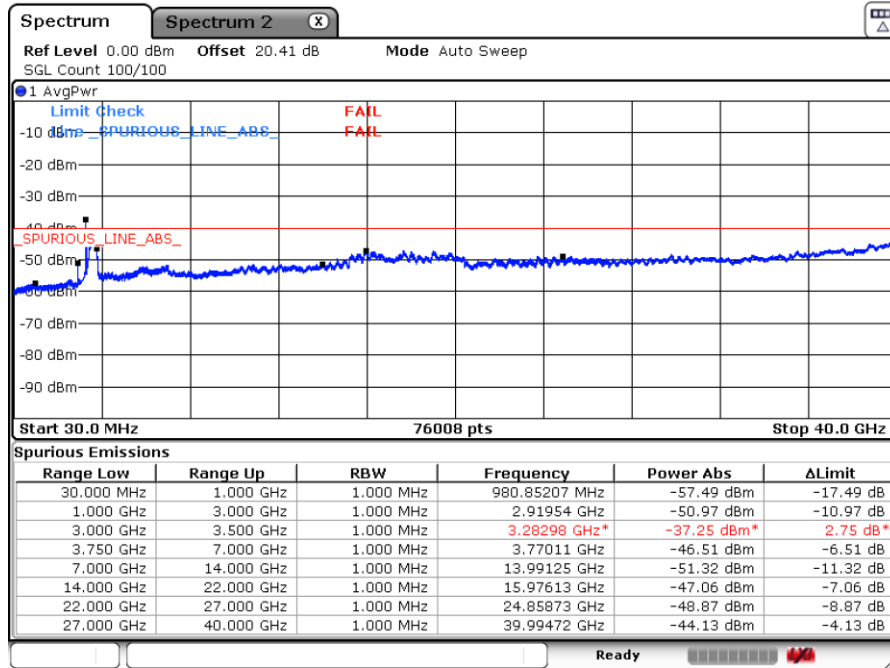


Date: 11.JUL.2023 17:30:42

Note : Verify channel power= $10 \cdot \text{LOG}(10^{(\text{Ant.0}/10)} + 10^{(\text{Ant.1}/10)}) = 10 \cdot \text{LOG}(10^{(-42.26/10)} + 10^{(-54.24/10)}) = -41.99 \text{ dBm}$. Pass

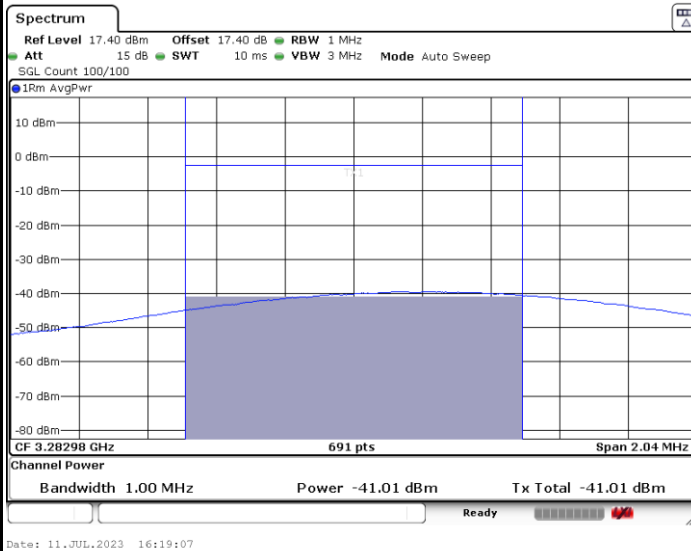


Middle Channel / FullRB

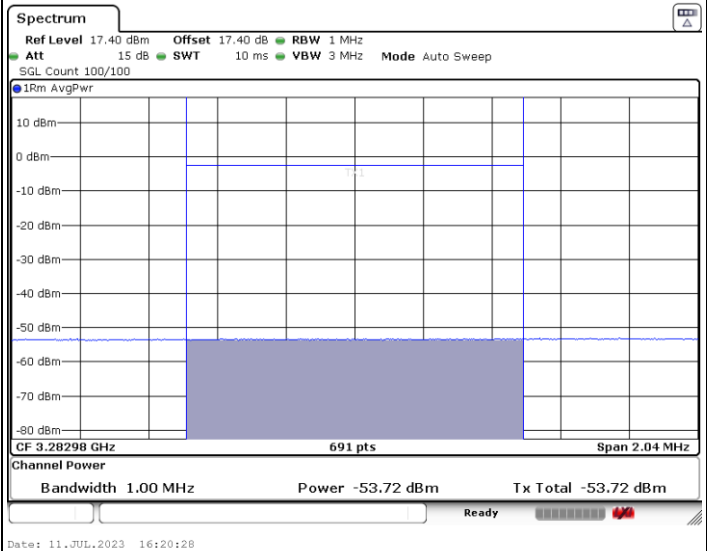


Date: 11.JUL.2023 15:53:07

ANT0



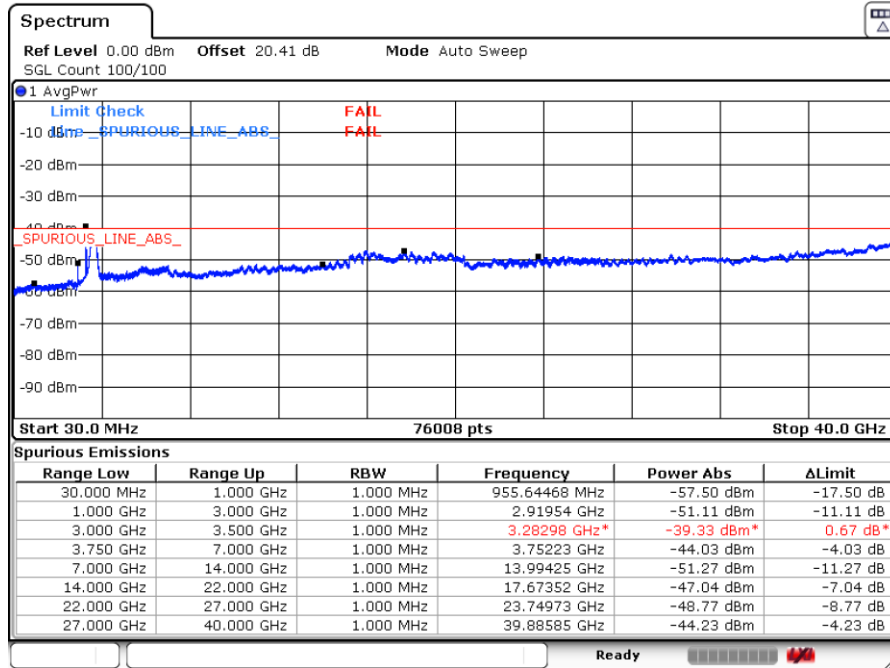
ANT1



Note : Verify channel power = $10 \cdot \text{LOG}(10^{(\text{Ant.0}/10)} + 10^{(\text{Ant.1}/10)}) = 10 \cdot \text{LOG}(10^{(-41.01/10)} + 10^{(-53.72/10)}) = -40.78 \text{ dBm}$. Pass

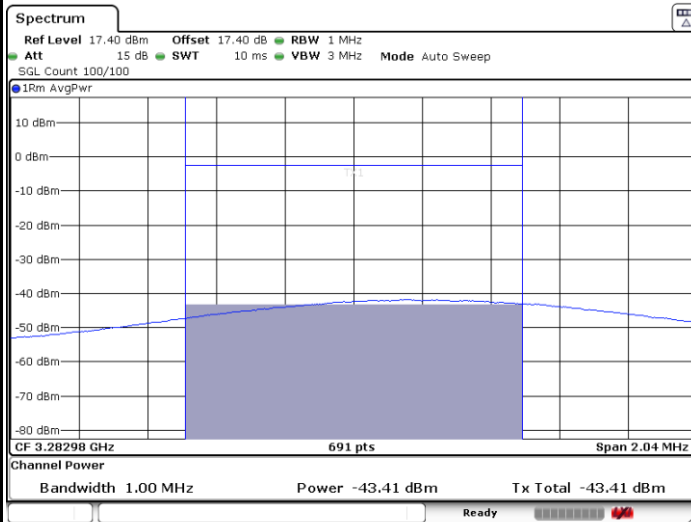


Highest Channel / FullRB



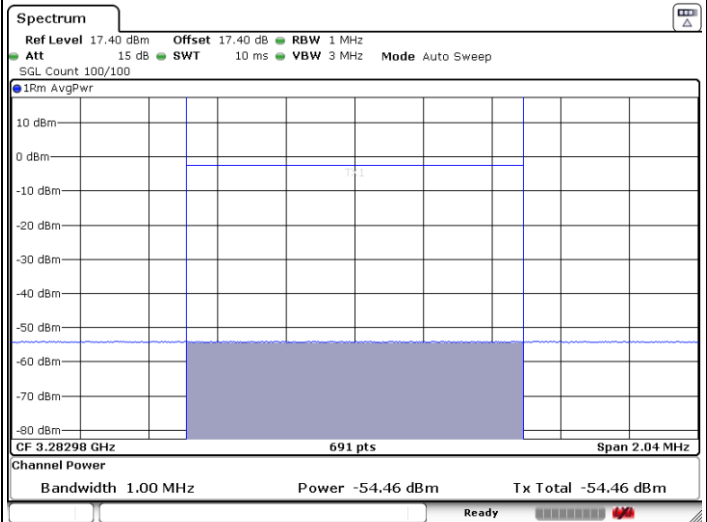
Date: 11.JUL.2023 16:50:45

ANT0



Date: 11.JUL.2023 16:51:24

ANT1



Date: 11.JUL.2023 16:52:38

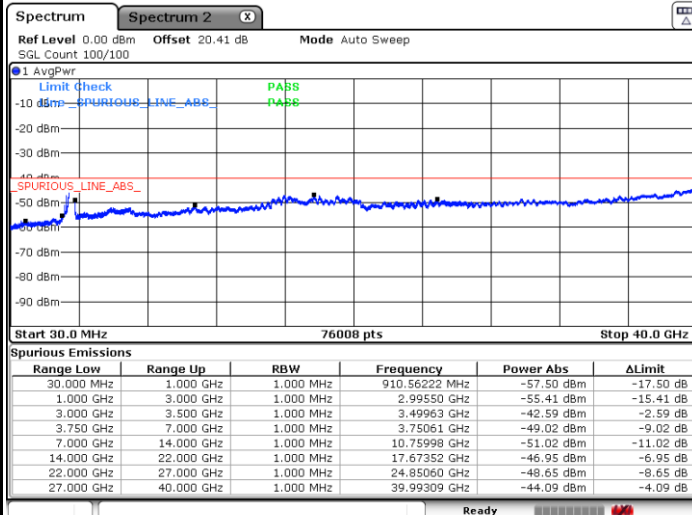
Note : Verify channel power = $10 \cdot \text{LOG}(10^{(\text{Ant.0}/10)} + 10^{(\text{Ant.1}/10)}) = 10 \cdot \text{LOG}(10^{(-43.41/10)} + 10^{(-54.46/10)}) = -43.08 \text{ dBm}$. Pass



FR1 Part 96 n48 / 80MHz

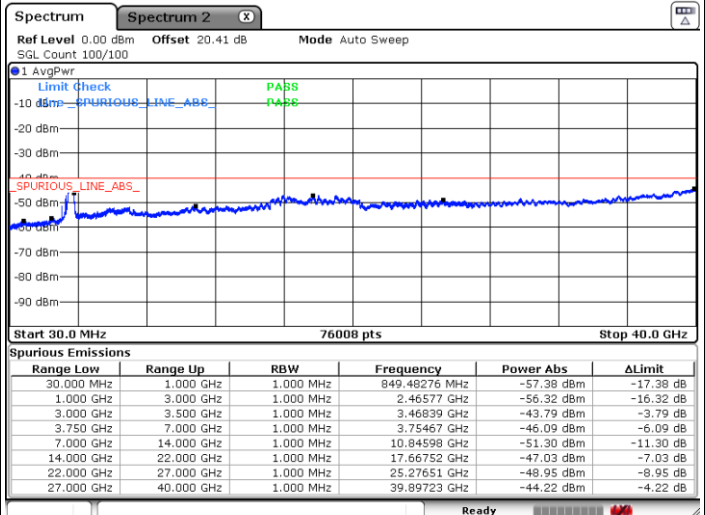
ANT1 64QAM

Lowest Channel / FullRB



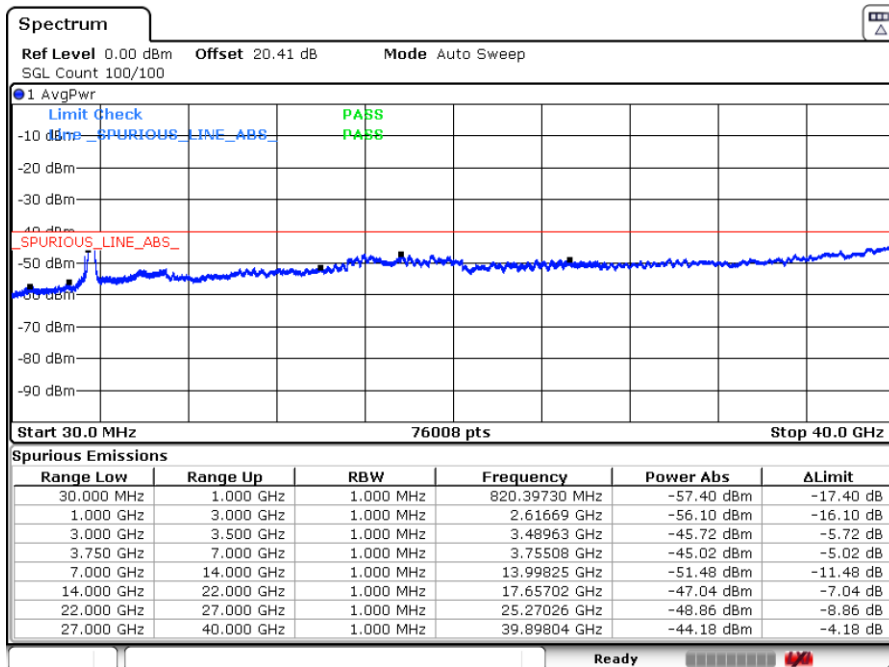
Date: 11.JUL.2023 17:30:10

Middle Channel / FullRB



Date: 11.JUL.2023 15:56:53

Highest Channel / FullRB



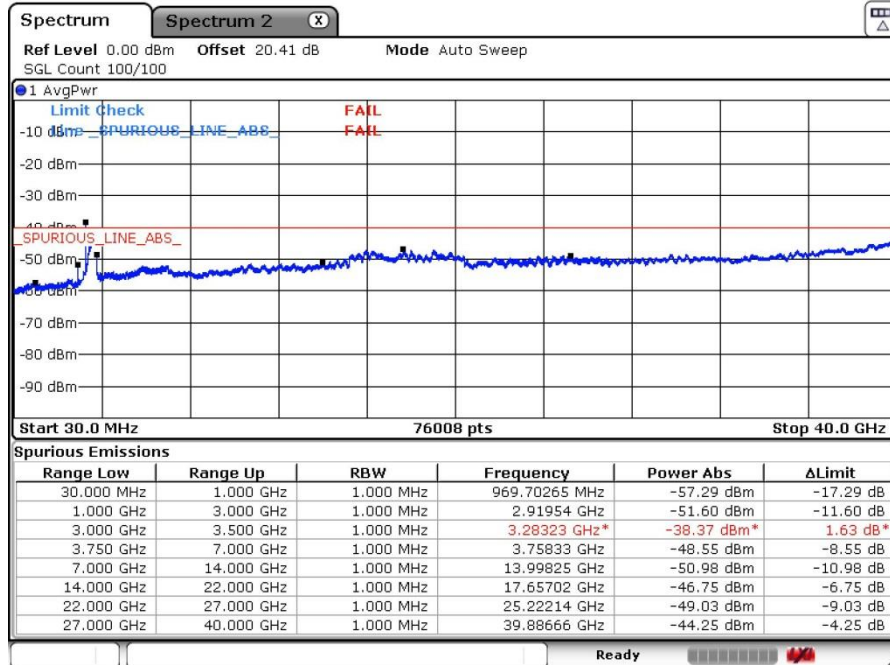
Date: 11.JUL.2023 16:47:15



FR1 Part 96 n48 / 80MHz

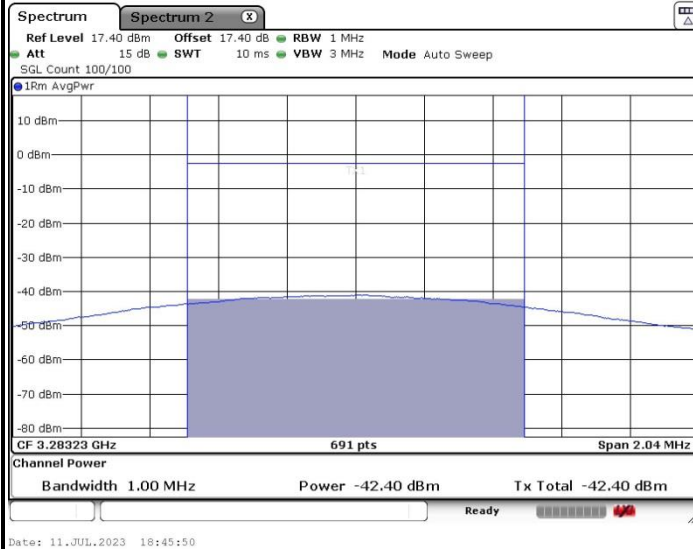
ANT0 256QAM

Lowest Channel / FullIRB



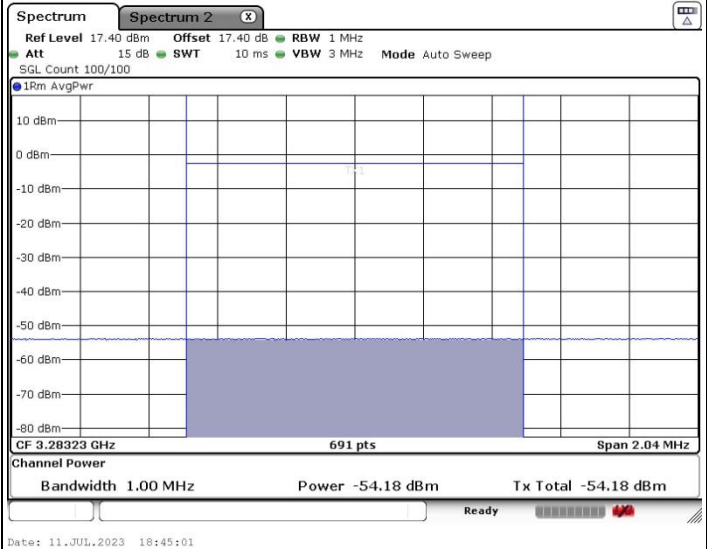
Date: 11.JUL.2023 18:32:09

ANT0



Date: 11.JUL.2023 18:45:50

ANT1

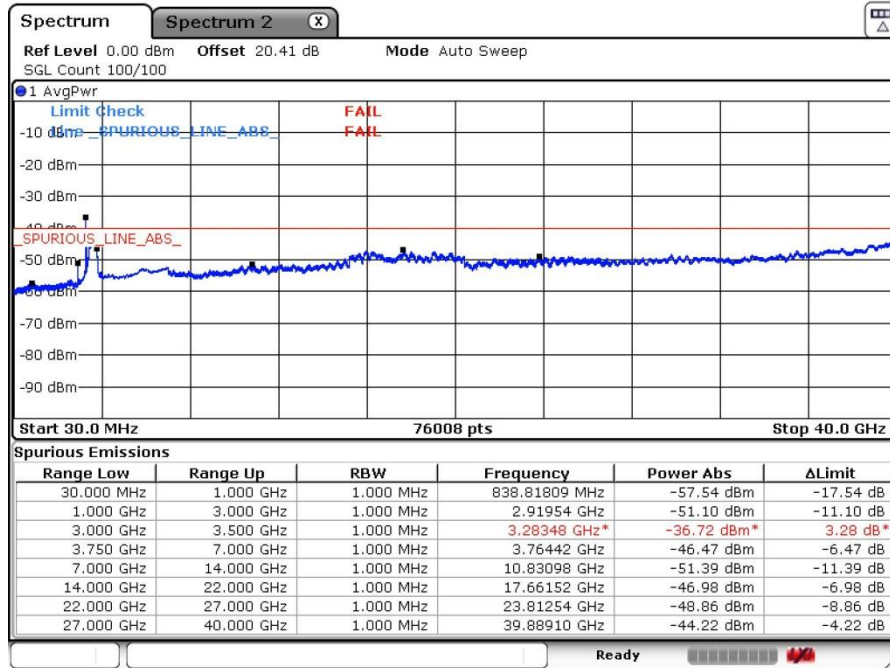


Date: 11.JUL.2023 18:45:01

Note : Verify channel power = $10 \cdot \log(10^{(\text{Ant.0}/10)} + 10^{(\text{Ant.1}/10)}) = 10 \cdot \log(10^{(-42.4/10)} + 10^{(-54.18/10)}) = -42.12 \text{ dBm}$. Pass

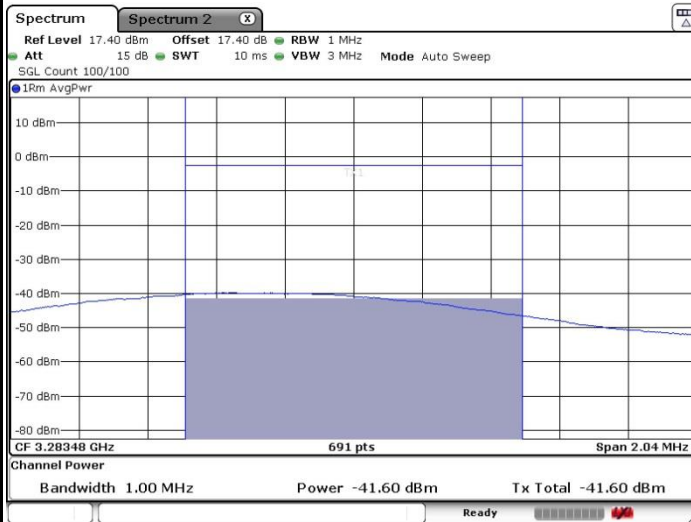


Middle Channel / FullRB



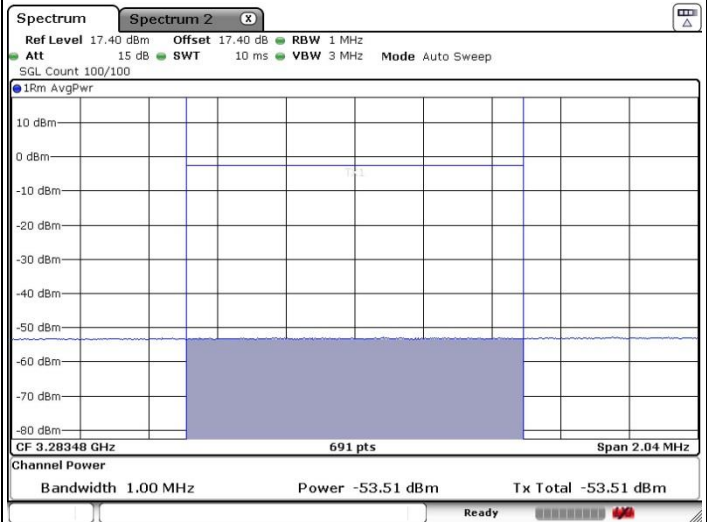
Date: 11.JUL.2023 20:05:21

ANT0



Date: 11.JUL.2023 20:07:00

ANT1

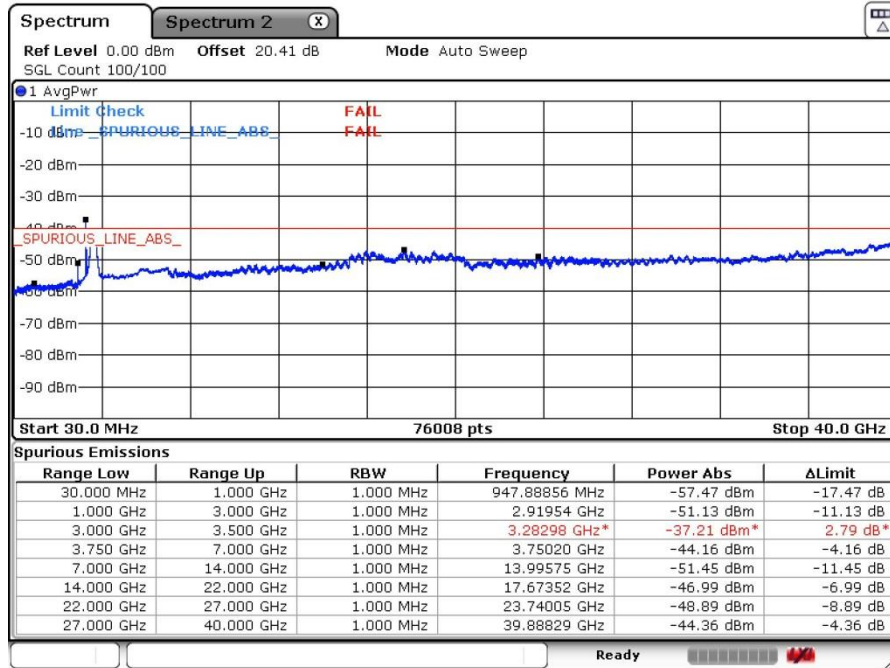


Date: 11.JUL.2023 20:07:39

Note : Verify channel power = $10 \cdot \log(10^{(\text{Ant.0/10})} + 10^{(\text{Ant.1/10})}) = 10 \cdot \log(10^{(-41.6/10)} + 10^{(-53.51/10)}) = -41.33 \text{ dBm}$. Pass

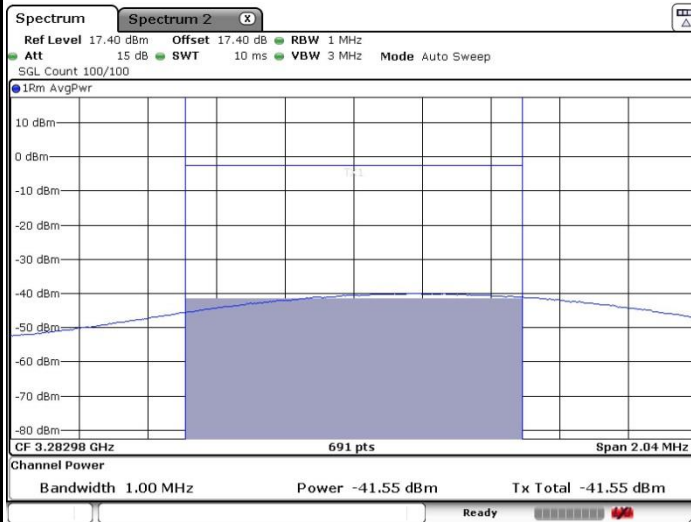


Highest Channel / FullRB



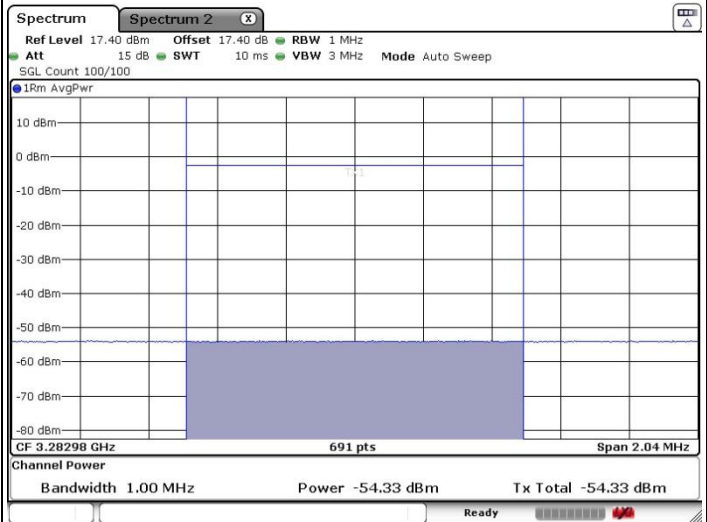
Date: 11.JUL.2023 20:28:11

ANT0



Date: 11.JUL.2023 20:33:40

ANT1



Date: 11.JUL.2023 20:31:18

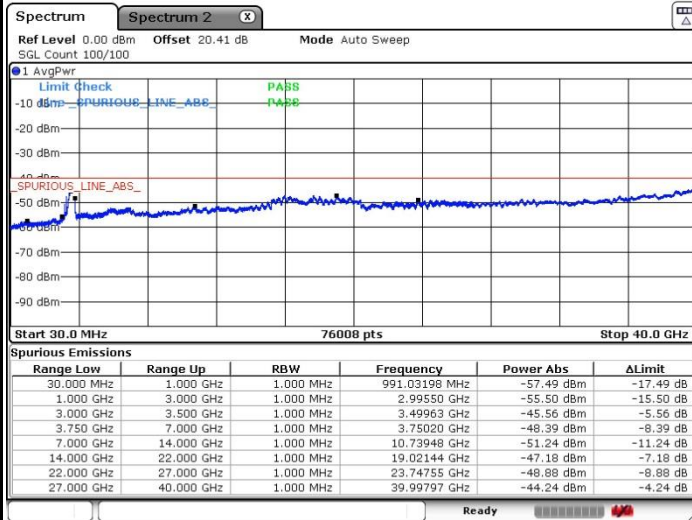
Note : Verify channel power = $10 \cdot \text{LOG}(10^{(\text{Ant.0}/10)} + 10^{(\text{Ant.1}/10)}) = 10 \cdot \text{LOG}(10^{(-41.55/10)} + 10^{(-54.33/10)}) = -41.33 \text{ dBm}$. Pass



FR1 Part 96 n48 / 80MHz

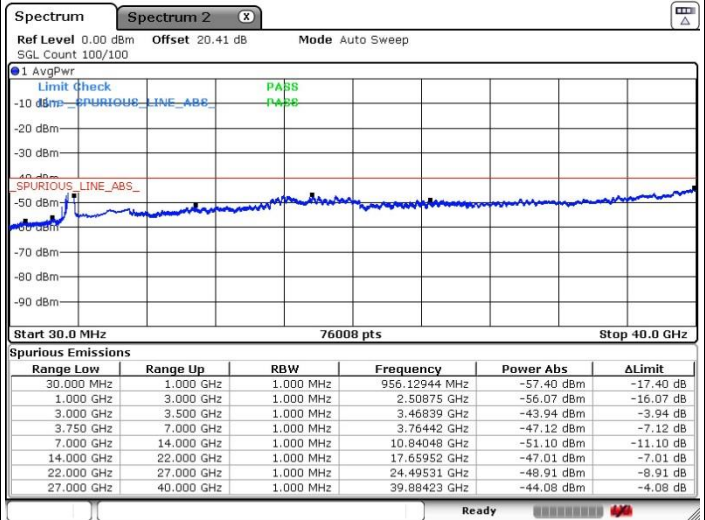
ANT1 256QAM

Lowest Channel / FullRB



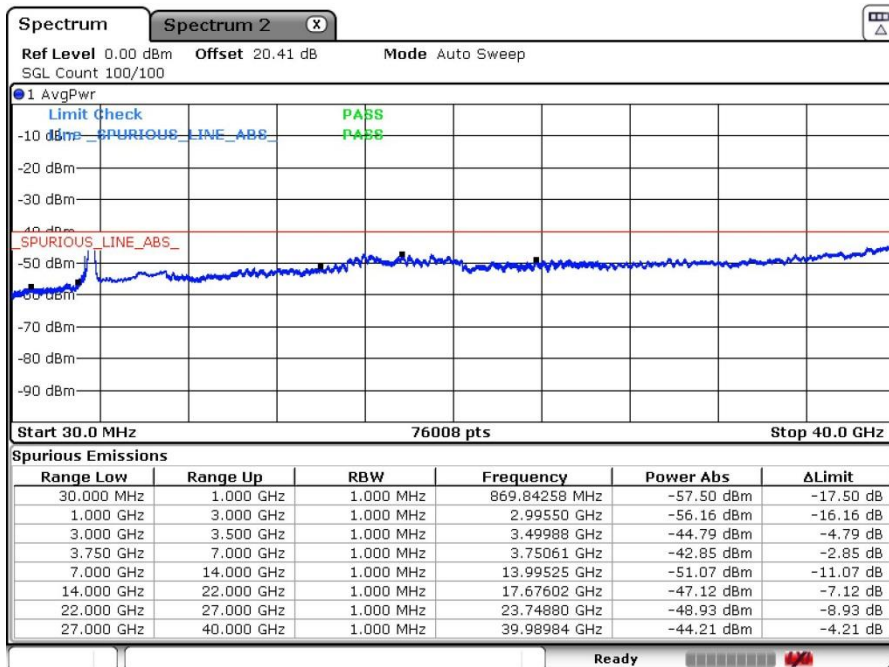
Date: 11.JUL.2023 18:42:47

Middle Channel / FullRB



Date: 11.JUL.2023 20:03:03

Highest Channel / FullRB



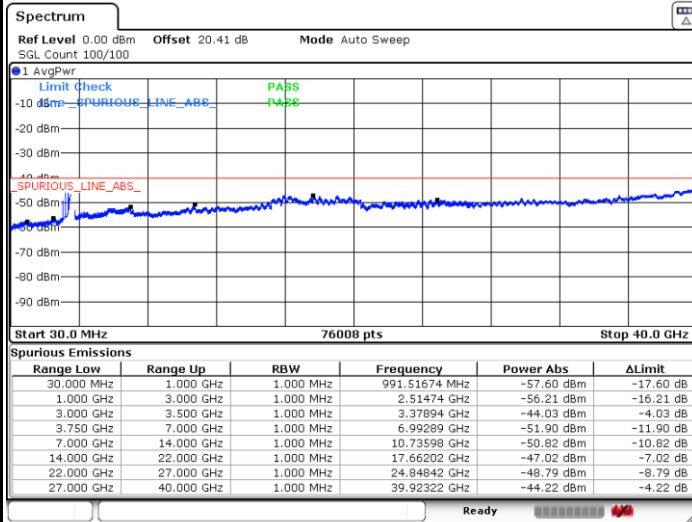
Date: 11.JUL.2023 20:30:23



FR1 Part 96 n48 / 60MHz

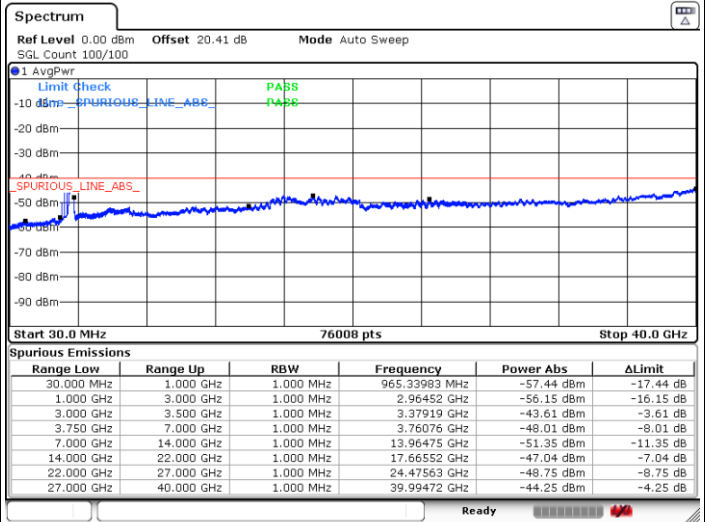
ANT0 64QAM

Lowest Channel / FullRB



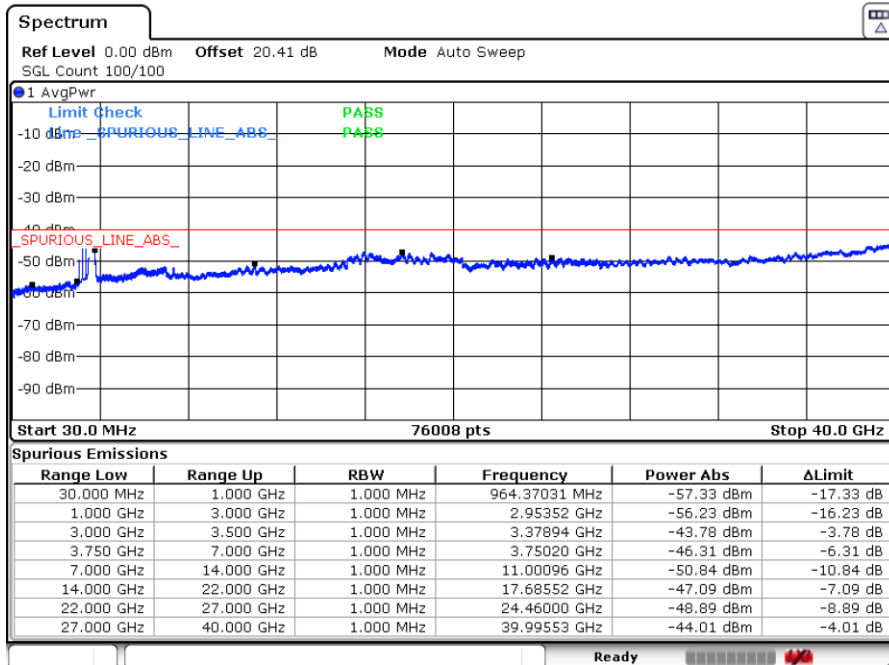
Date: 11.JUL.2023 10:51:30

Middle Channel / FullRB



Date: 11.JUL.2023 09:27:05

Highest Channel / FullRB



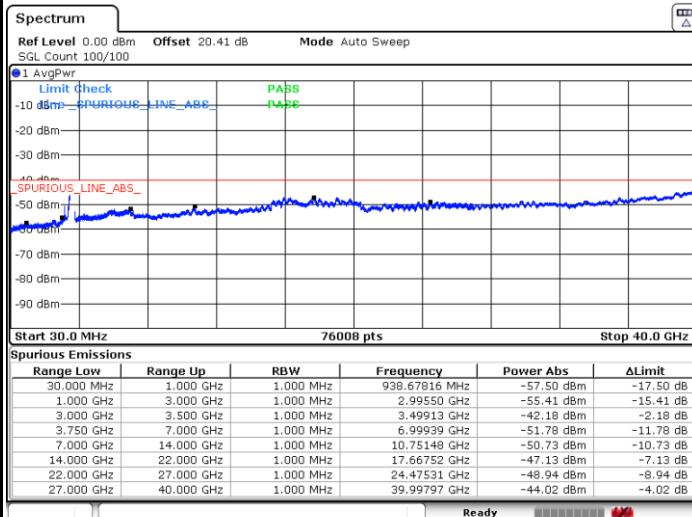
Date: 11.JUL.2023 10:18:26



FR1 Part 96 n48 / 60MHz

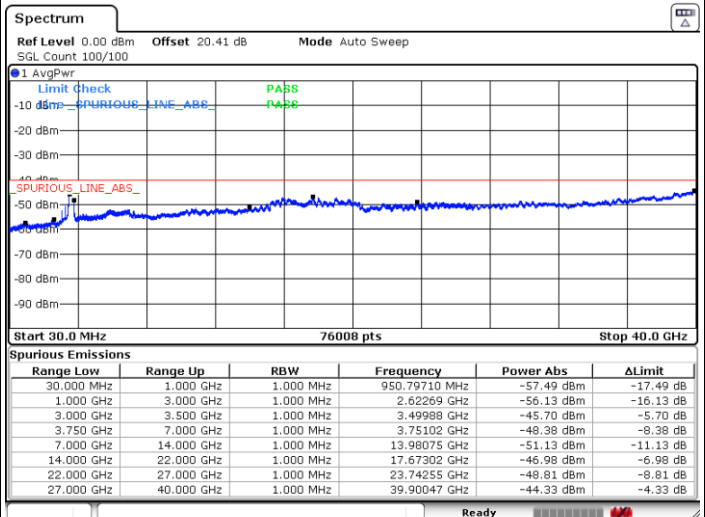
ANT1 64QAM

Lowest Channel / FullRB



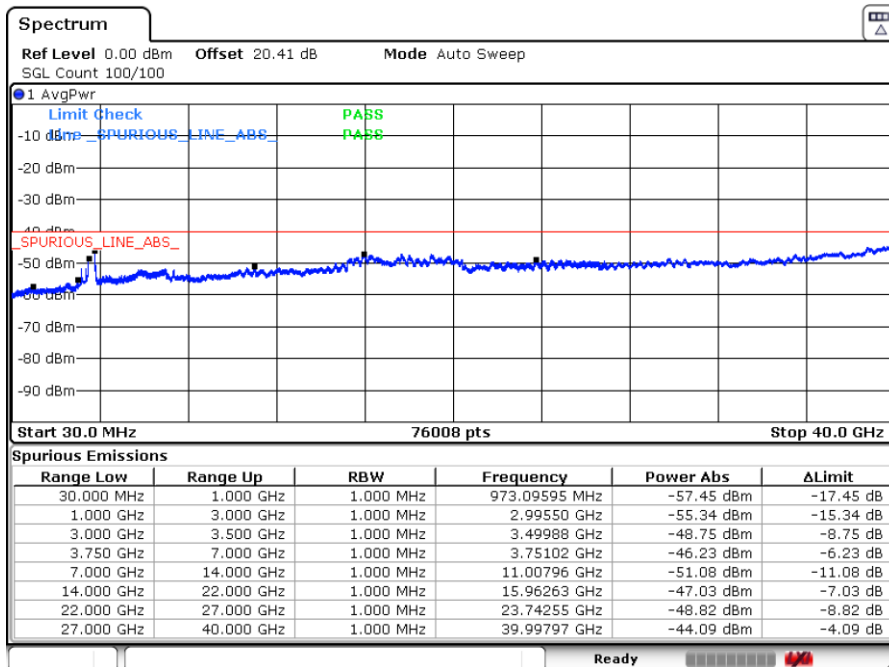
Date: 11.JUL.2023 10:55:42

Middle Channel / FullRB



Date: 11.JUL.2023 09:29:56

Highest Channel / FullRB



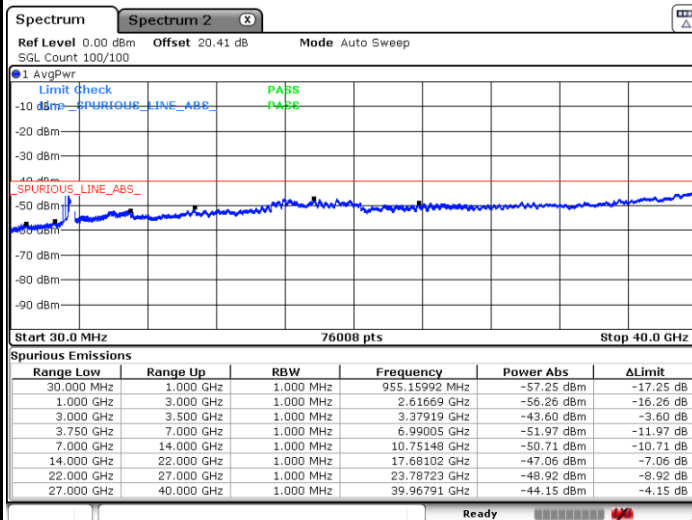
Date: 11.JUL.2023 10:21:31



FR1 Part 96 n48 / 60MHz

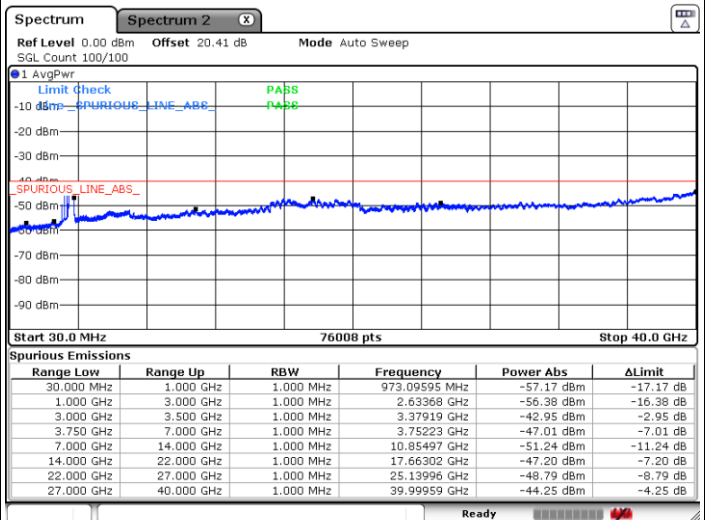
ANT0 256QAM

Lowest Channel / FullRB



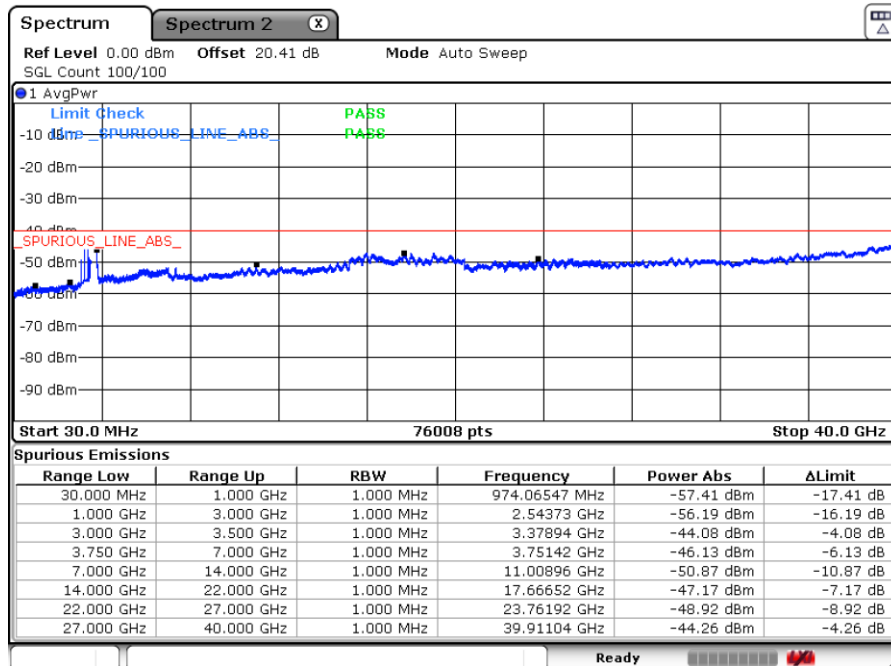
Date: 11.JUL.2023 13:37:39

Middle Channel / FullRB



Date: 11.JUL.2023 13:51:11

Highest Channel / FullRB



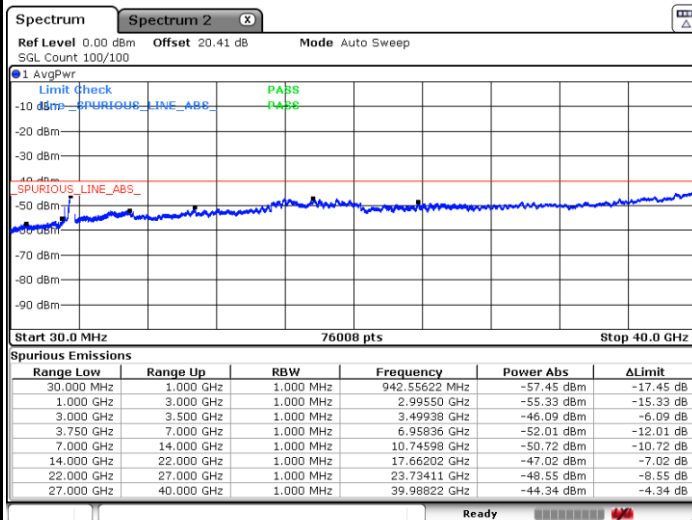
Date: 11.JUL.2023 14:34:36



FR1 Part 96 n48 / 60MHz

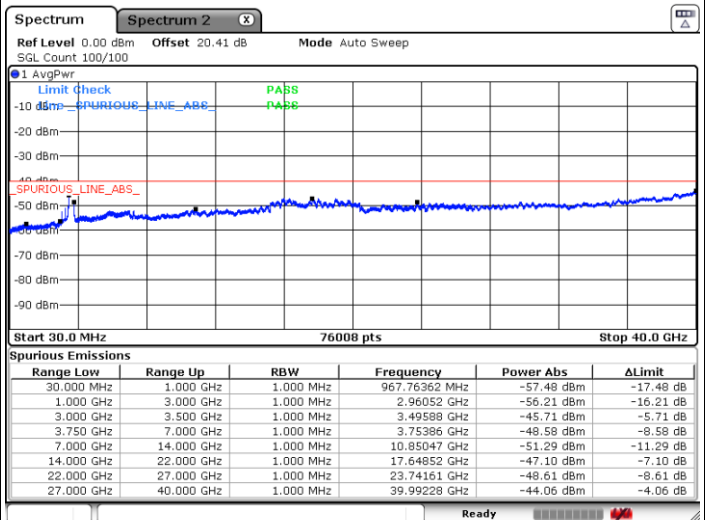
ANT1 256QAM

Lowest Channel / FullRB



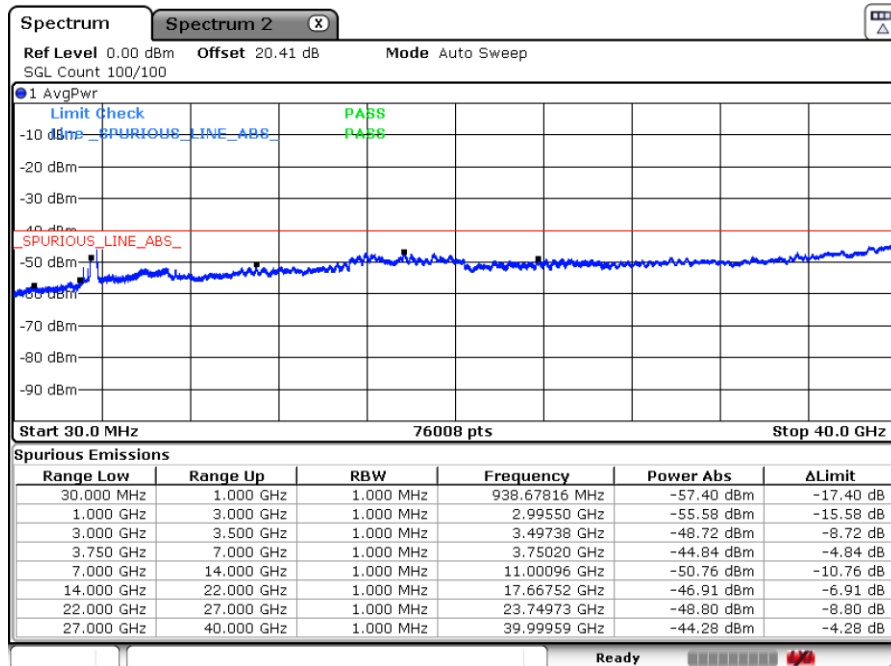
Date: 11.JUL.2023 13:40:12

Middle Channel / FullRB



Date: 11.JUL.2023 13:48:56

Highest Channel / FullRB



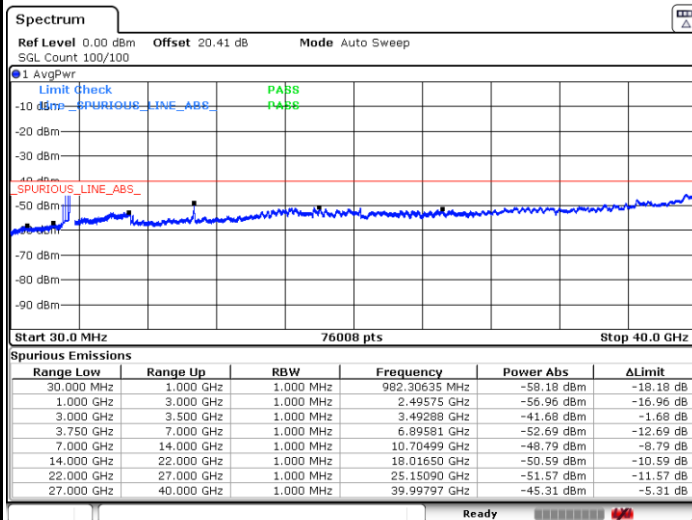
Date: 11.JUL.2023 14:36:44



FR1 Part 96 n48 / 40MHz

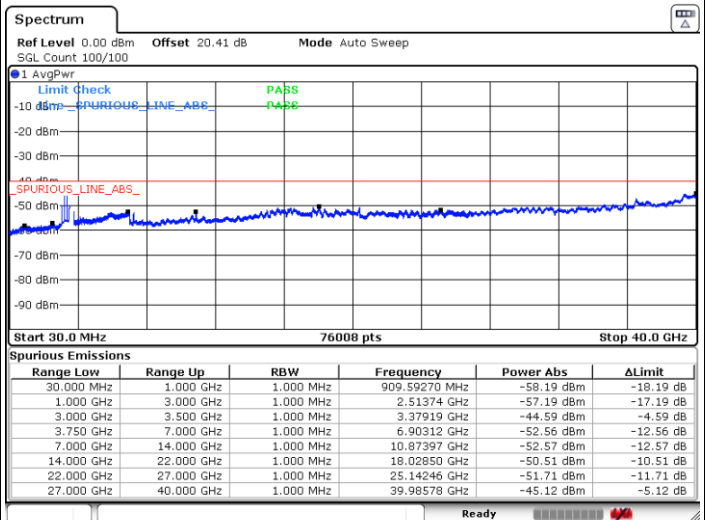
ANT0 64QAM

Lowest Channel / FullRB



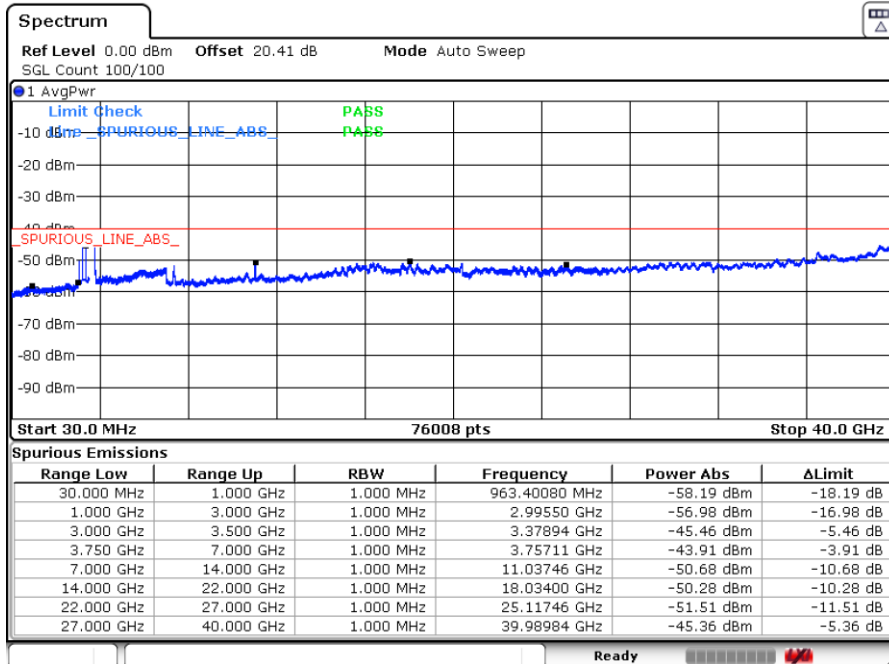
Date: 19.JUL.2023 20:54:39

Middle Channel / FullRB



Date: 19.JUL.2023 21:02:39

Highest Channel / FullRB



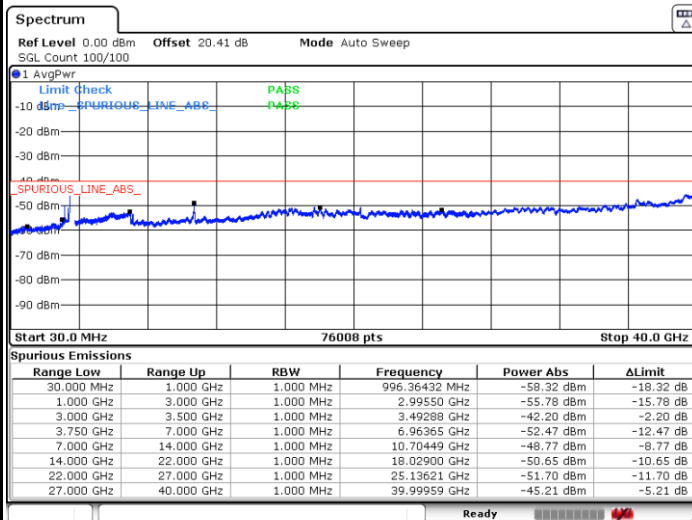
Date: 19.JUL.2023 21:48:56



FR1 Part 96 n48 / 40MHz

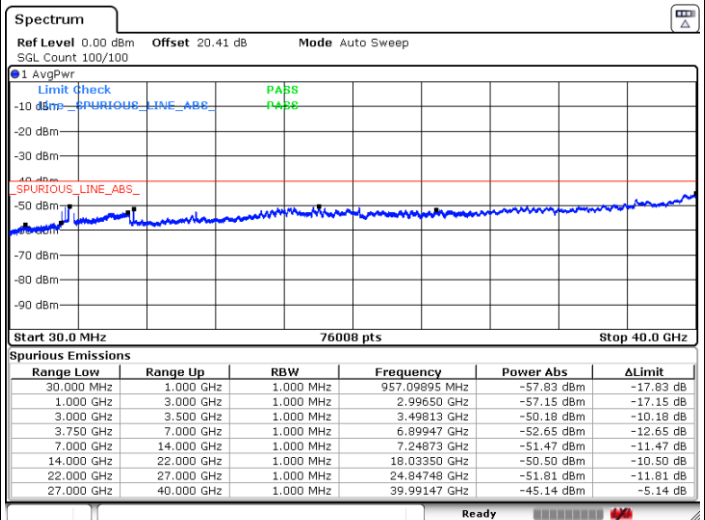
ANT1 64QAM

Lowest Channel / FullRB



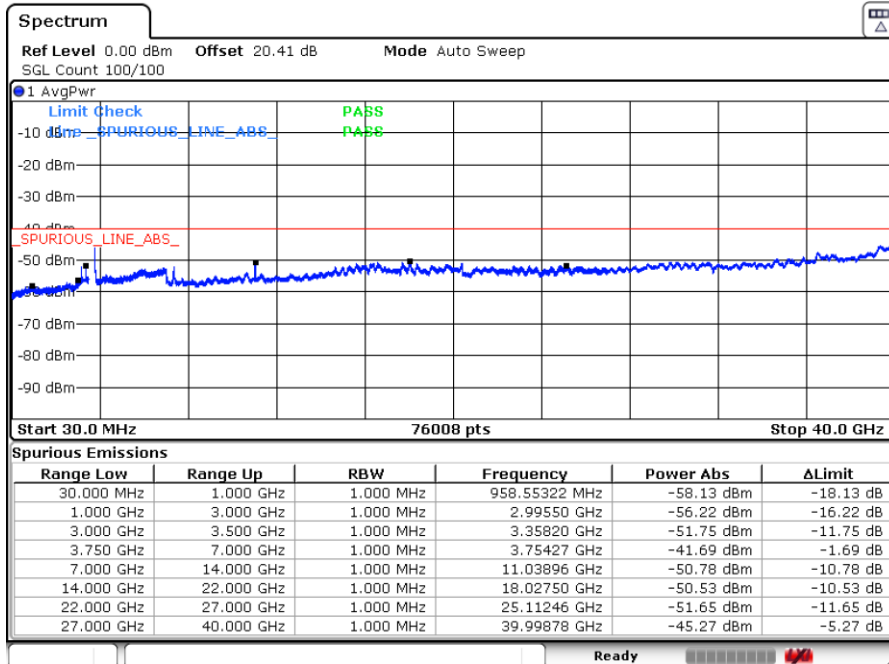
Date: 19.JUL.2023 21:35:43

Middle Channel / FullRB



Date: 19.JUL.2023 21:13:29

Highest Channel / FullRB



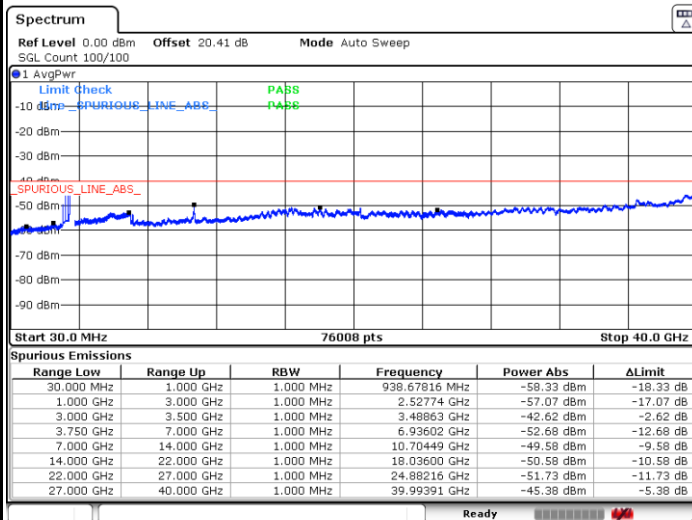
Date: 19.JUL.2023 21:45:16



FR1 Part 96 n48 / 40MHz

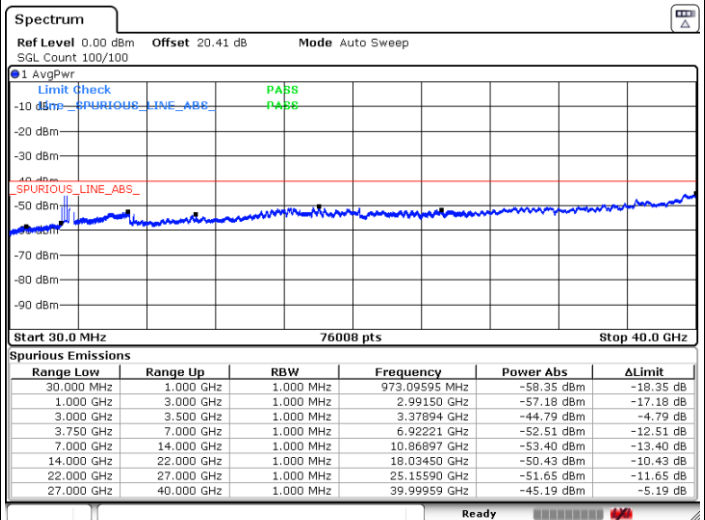
ANT0 256QAM

Lowest Channel / FullRB



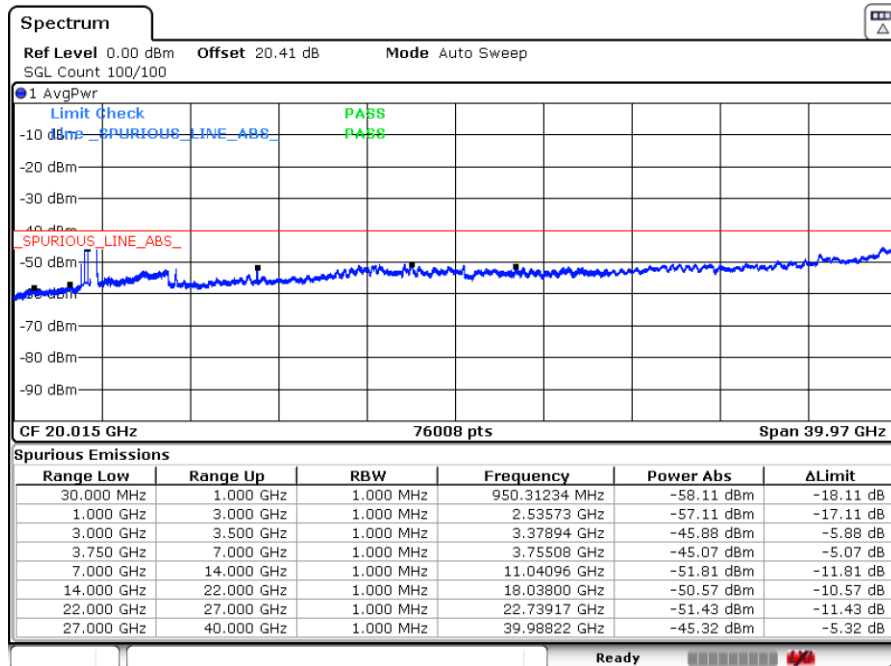
Date: 19.JUL.2023 18:55:27

Middle Channel / FullRB



Date: 19.JUL.2023 20:08:18

Highest Channel / FullRB



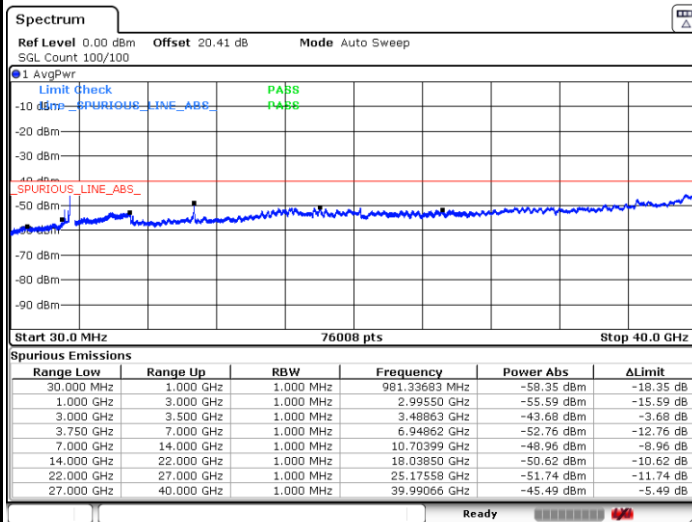
Date: 19.JUL.2023 20:26:54



FR1 Part 96 n48 / 40MHz

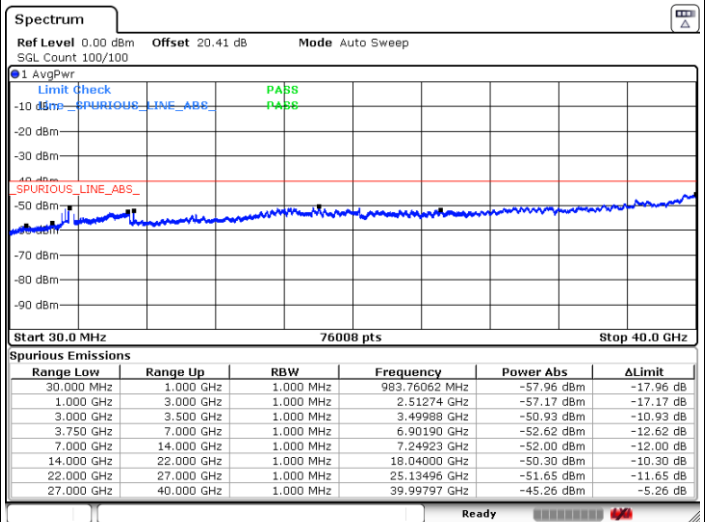
ANT1 256QAM

Lowest Channel / FullRB



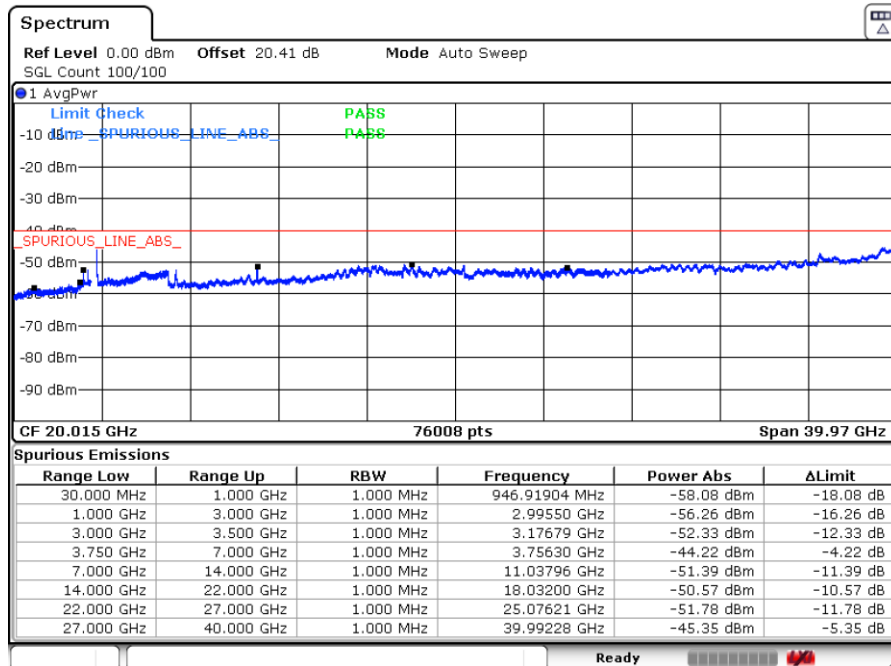
Date: 19.JUL.2023 18:46:54

Middle Channel / FullRB



Date: 19.JUL.2023 20:16:08

Highest Channel / FullRB



Date: 19.JUL.2023 20:24:40

**Frequency Stability**

Test Conditions		N48 (64QAM) / ANT0		
Temperature (°C)	Voltage (Volt)	BW 100MHz		Note 2.
		3550MHz	3700MHz	Result
		FL	FH	
50	Normal Voltage	3550.12	3699.76	PASS
40	Normal Voltage	3550.12	3699.76	
30	Normal Voltage	3550.12	3699.76	
20(Ref.)	Normal Voltage	3550.12	3699.76	
10	Normal Voltage	3550.12	3699.76	
0	Normal Voltage	3550.12	3699.76	
-10	Normal Voltage	3550.12	3699.76	
-20	Normal Voltage	3550.12	3699.76	
-30	Normal Voltage	3550.12	3699.76	
20	Maximum Voltage	3550.12	3699.76	
20	Normal Voltage	3550.12	3699.76	
20	Minimum Voltage	3550.12	3699.76	

Test Conditions		N48 (64QAM) / ANT1		
Temperature (°C)	Voltage (Volt)	BW 100MHz		Note 2.
		3550MHz	3700MHz	Result
		FL	FH	
50	Normal Voltage	3550.12	3699.76	PASS
40	Normal Voltage	3550.12	3699.76	
30	Normal Voltage	3550.12	3699.76	
20(Ref.)	Normal Voltage	3550.12	3699.76	
10	Normal Voltage	3550.12	3699.76	
0	Normal Voltage	3550.12	3699.76	
-10	Normal Voltage	3550.12	3699.76	
-20	Normal Voltage	3550.12	3699.76	
-30	Normal Voltage	3550.12	3699.76	
20	Maximum Voltage	3550.12	3699.76	
20	Normal Voltage	3550.12	3699.76	
20	Minimum Voltage	3550.12	3699.76	

Note:

1. Normal Voltage =12V. ; Minimum Voltage =11V. ; Maximum Voltage =13 V.
2. Note: The frequency fundamental emissions stay within the authorized frequency block.



Appendix B. Test Results of Radiated Test

Radiated Spurious Emission

Test Engineer :	Herry Li	Temperature :	23~25°C
		Relative Humidity :	41~42%

n48 UL MIMO / NR 100MHz / QPSK / ANT0+1(NR)								
Channel	Frequency (MHz)	EIRP (dBm)	Limit (dBm)	Over Limit (dB)	S.G. Power (dBm)	TX Cable loss (dB)	TX Antenna Gain (dBi)	Polarization (H/V)
Middle	7158	-62.39	-40	-22.39	-73.85	2.84	14.30	H
	10740	-60.93	-40	-20.93	-70.87	3.49	13.43	H
	14322	-59.71	-40	-19.71	-69.95	3.85	14.09	H
	7158	-62.10	-40	-22.10	-73.56	2.84	14.30	V
	10740	-61.14	-40	-21.14	-71.08	3.49	13.43	V
	14322	-59.66	-40	-19.66	-69.90	3.85	14.09	V

Remark: Spurious emissions within 30-1000MHz were found more than 20dB below limit line.