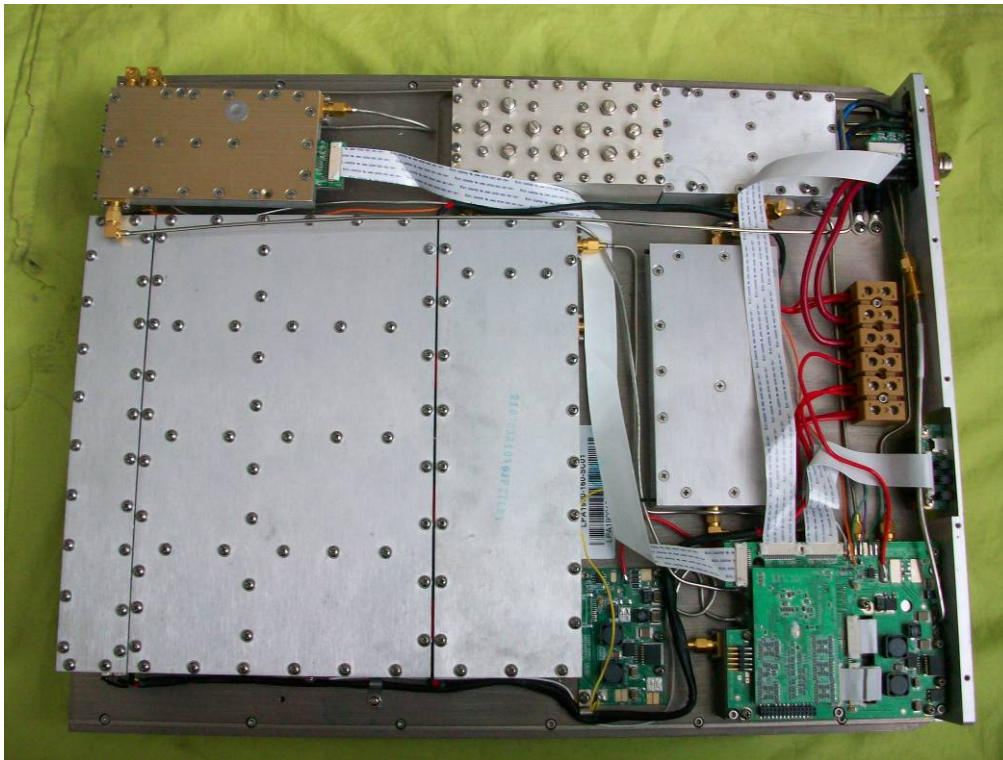
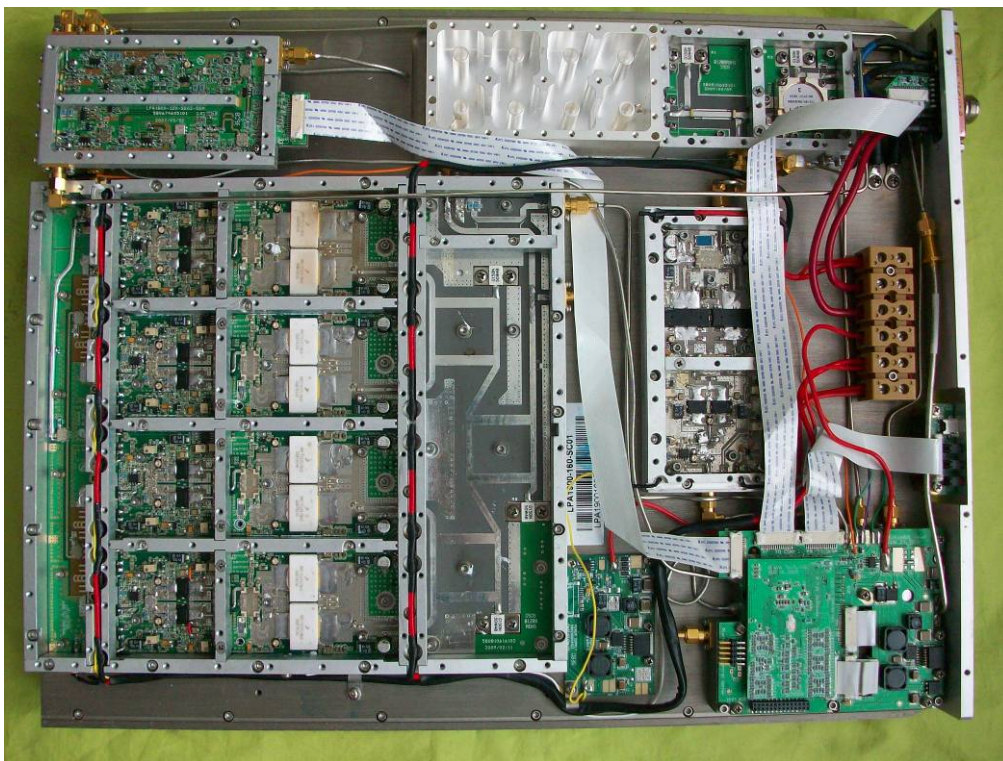


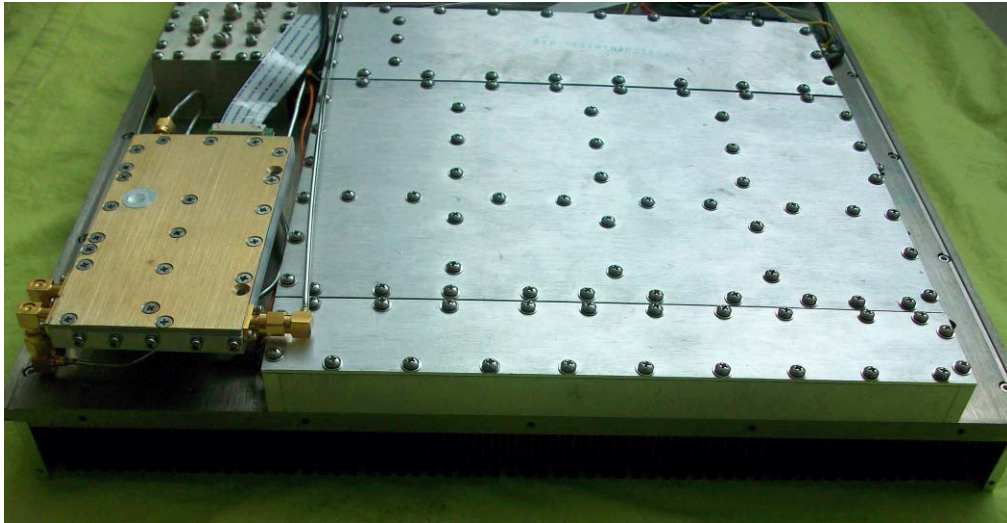
APPENDIX 3 INTERNAL PHOTOGRAPHS OF EUT



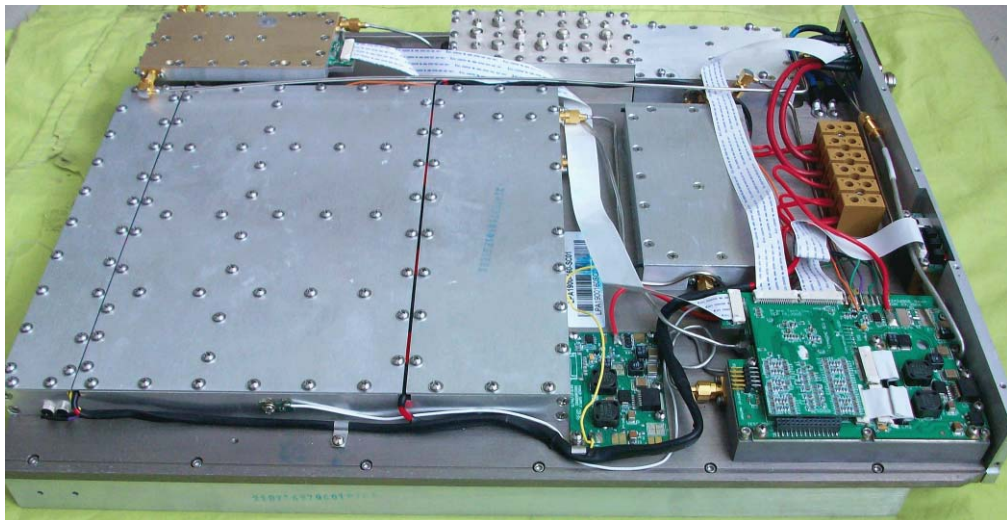
TOP VIEW WITHOUT OUTER COVER (COVER_1)



TOP VIEW WITHOUT INTERNAL COVERS (COVER_2s)



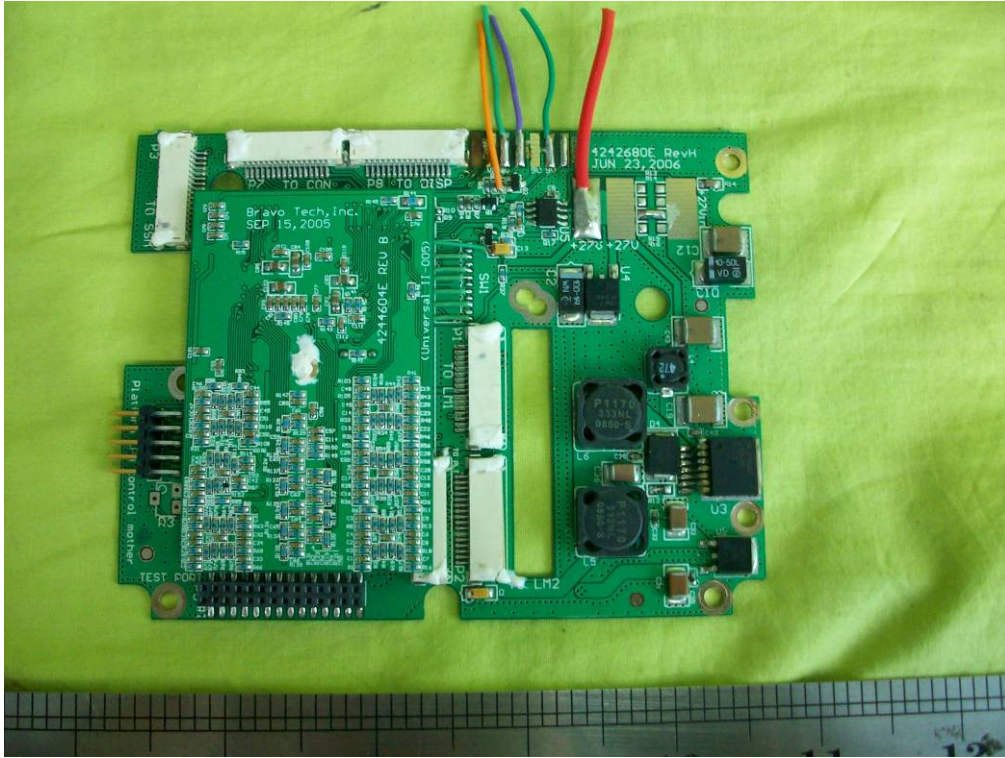
SIDE VIEW WITHOUT COVER_1 -1



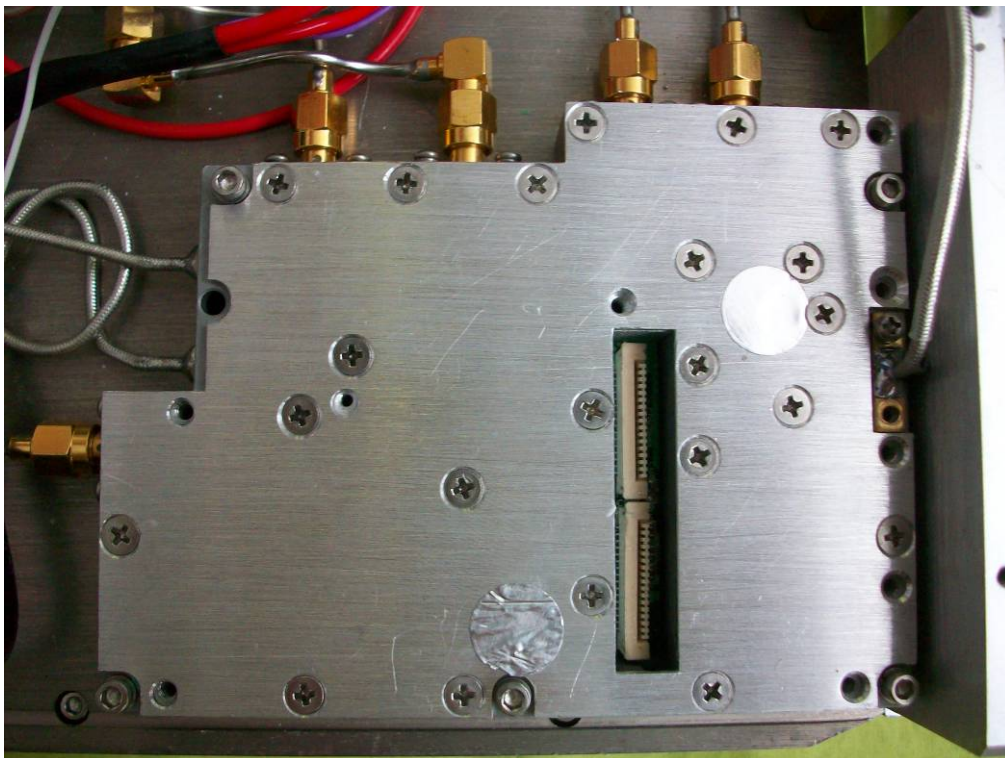
SIDE VIEW WITHOUT COVER_1 -2



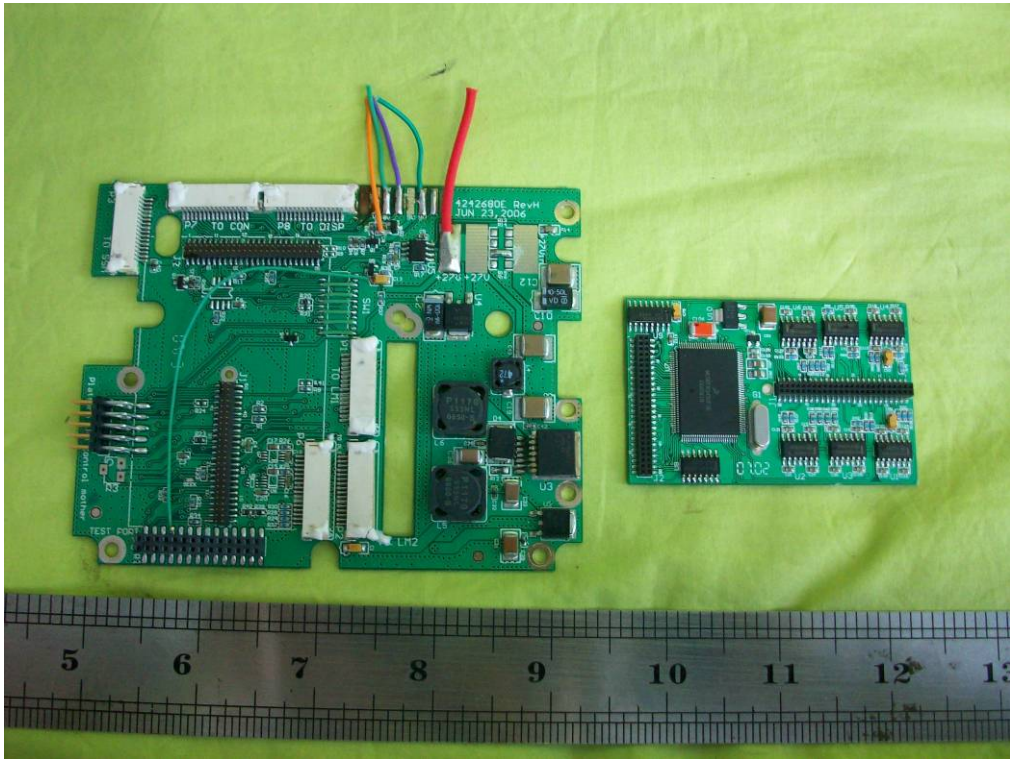
SIDE VIEW WITHOUT COVER_1 -3



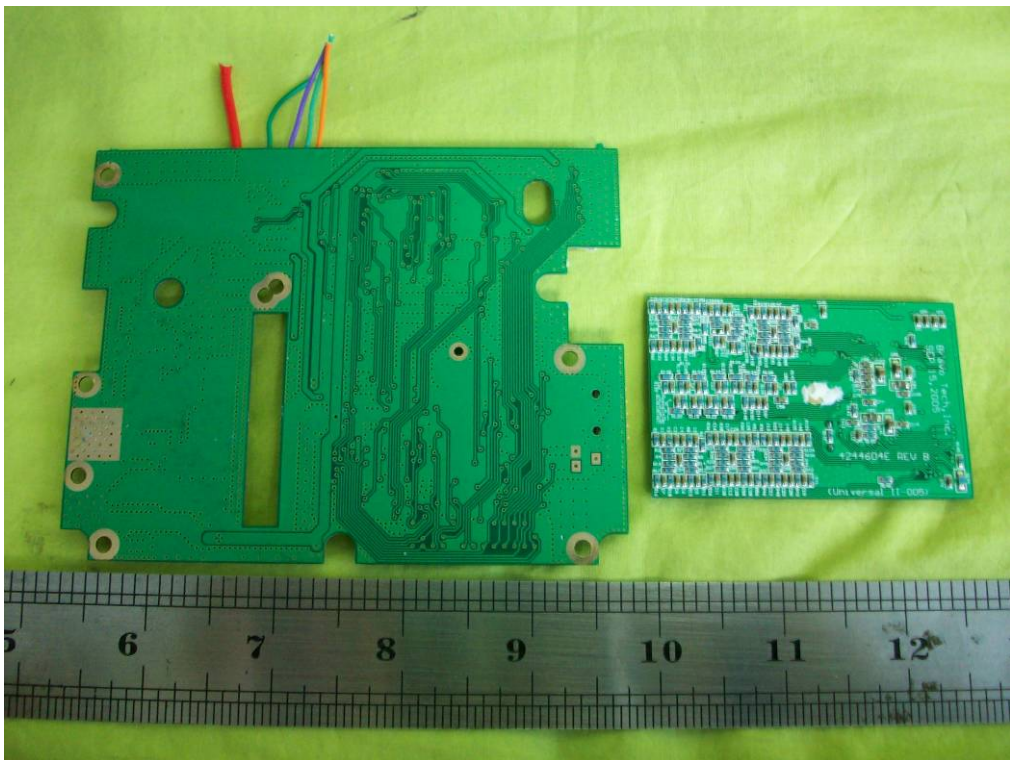
TOP VIEW OF MODULE (MODULE_1)



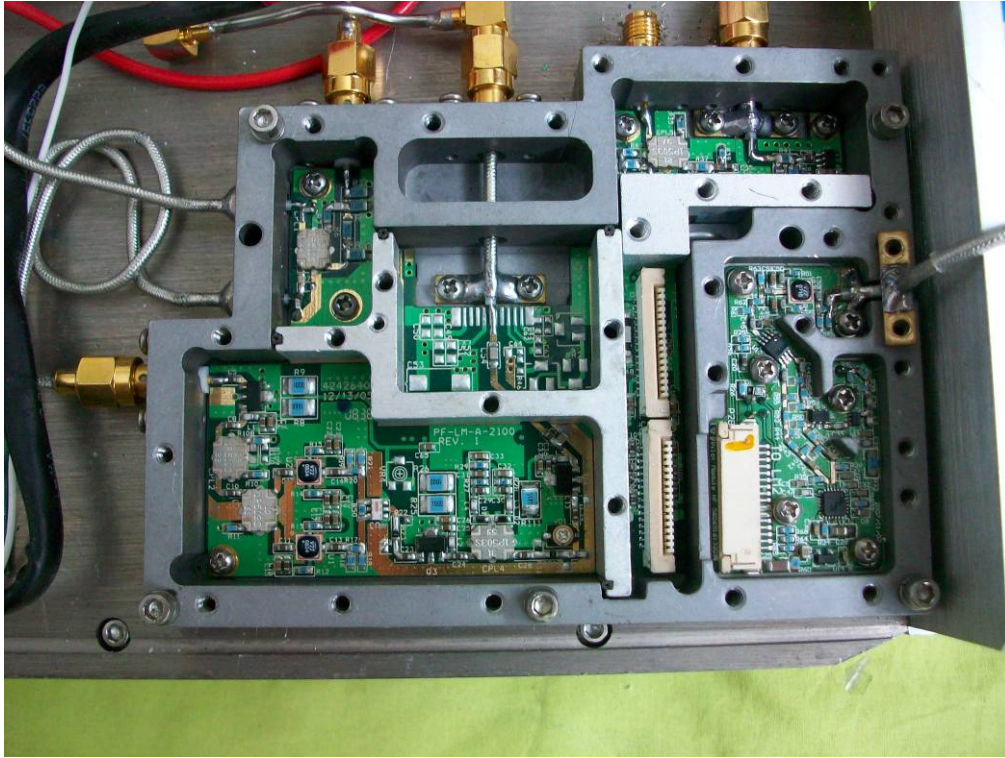
TOP VIEW OF COVER (COVER_3) WITHOUT MODULE_1



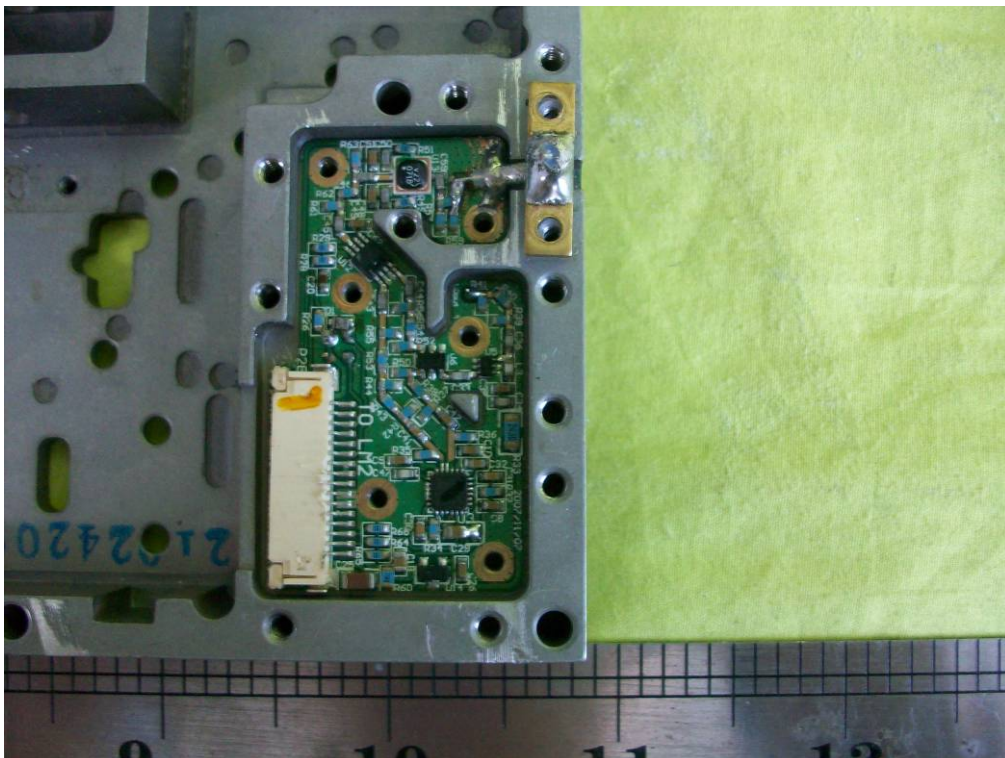
TOP VIEW OF PARTS OF MODULE_1



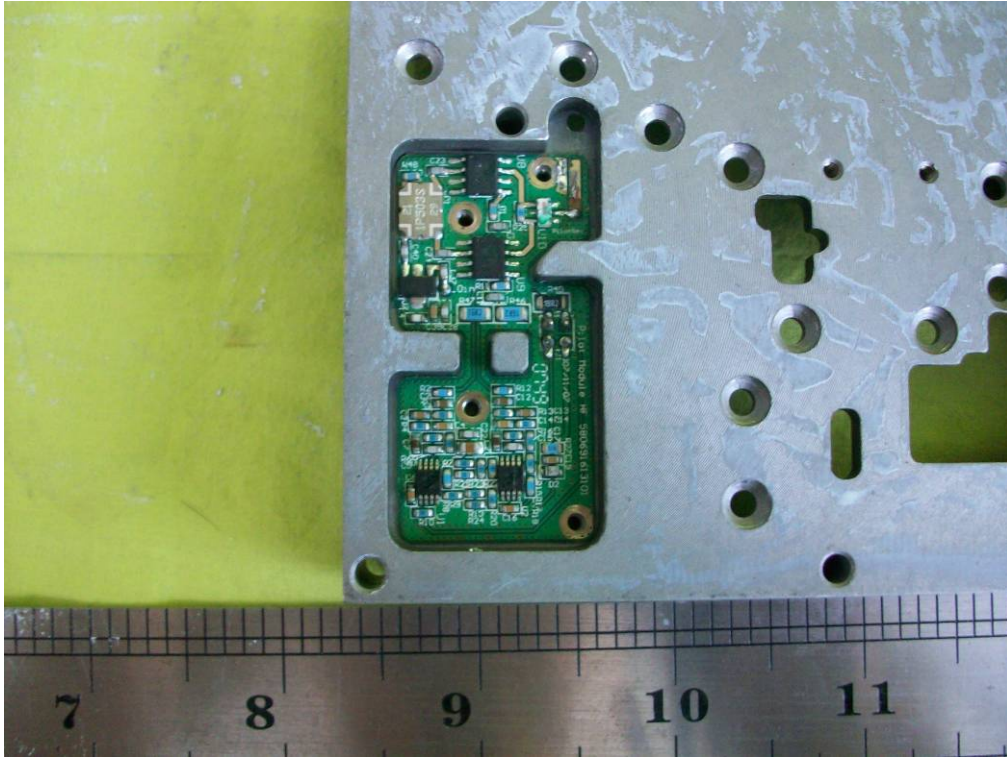
BOTTOM VIEW OF PARTS OF MODULE_1



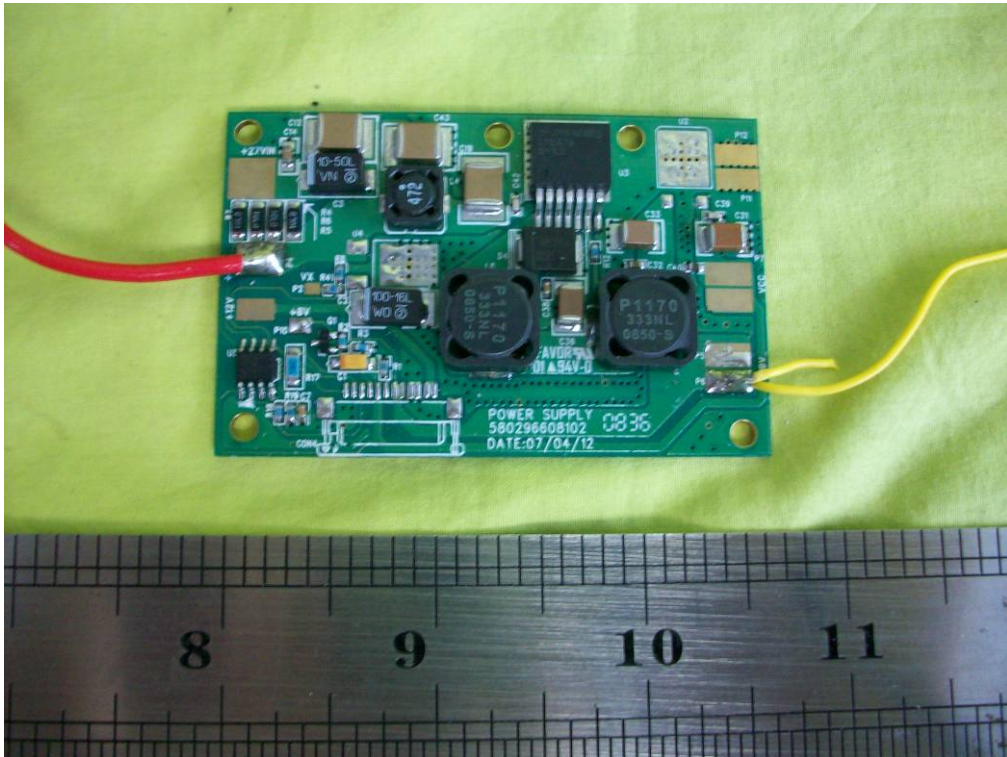
TOP VIEW OF MODULE (MODULE_2) WITHOUT COVER_3



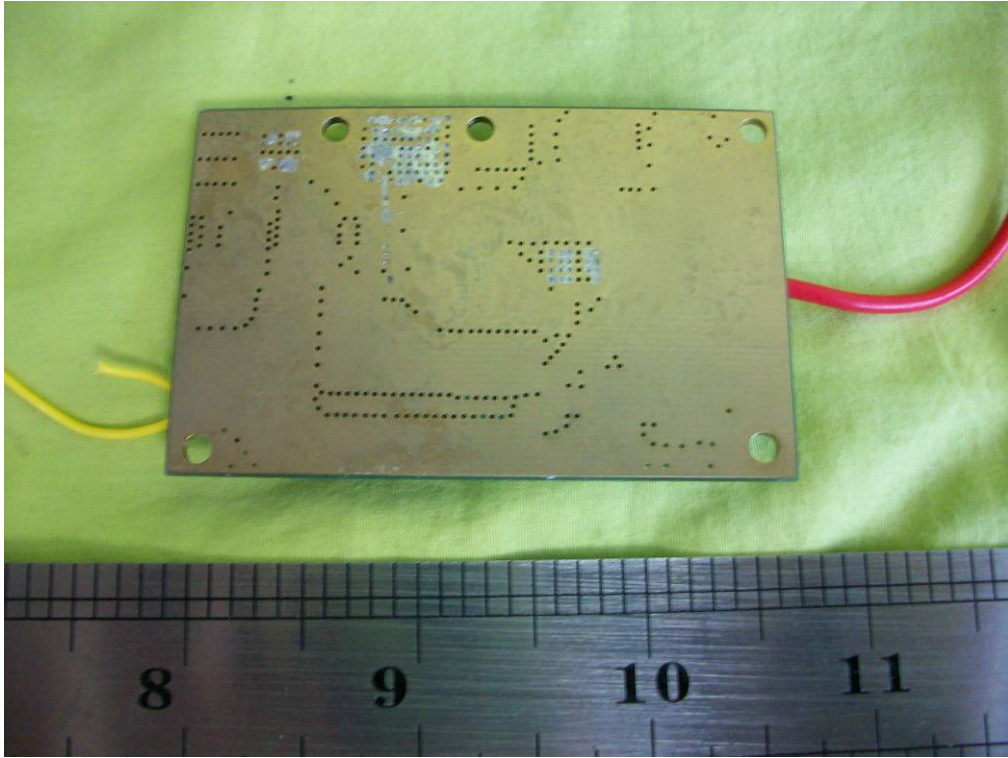
TOP VIEW OF PART (MODULE_2_2) OF MODULE_2



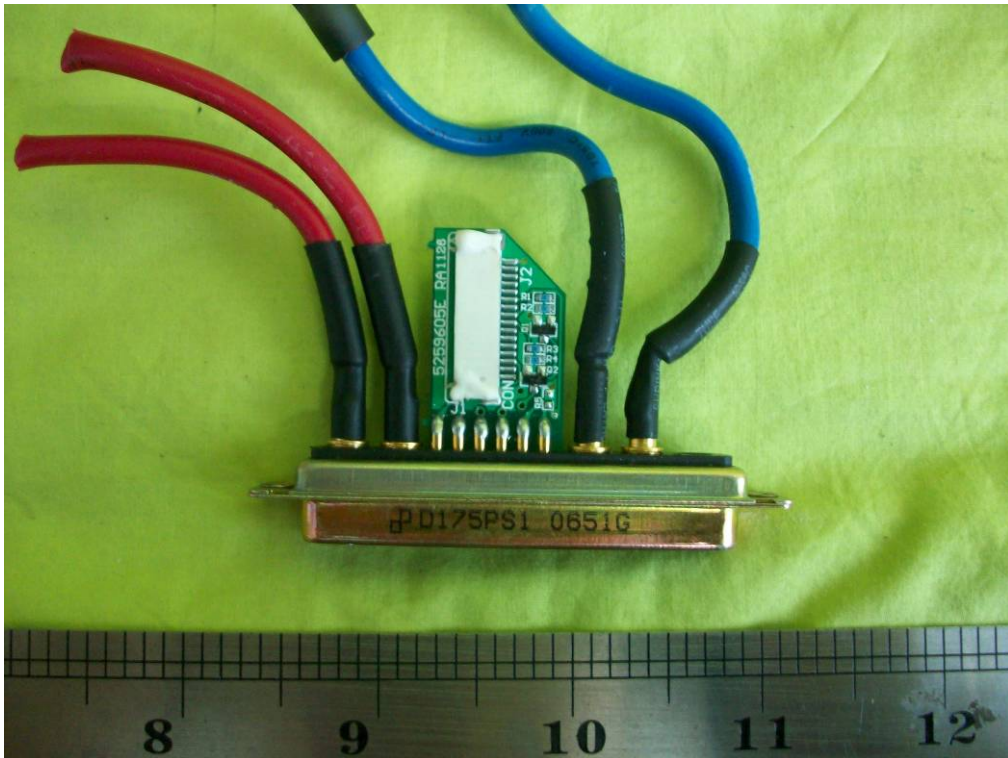
BOTTOM VIEW OF MODULE_2_2 OF MODULE_2
NOTE: MODUL_2_2 includes two PCBs installed together



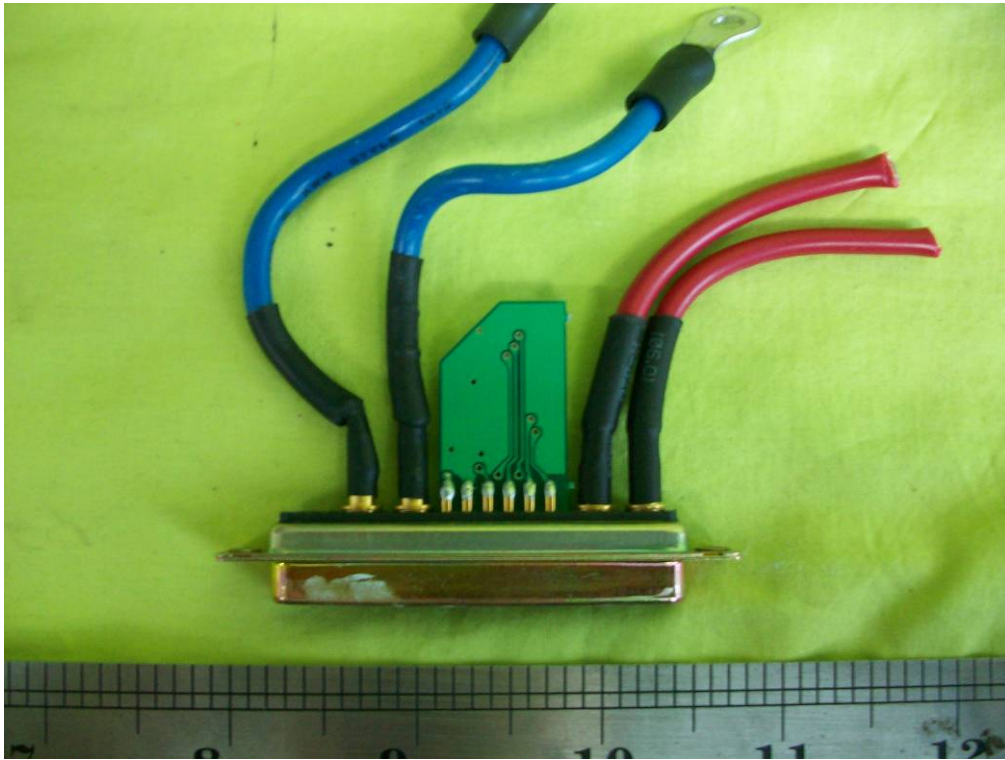
TOP VIEW OF MODULE (MODULE_3)



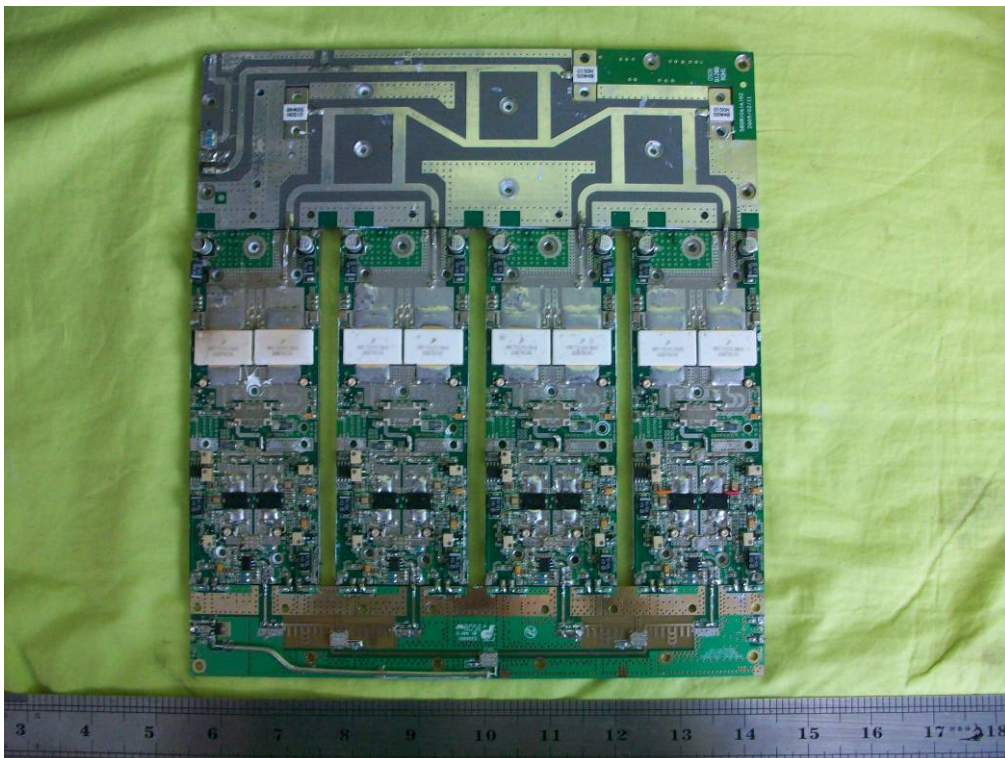
BOTTOM VIEW OF MODULE_3



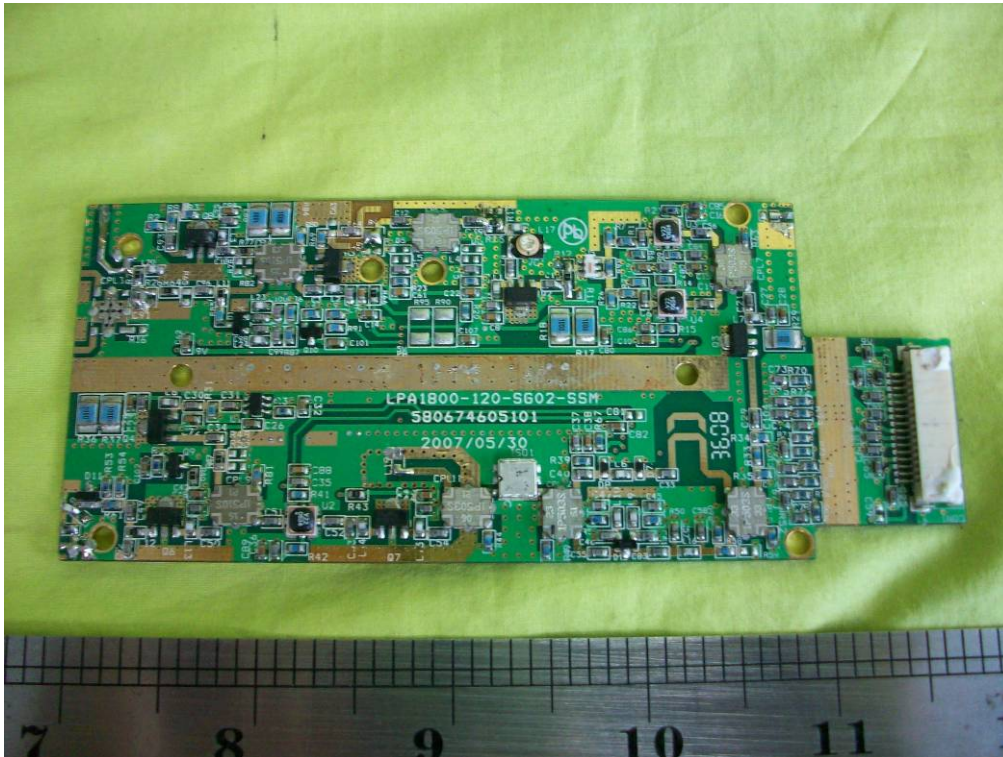
TOP VIEW OF MODULE (MODUL_4)



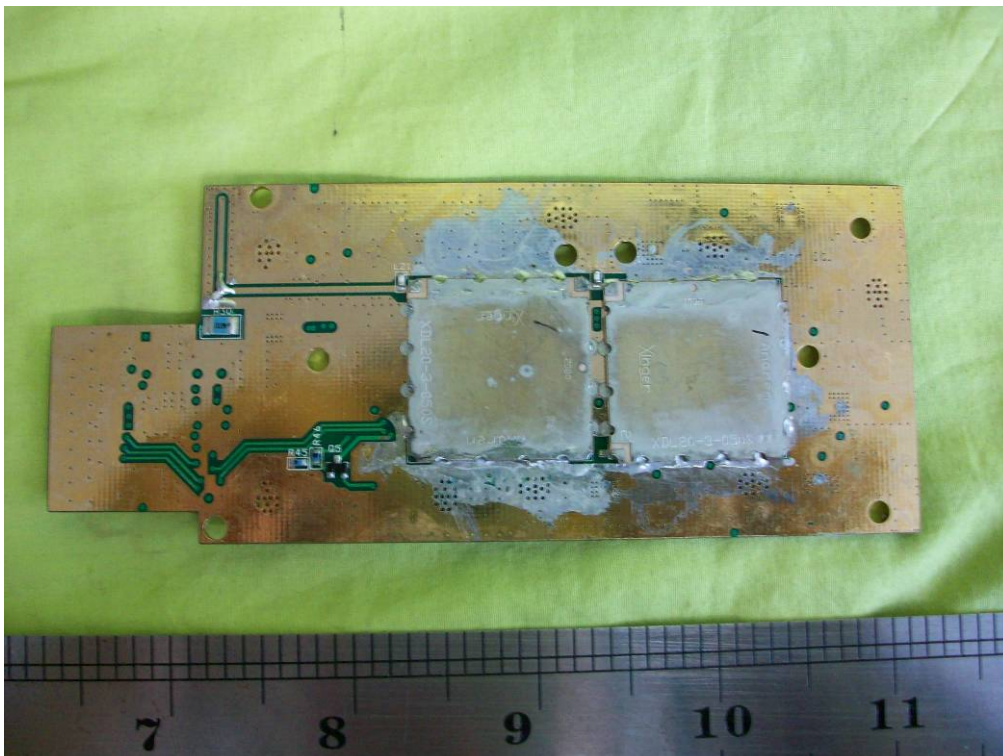
BOTTOM VIEW OF MODUL_4



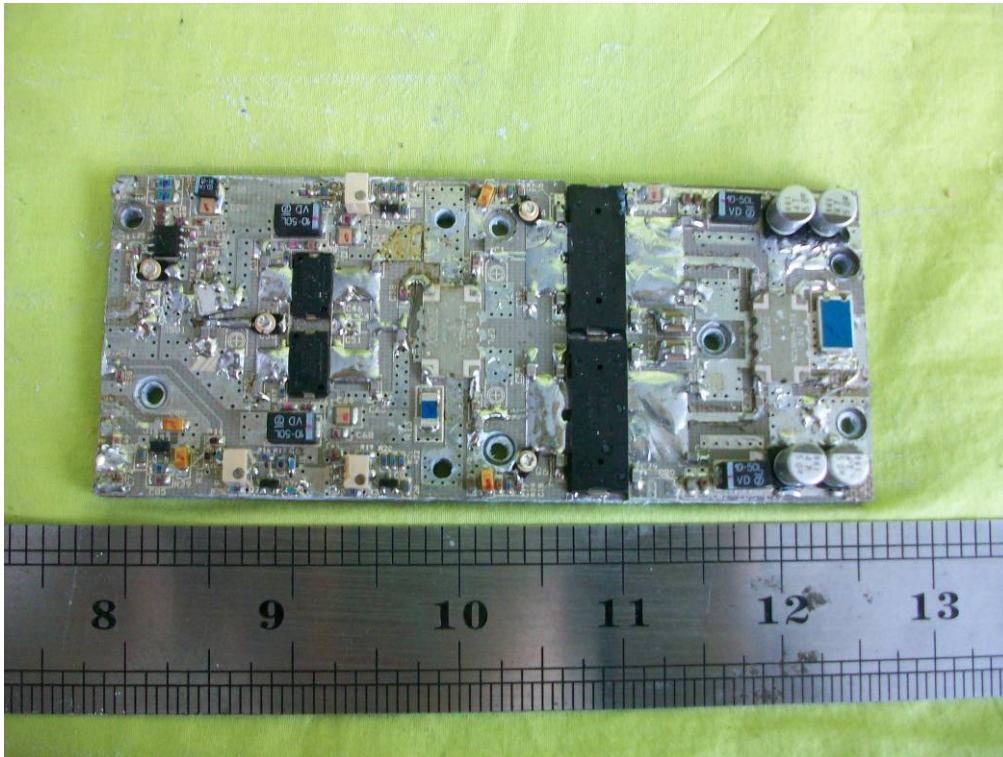
TOP VIEW OF MODULE (MODUL_5)



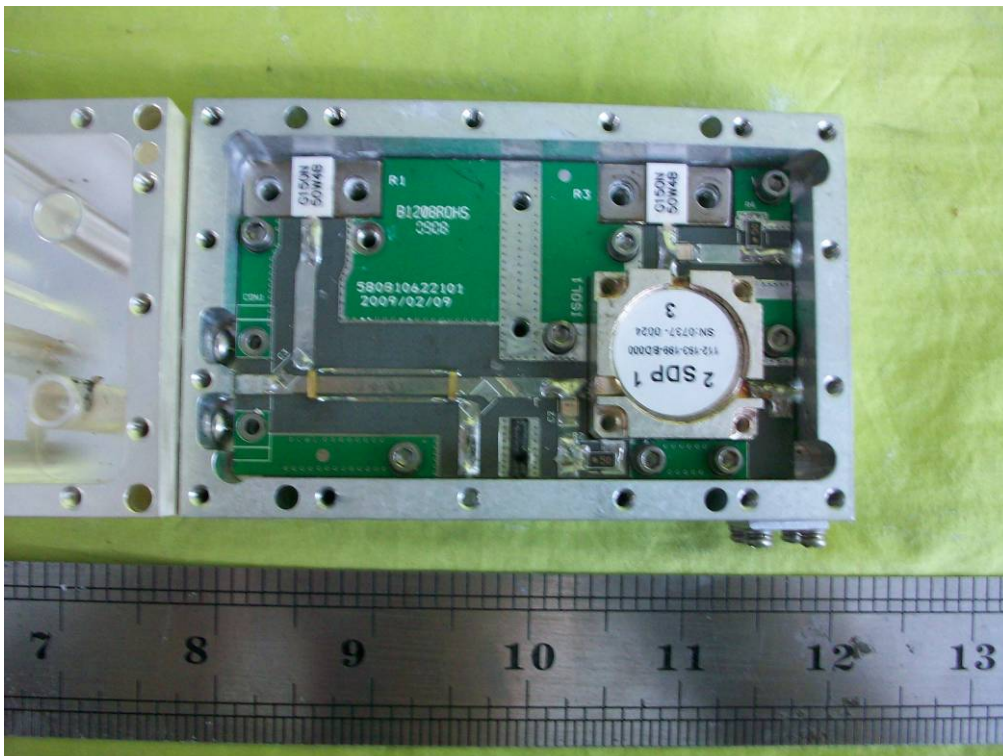
TOP VIEW OF MODULE (MODUL_6)



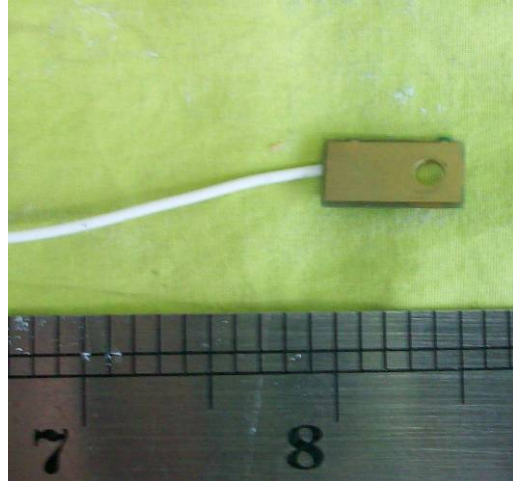
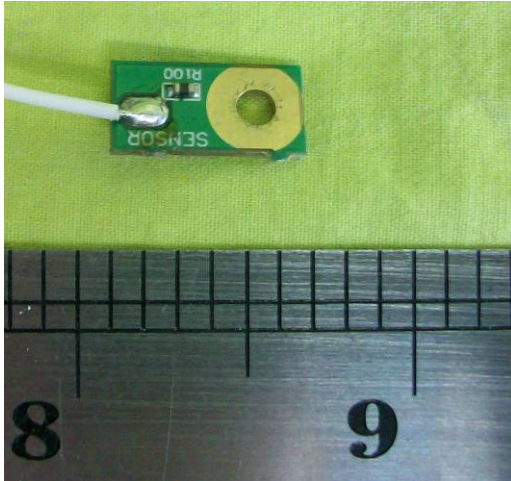
BOTTOM VIEW OF MODUL_6



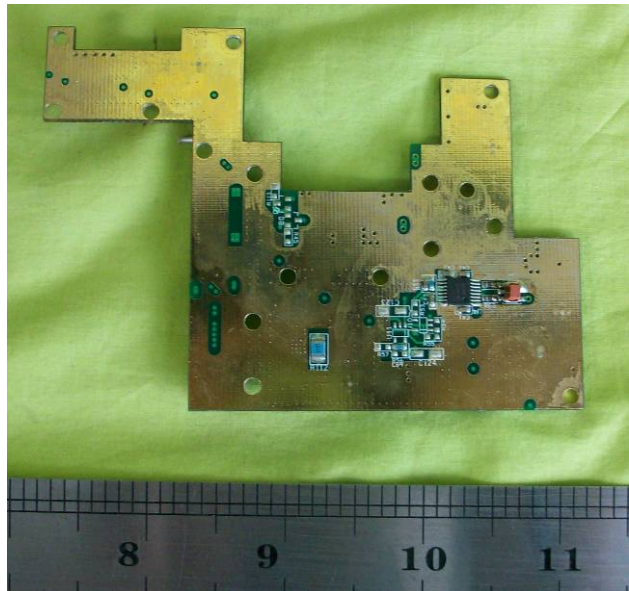
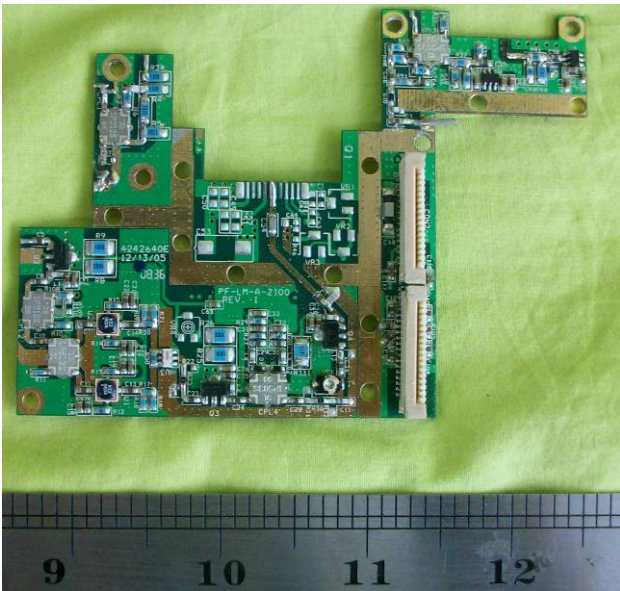
TOP VIEW OF MODULE (MODUL_7)



TOP VIEW OF MODULE (MODUL_8)



TOP & BOTTOM VIEW OF MODULE (MODUL_9)



TOP & BOTTOM VIEW OF PART (MODULE_2_1) OF MODULE_2