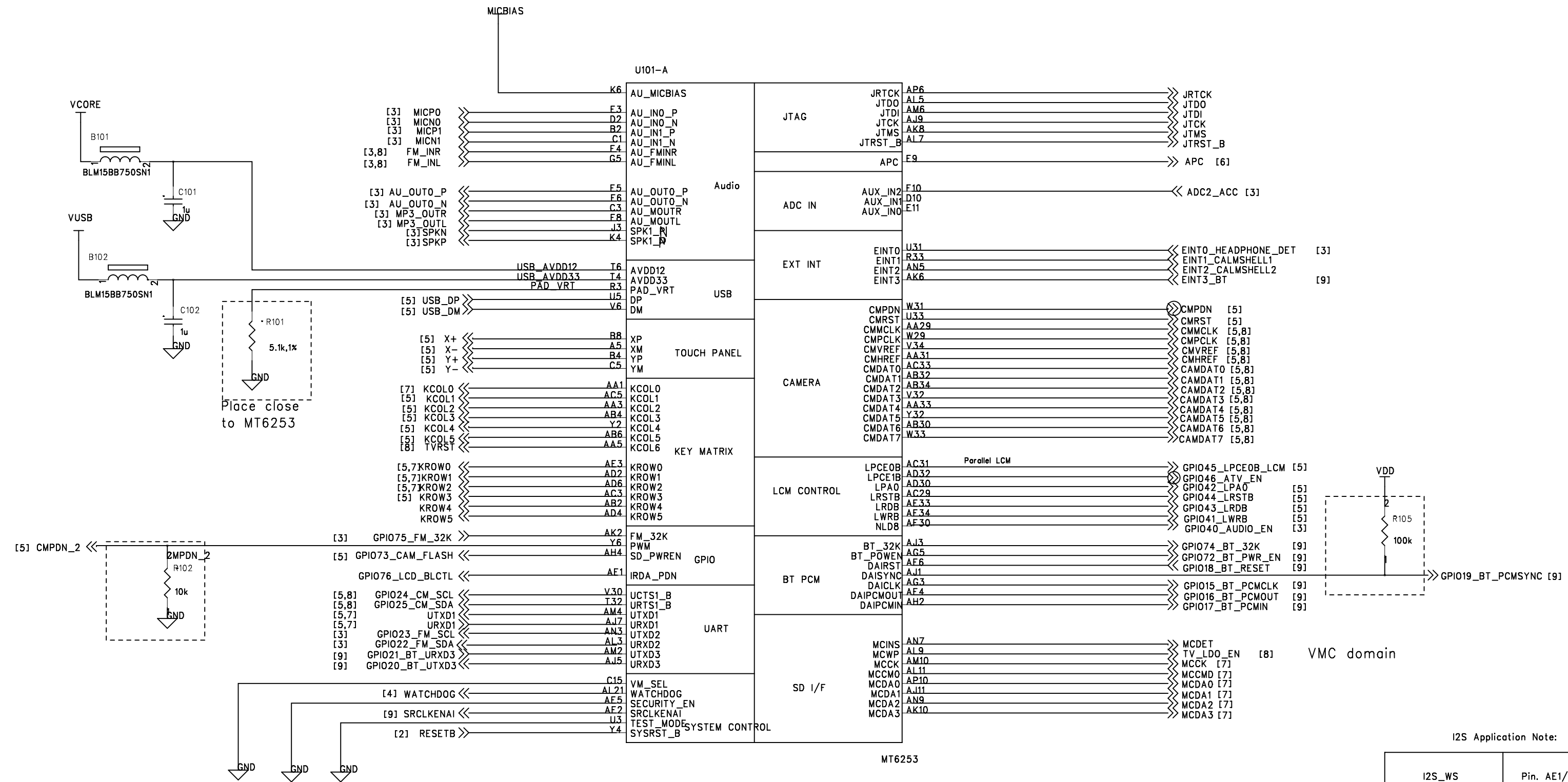


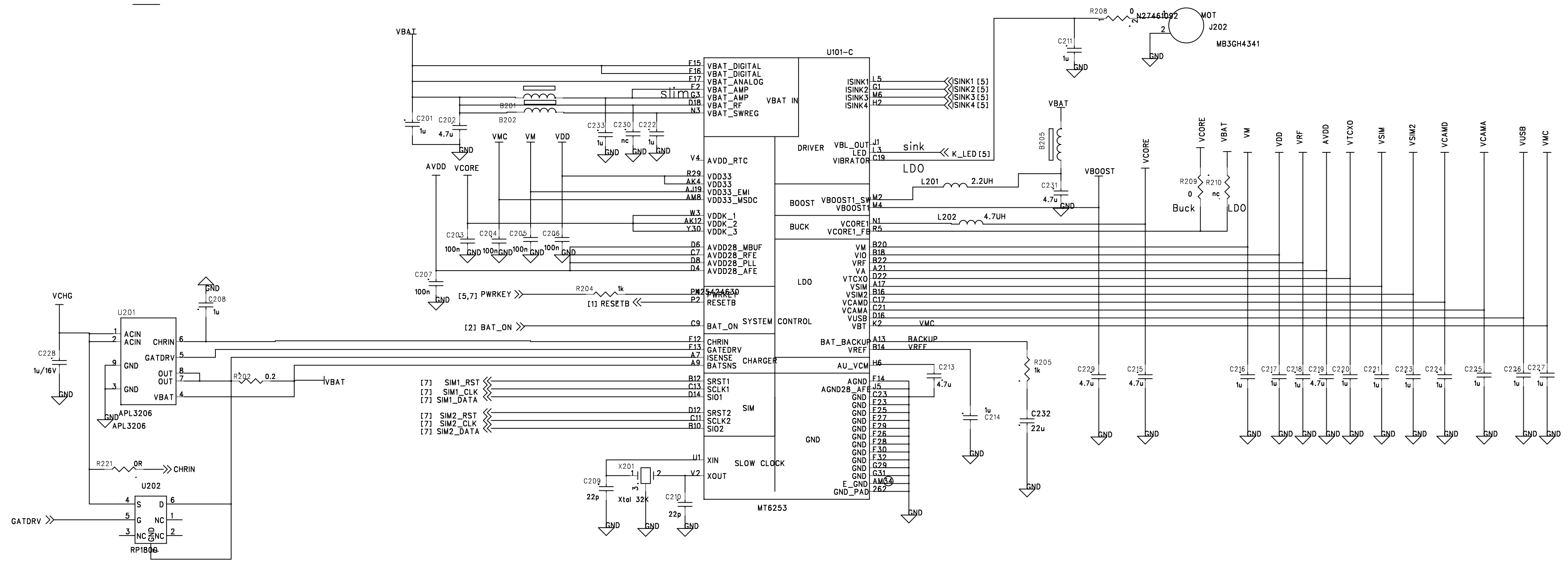
# BASEBAND



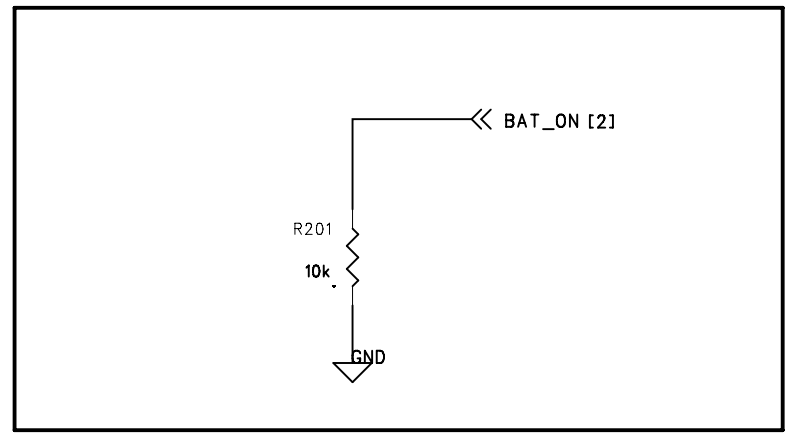
I2S Application Note:

I2S_WS	Pin. AE1/IRDA_PDN
I2S_DATA	Pin AN3UTXD2
I2S_CLK	Pin AF30/NLDB

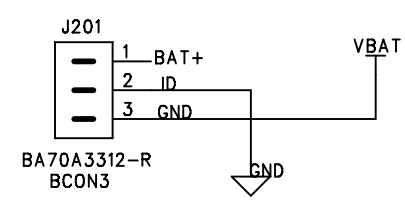
# BB\_PMU



Check,Trace 4mil is enough

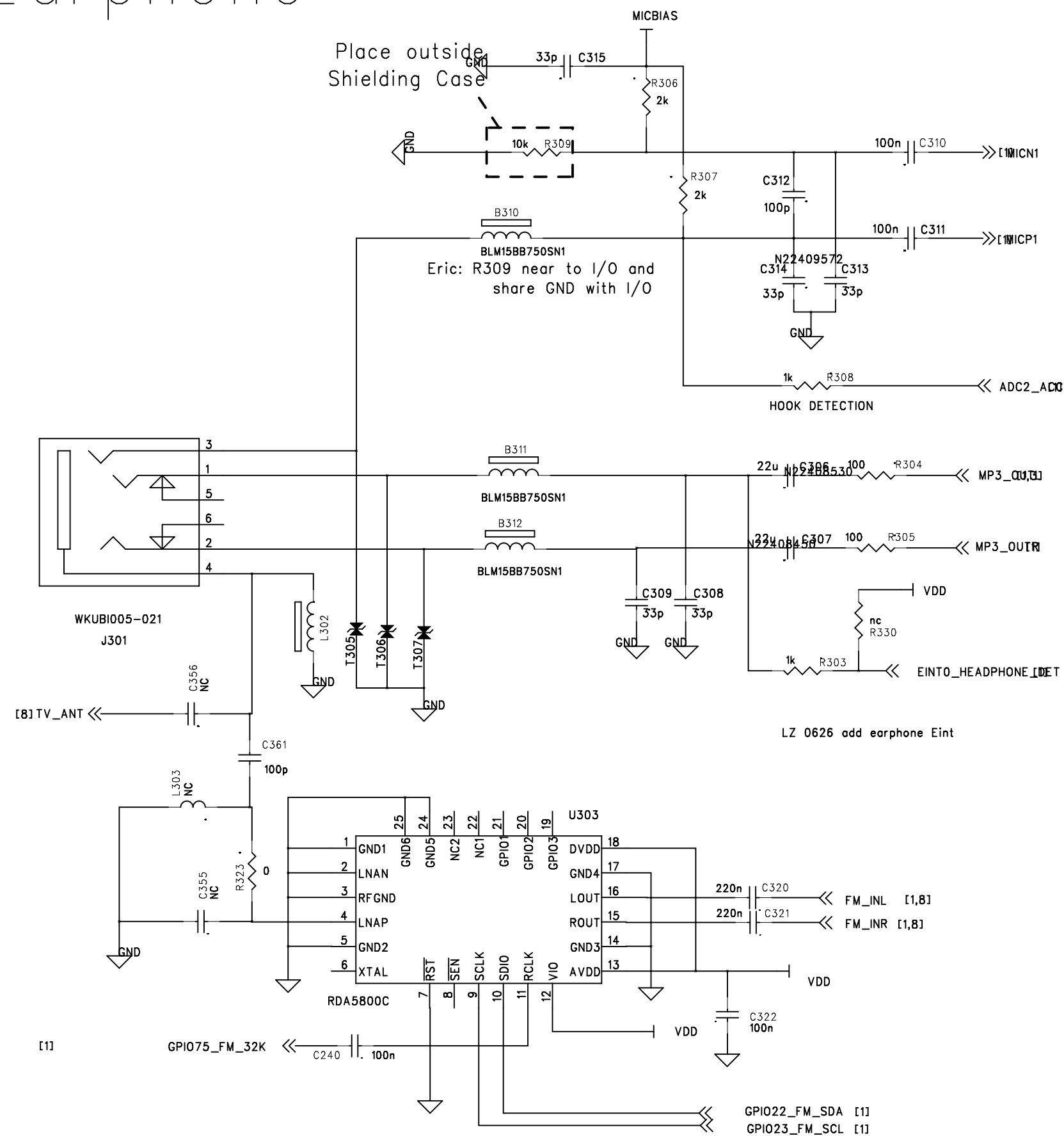


Eric: NTC Pin Floating, So Bat-on Pull to GND

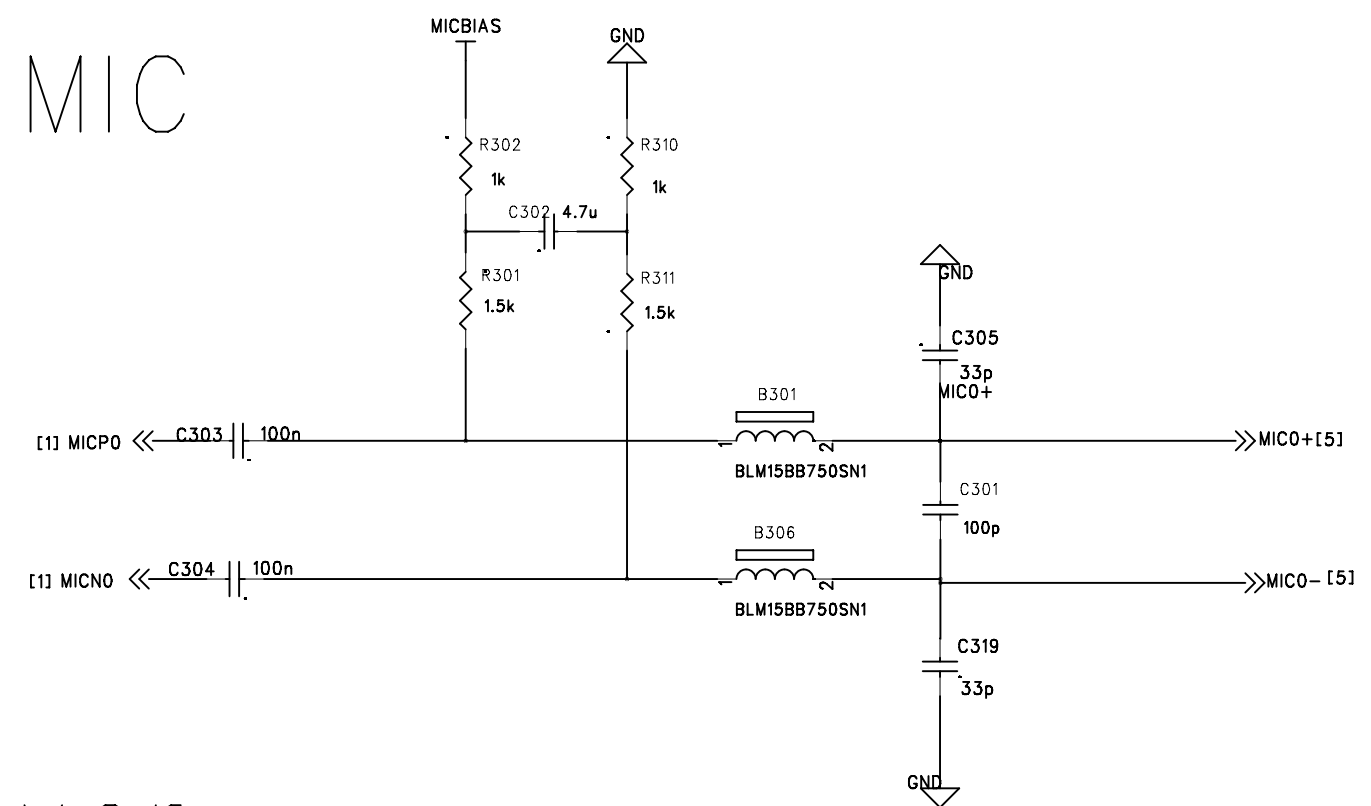


Symbol	Vout (V)	Iout (mA)	Setting
Buck	VCORE 1.2	200	
RF LDO	VRF 2.8	200	
Analog LDO	VTCXO 2.8	20	
	VA 2.8	125	
	VCAM_A 1.5/1.8/2.5/2.8	150	
Digital LDO	VM 1.8/2.8	200	VM_SEL
	VIO 2.8	100	
	VSIM 1.8/3.0	100	
	VUSB 3.3	100	
	VBT 1.3/1.5/1.8/2.5/2.8/3.0/3.3	100	
	VCAM_D 1.3/1.5/1.8/2.5/2.8/3.0/3.3	100	
	VSIM2 1.3/1.5/1.8/2.5/2.8/3.0/3.3	100	
Vibrator	VIBR 1.8/3.0	200	
RTC	VBACKUP 2.8	2	
Boost Converter	VBOOST1 3.4~5.5	100	

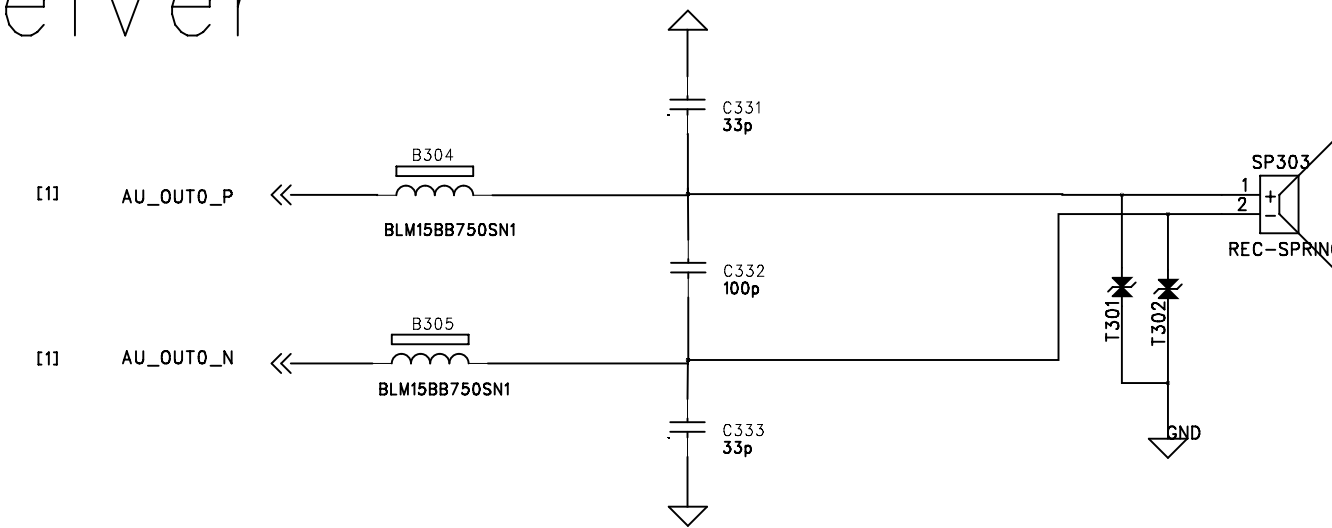
# Earphone



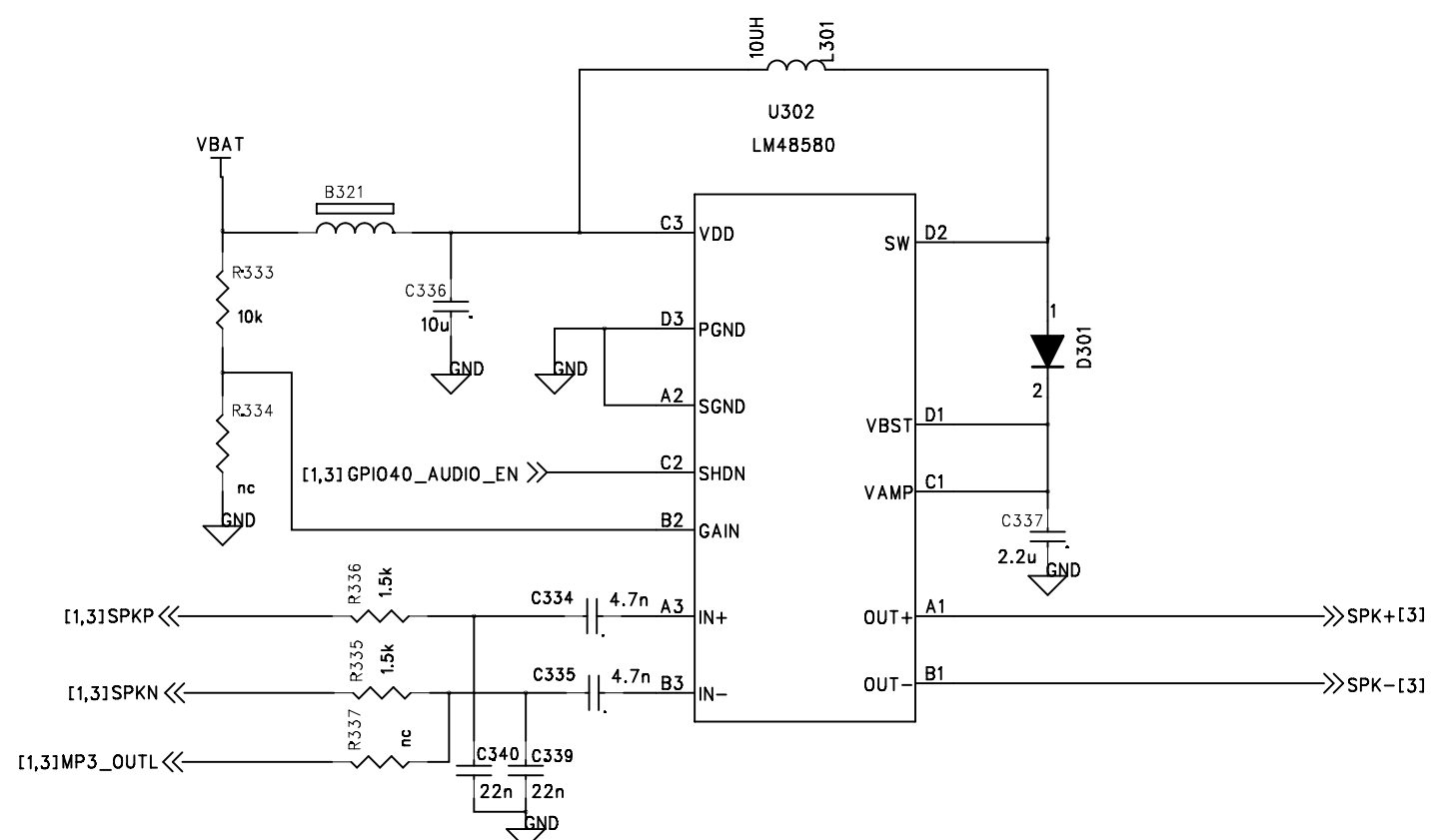
# MAIN MIC



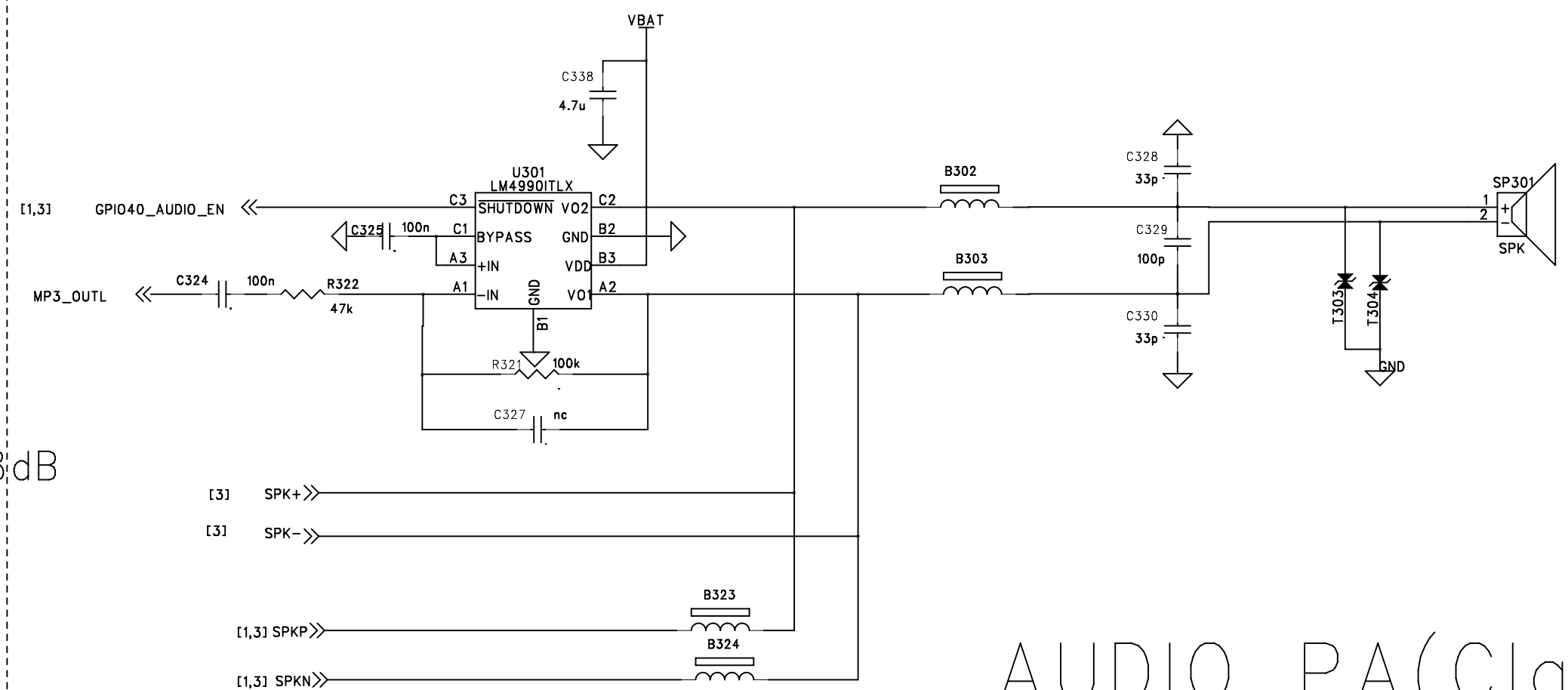
# Receiver



# AUDIO PA(Class G)

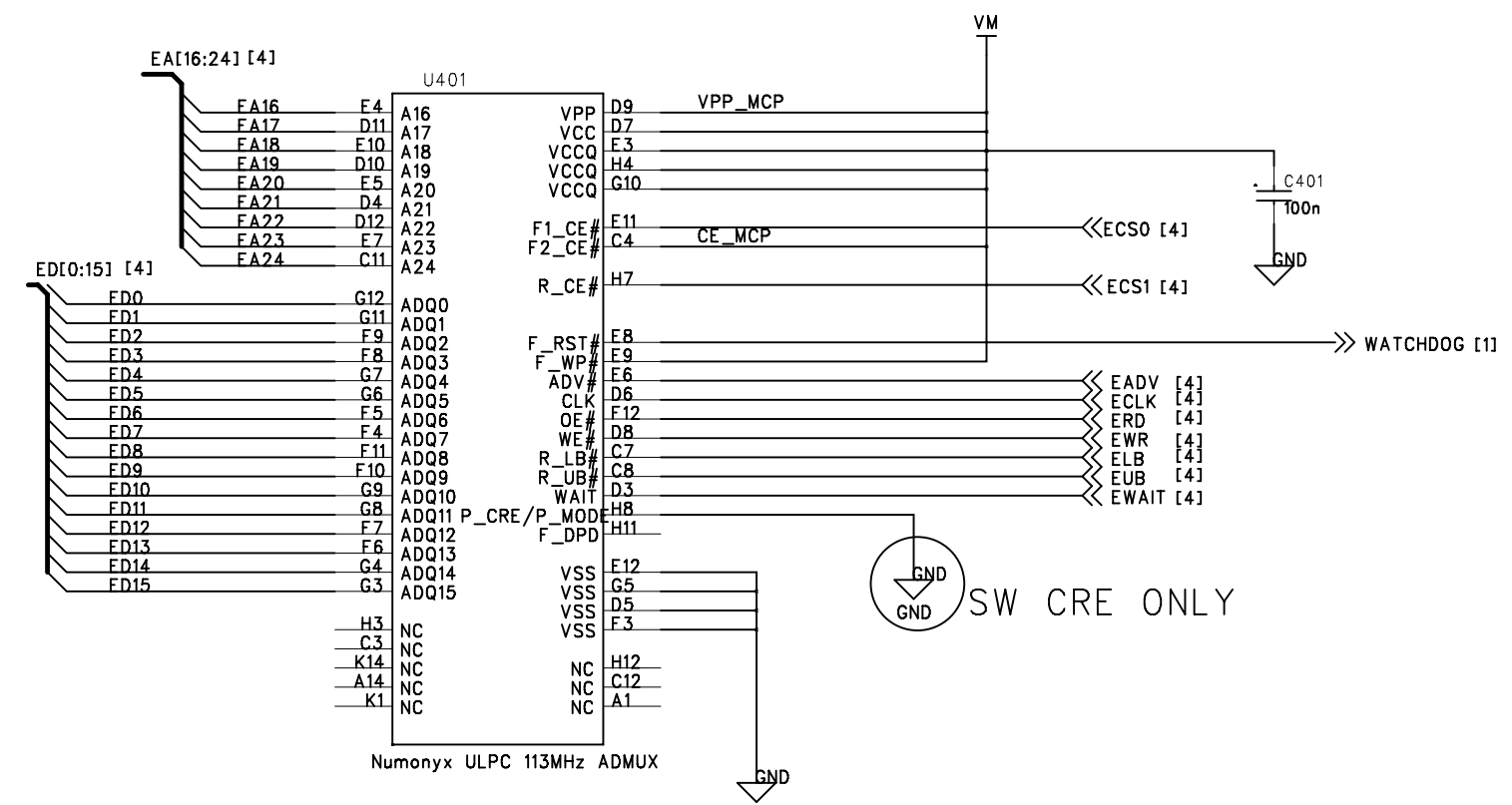
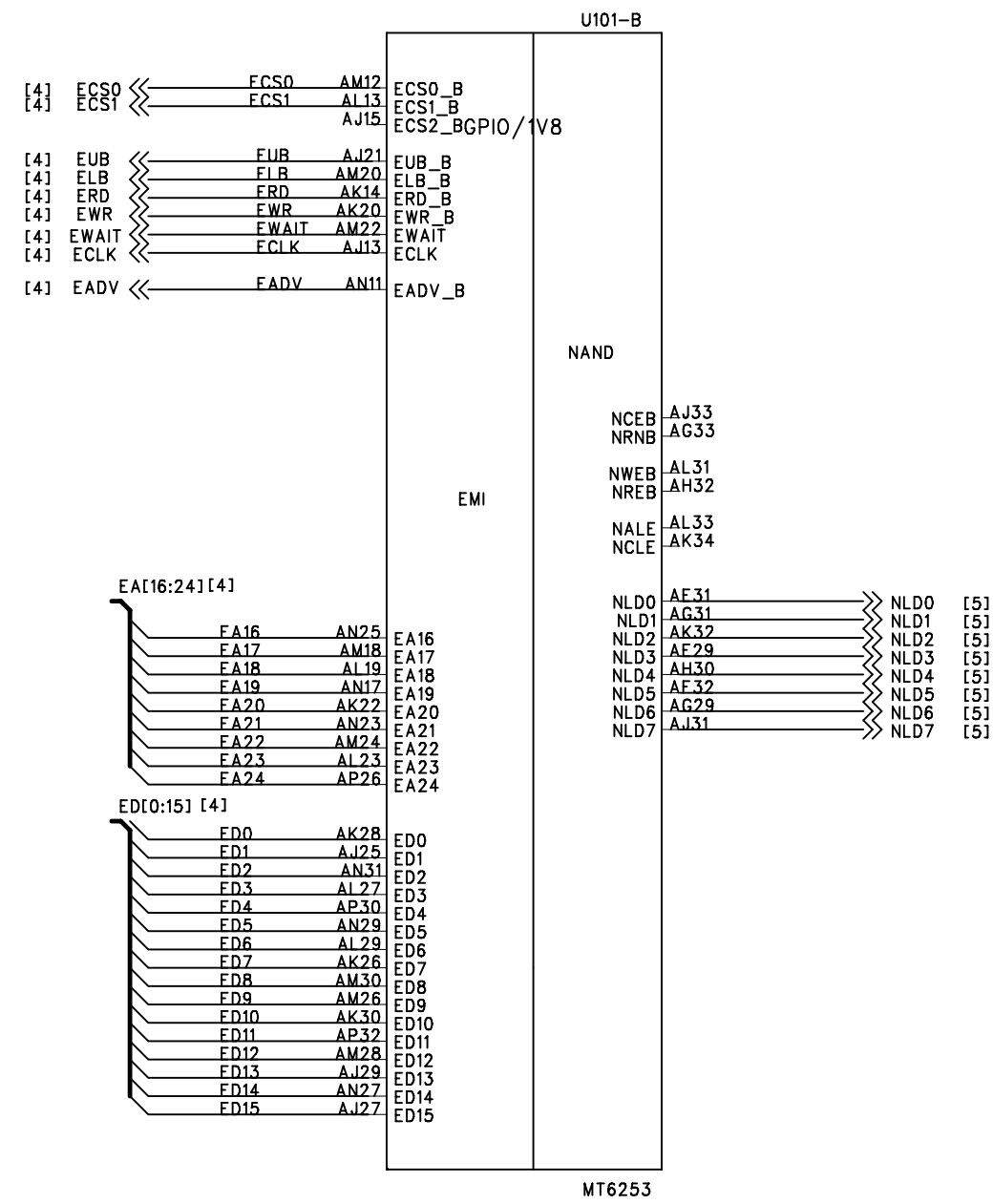


Gain=floating: 18dB  
 Gain=GND: 24dB  
 Gain=VDD: 30dB



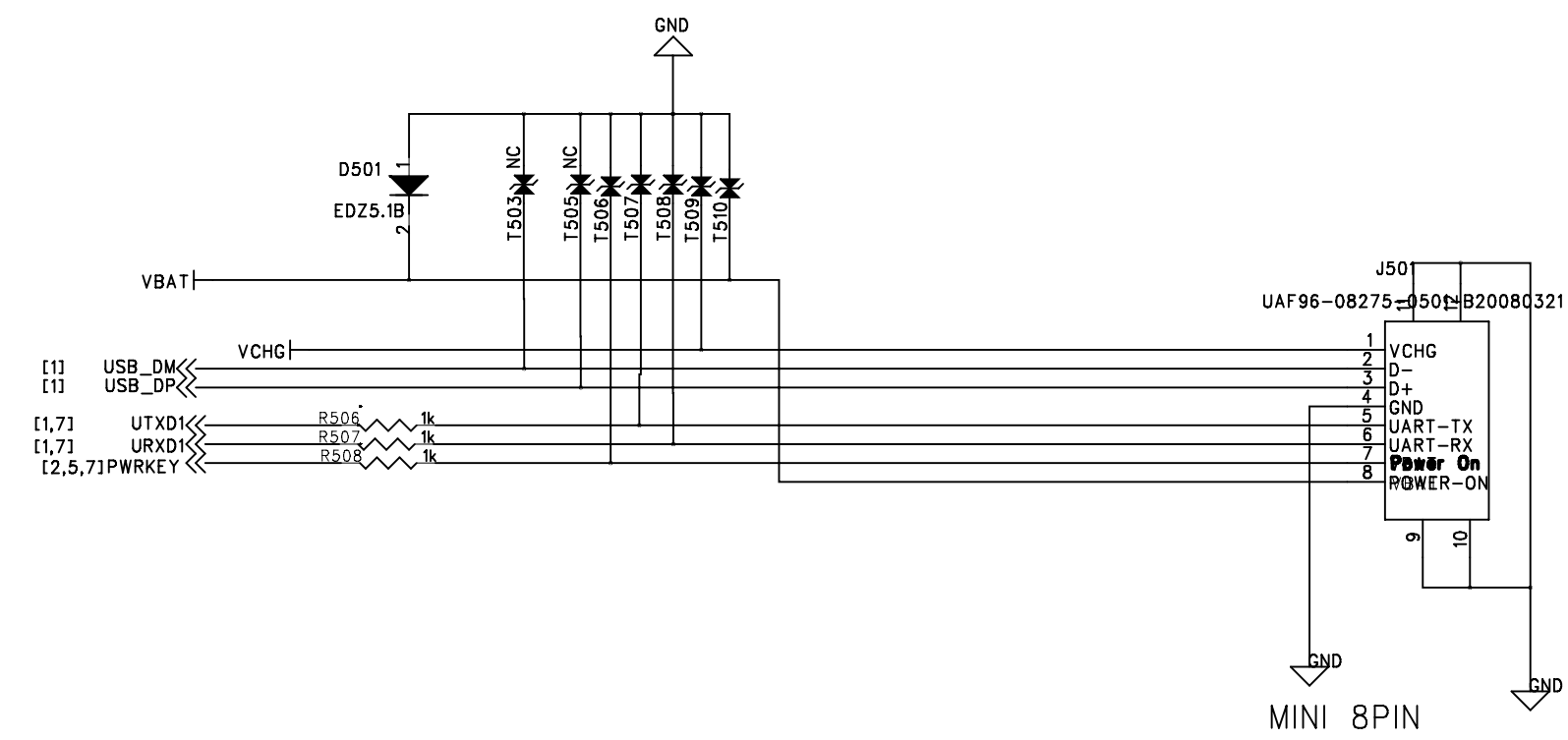
# AUDIO PA(Class D)

BASEBAND-MCP INTERFACE



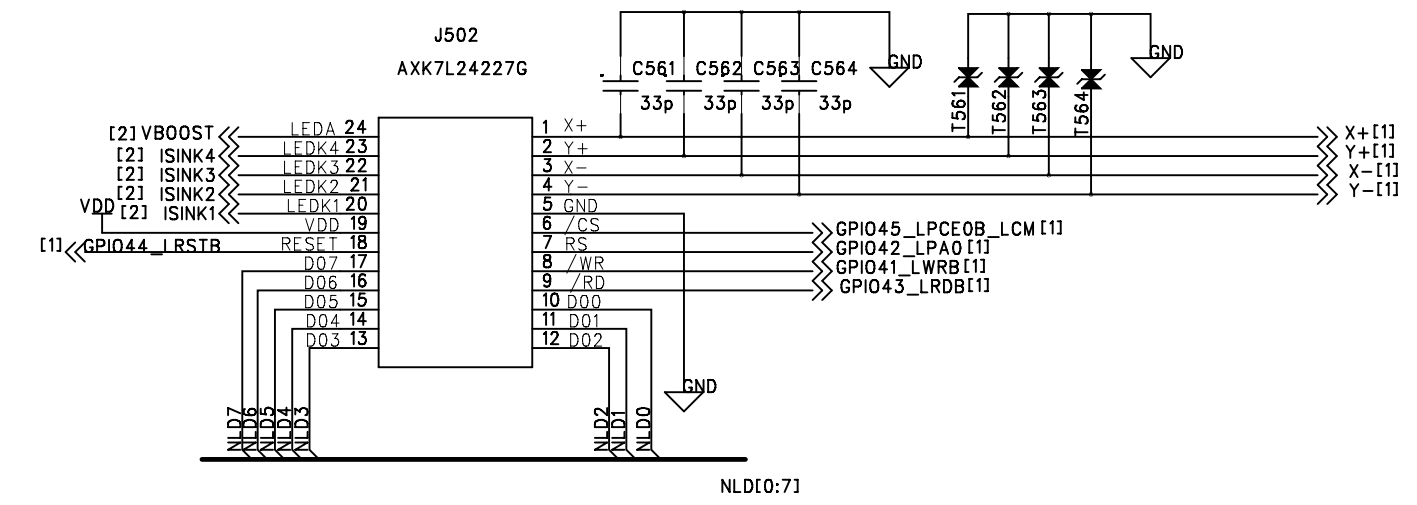
Numonyx ULPC 113MHz ADMUX	6.2*7.7	113MHz
Toshiba 108MHz ADMUX	7.1*10.1	108MHz
Numonyx ULPC	8*8	104MHz
Samsung ULPC 113MHz ADMUX	8*9.2	108MHz
Samsung 108MHz ADMUX	6*6	108MHz

# I/O



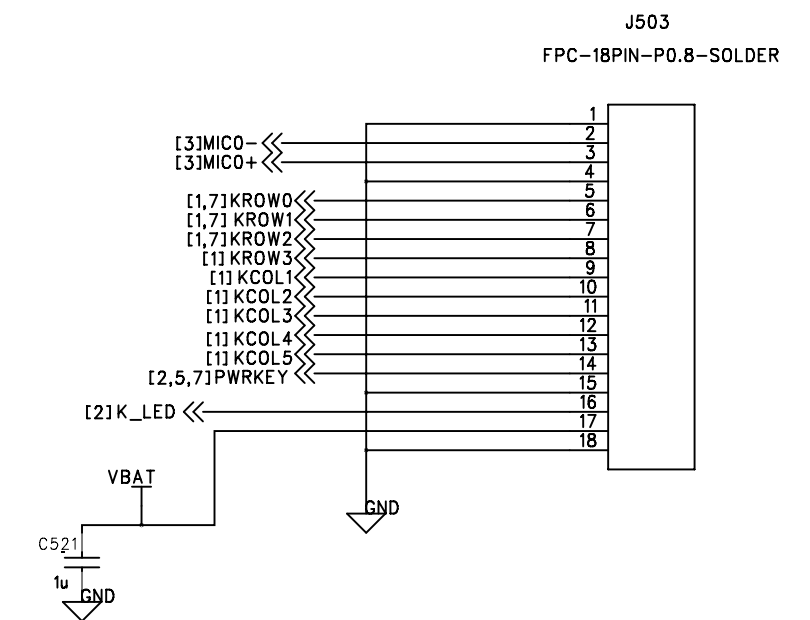
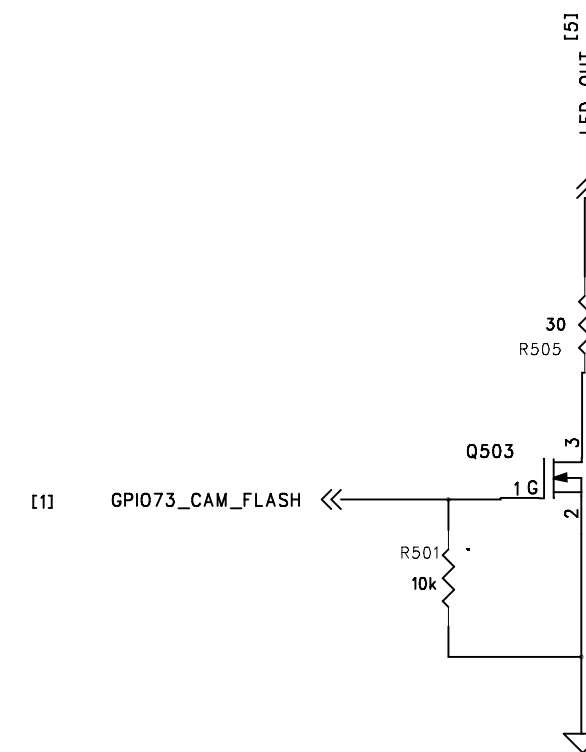
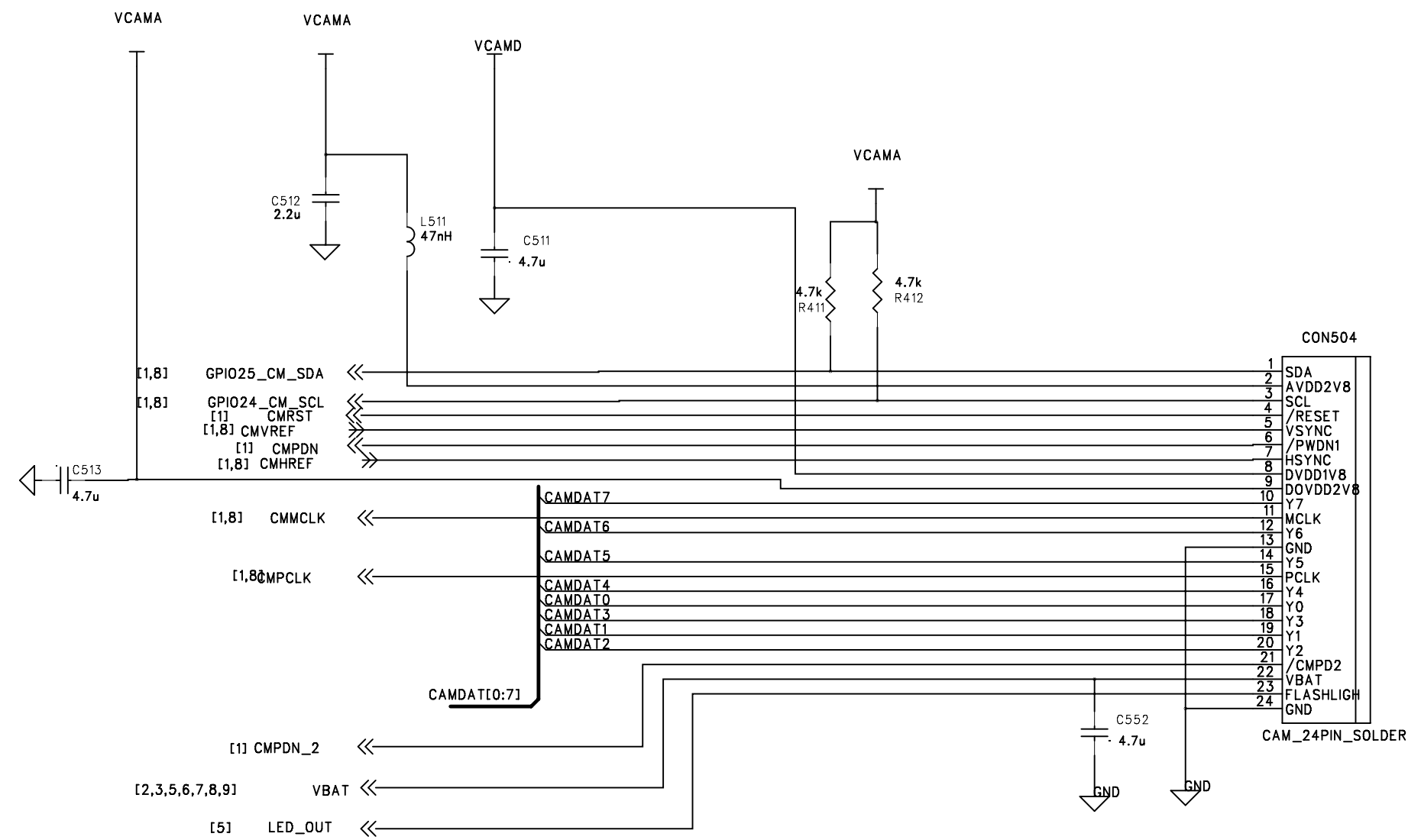
Eric: Add Zenar Diode to protect BT/BB Vbat input from I/O

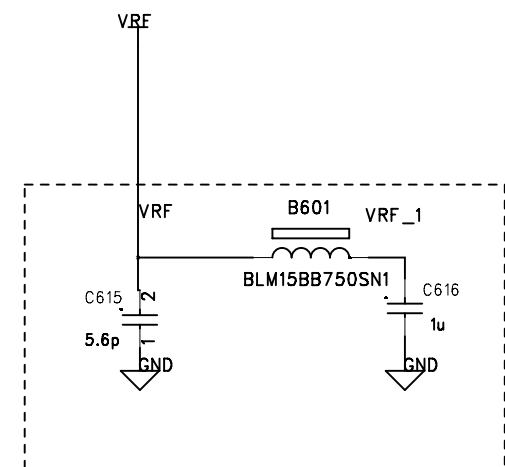
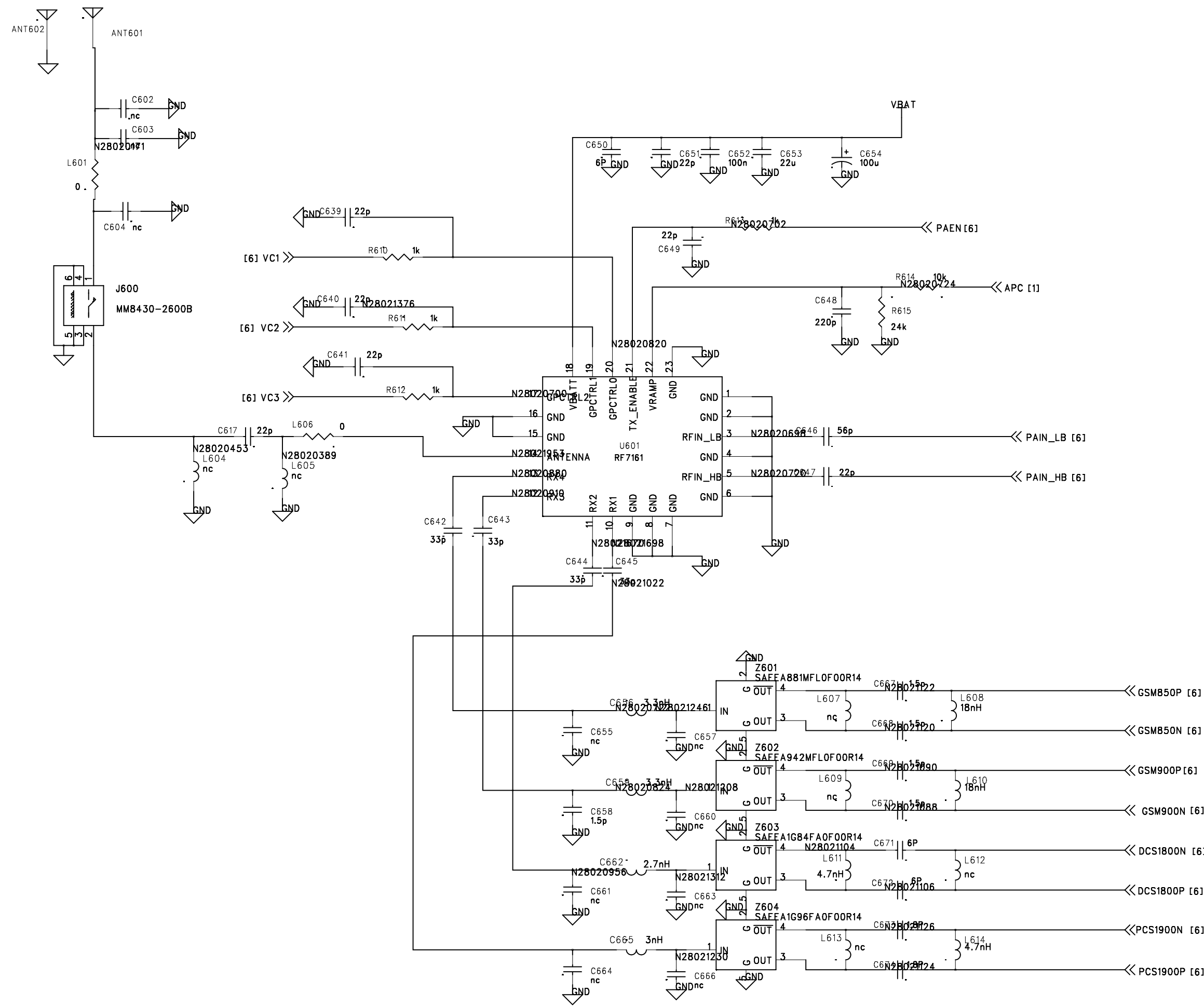
# LCD



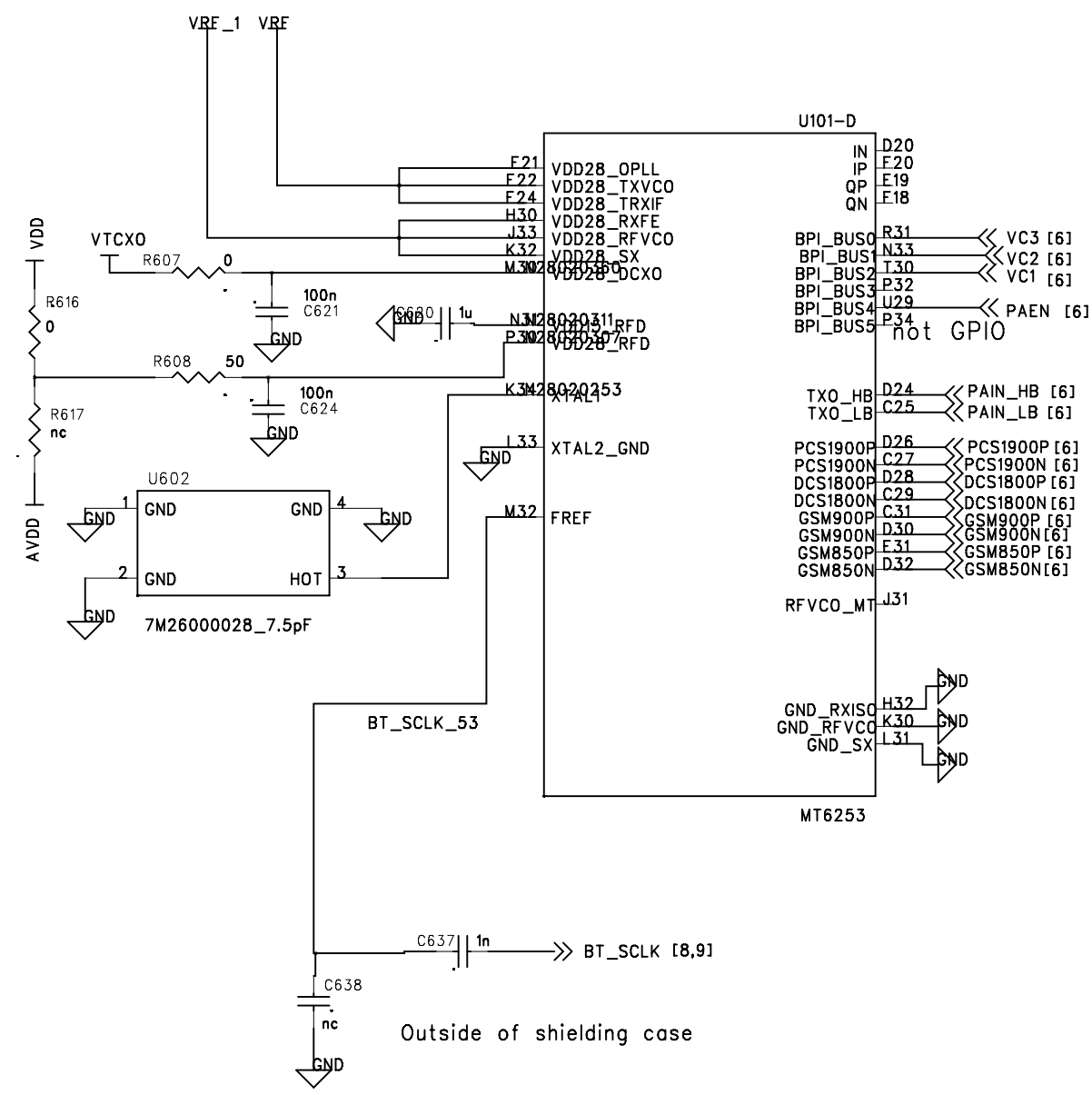
# CAM

## Sensor Interface



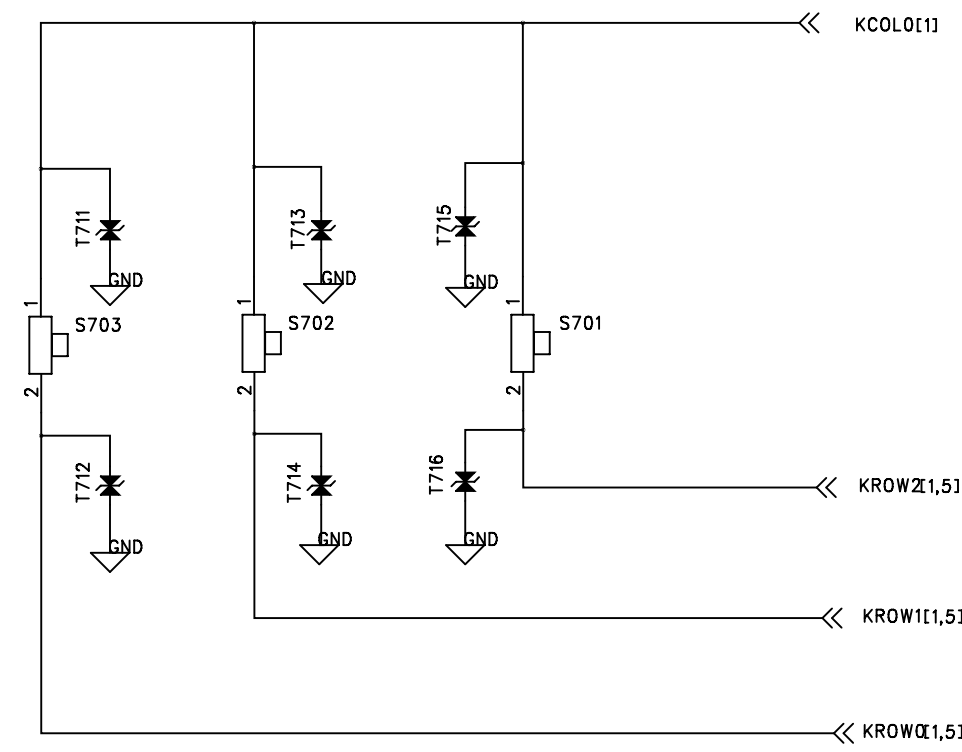


C615 close to VDD28\_OPLL  
 L603 close to VDD28\_OPLL  
 C616 close to VDD28\_RFVCO

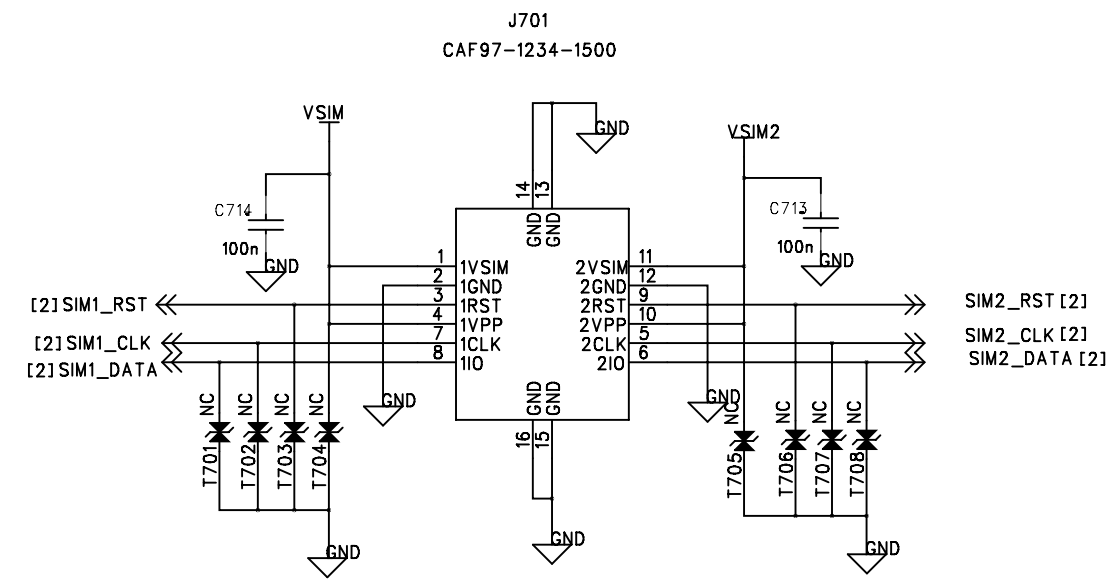


Outside of shielding case

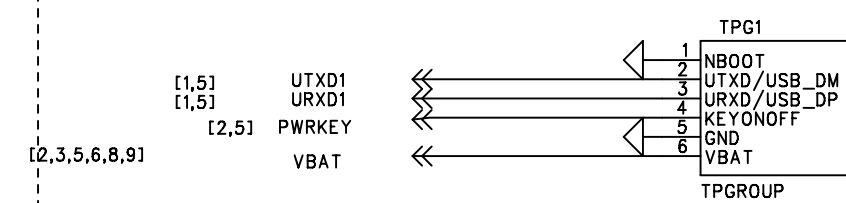
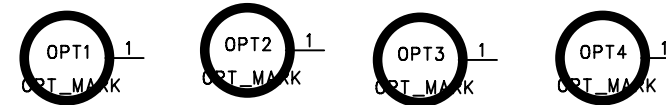
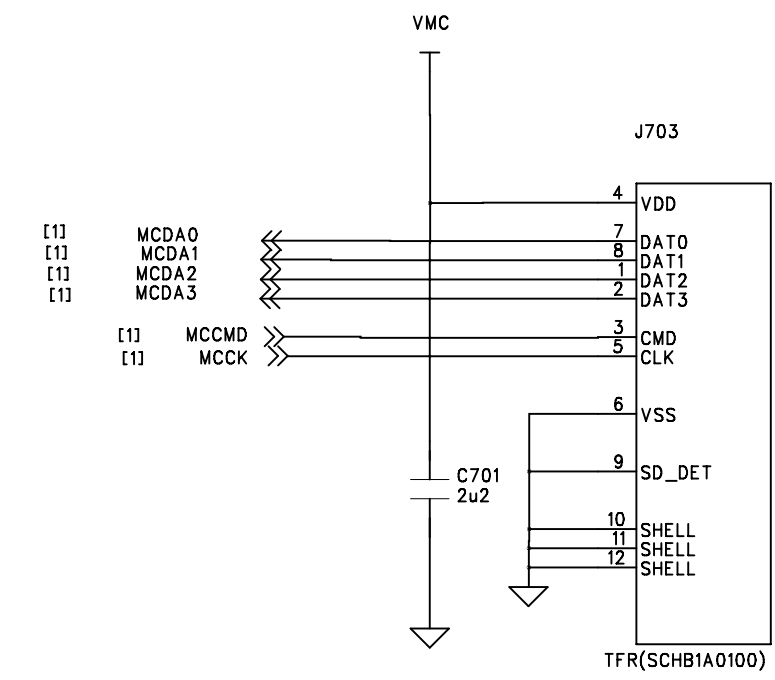
# KEYPAD



# SIM



# T-FLASH







# MT6612

