\$203-175.6 \$203-775.4 \$203-775.4 \$203-775.1 \$203-775.1 \$203-775.1 \$203-775.1 \$203-775.1 \$203-775.1 \$203-775.1 \$203-775.1 \$203-775.1 \$203-775.1 \$203-775.1 \$203-775.1 IP1 IP2 52 \$EG0 /P5.7 SEG13/P4.2 51 C CM 0/P4.0 SEG14/P41 50 **I** P3 C OM 1/P4.1 SEG-15/P4.0 VDD 49 C OM 2/P4.2 \$EG14/P3.7 48 C OM 3/P4.3 \$EG17/P3.6 47 C2 C3 VLCO \$EG18/P35 46 VDD SDATALCI £EG19/P3 # +5 0 SC LEAVIC 2 \$EG-20/P3 3 vi ++ VDD 8 EG-21/P3 2 BAI(AAA) 83 C 82 74 43 N/A VEE SEG-22/P3 1 1.5V +2 0.7 Y2Xo w \$EG-23/P3.0 +1 N/A T SEKTRI TOOME Xin 8 EG:24/P2.7 ■IP5 40 - BAI(AAA) 73 $V_{PP}/\mathtt{IE}\mathtt{SI}$ \$EG25/P2.6 N/A 39 1.5V X I out 8EG24/P25 C 10 15 38 XIm. 8 EG-27/P2 A 16 37 22pF nRESET \$EG:28/P23 Pi Pi 17 36 32.768**1 Hs** - YI Vmg 8 EG 29/P2 2 18 35 SEG-30/P21 P0.0/INTO 19 3+ P0 1/IN 11 22pF C+ \$EG31/P2.0 ΪÞ NTCIOK TUF C 6 B3 [100NF 10 K ÷ cm 🗀 2.20F LED 1K R.4 VDD 10 0k

FINE OFFSET ELECTRONICS CO.,LTD

CIRCUIT DIAGRAM

NOTE:

- 1. After Program Close J3 J4
- 2. J1 J2=Open

РСВ ТҮРЕ	MAIN	√
	OTHER	
REVISION	С	
ENGINEER	wpm	
MANAGER		
DATE	8-06-17	7

PAGE 2 OF 10 PAGES

MODEL NO.:

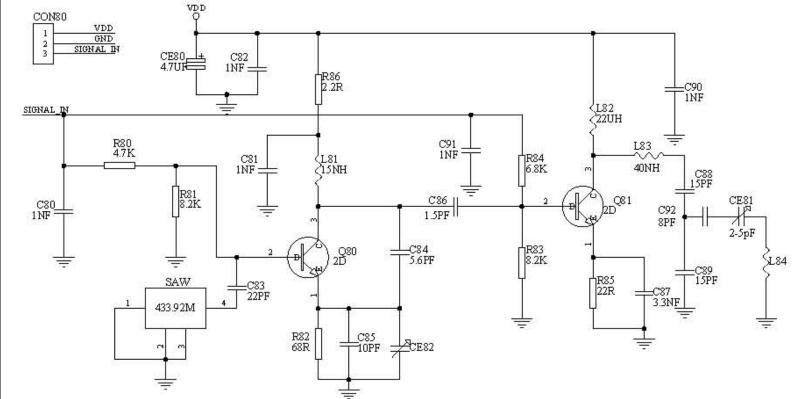
WH5

DESCRIPTION:



CIRCUIT DIAGRAM

NOTE:

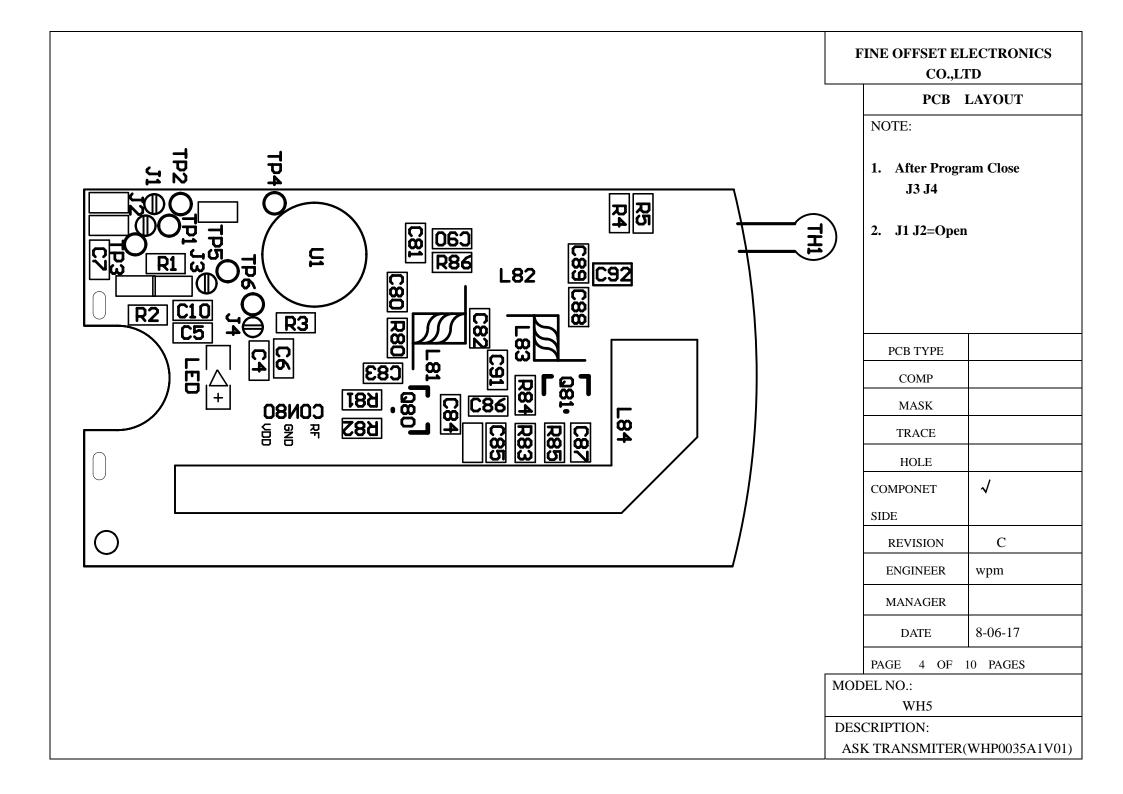


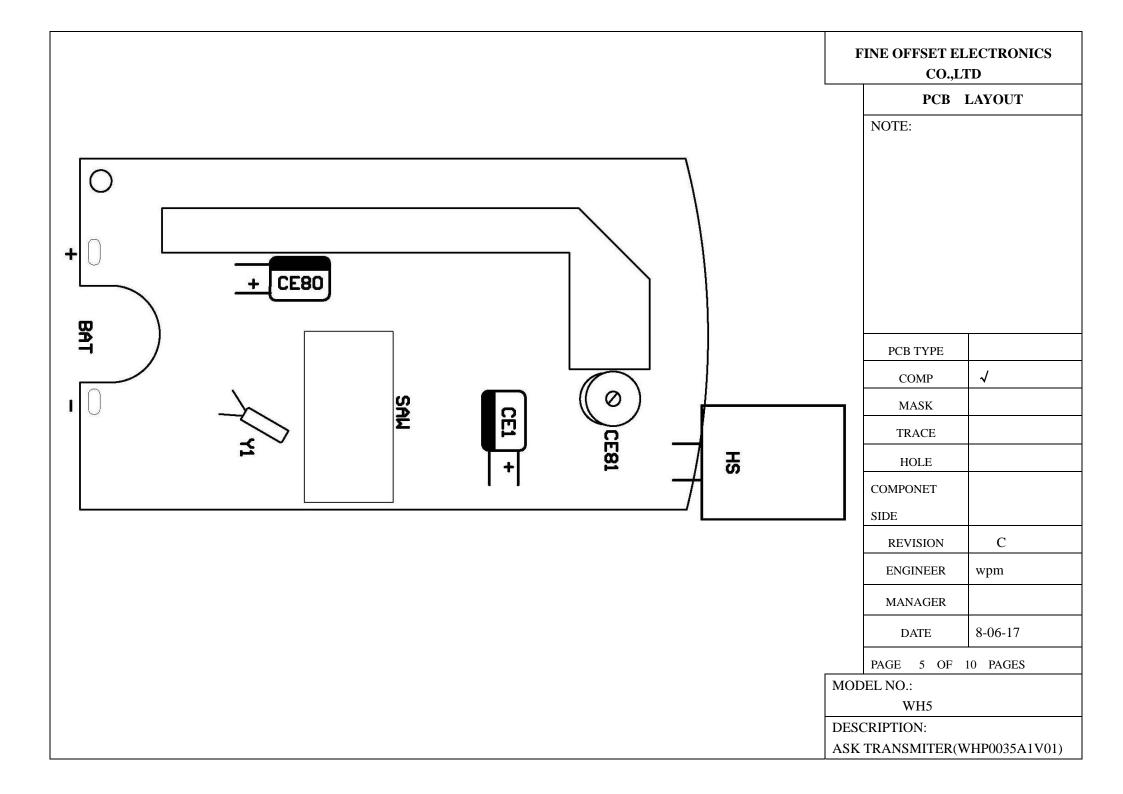
РСВ ТҮРЕ	MAIN	
TCDTTTE	RF	√
REVISION	C	
ENGINEER	wpm	
MANAGER		
DATE	8-06-17	7
PAGE 3 OF 10 PAGES		

MODEL NO.:

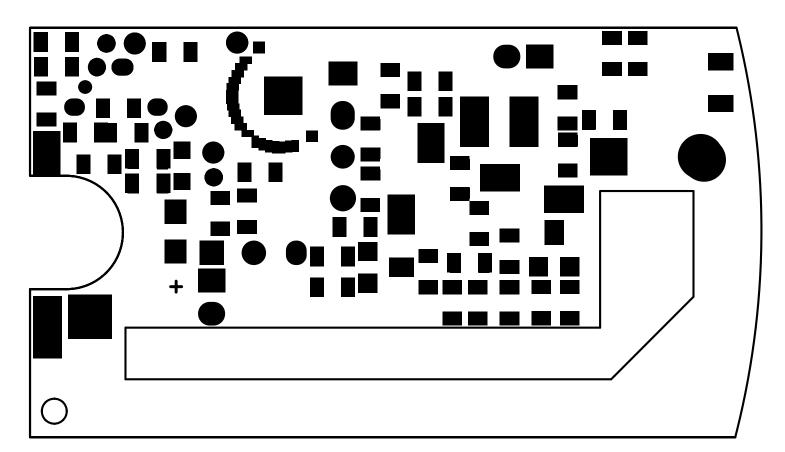
WH5

DESCRIPTION:





FINE OFFSET ELECTRONICS CO.,LTD PCB LAYOUT NOTE: WHP0035A1 V01 PCB TYPE COMP MASK TRACE HOLE COMPONET SIDE C REVISION **ENGINEER** wpm MANAGER 8-06-17 DATE PAGE 6 OF 10 PAGES MODEL NO.: WH5 DESCRIPTION: ASK TRANSMITER (WHP0035A1V01)



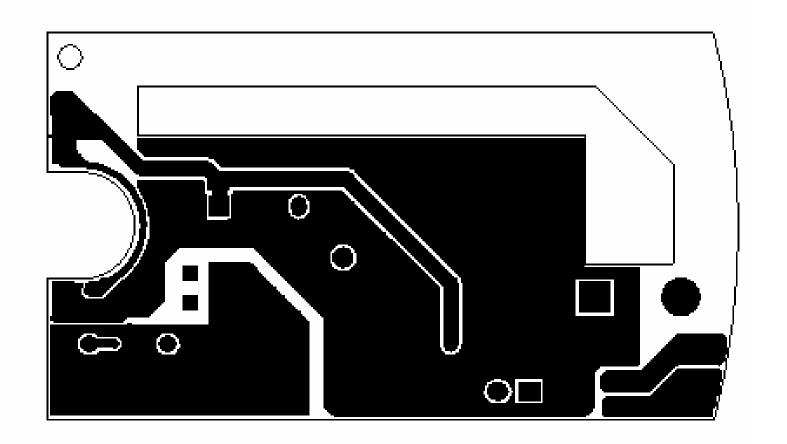
FINE OFFSET ELECTRONICS CO.,LTD

PCB	LAYOUT	
NOTE:		
DOD SIVE		
PCB TYPE		
COMP		
MASK	√	
TRACE		
HOLE		
COMPONET		
SIDE		
REVISION	C	
ENGINEER	wpm	
MANAGER		

MODEL NO.:

WH5

DESCRIPTION:



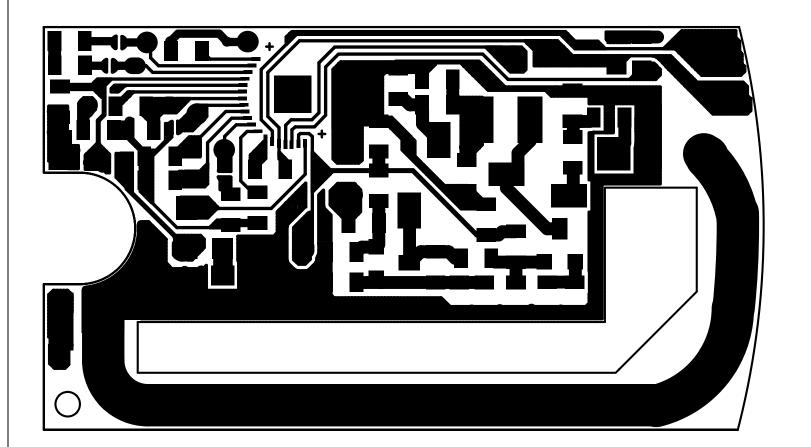
FINE OFFSET ELECTRONICS CO.,LTD

PCB	LAYOUT	
NOTE:		
PCB TYPE		
COMP	✓	
MASK		
TRACE	✓	
HOLE		
COMPONET		
SIDE		
REVISION	С	
ENGINEER	wpm	
MANAGER		
DATE	8-06-17	
PAGE 8 OF	10 PAGES	

MODEL NO.:

WH5

DESCRIPTION:



FINE OFFSET ELECTRONICS CO.,LTD

PCB 1	PCB LAYOUT	
NOTE:		
	<u> </u>	
PCB TYPE		
COMP		
MASK		
TRACE	✓	
HOLE		
COMPONET	✓	
SIDE		
REVISION	С	
ENGINEER	wpm	
MANAGER		
DATE	8-06-17	
PAGE 9 OF	10 PAGES	

MODEL NO.:

WH5

DESCRIPTION:

FINE OFFSET ELECTRONICS CO.,LTD PCB LAYOUT NOTE: + + + + + + + **(+**) + + + \mathbb{H} (+) ++ 6. 8mm + PCB TYPE + **COMP** + 0, 8x1, 8mm MASK + + + + + + + + + + TRACE HOLE **COMPONET** SIDE \mathbf{C} REVISION **ENGINEER** wpm MANAGER 8-06-17 DATE PAGE 10 OF 10 PAGES MODEL NO.: WH5 **DESCRIPTION:** ASK TRANSMITER (WHP0035A1V01)