

Figure 5
Signal Port of the EUT



Figure 6
Inside Configuration of the EUT

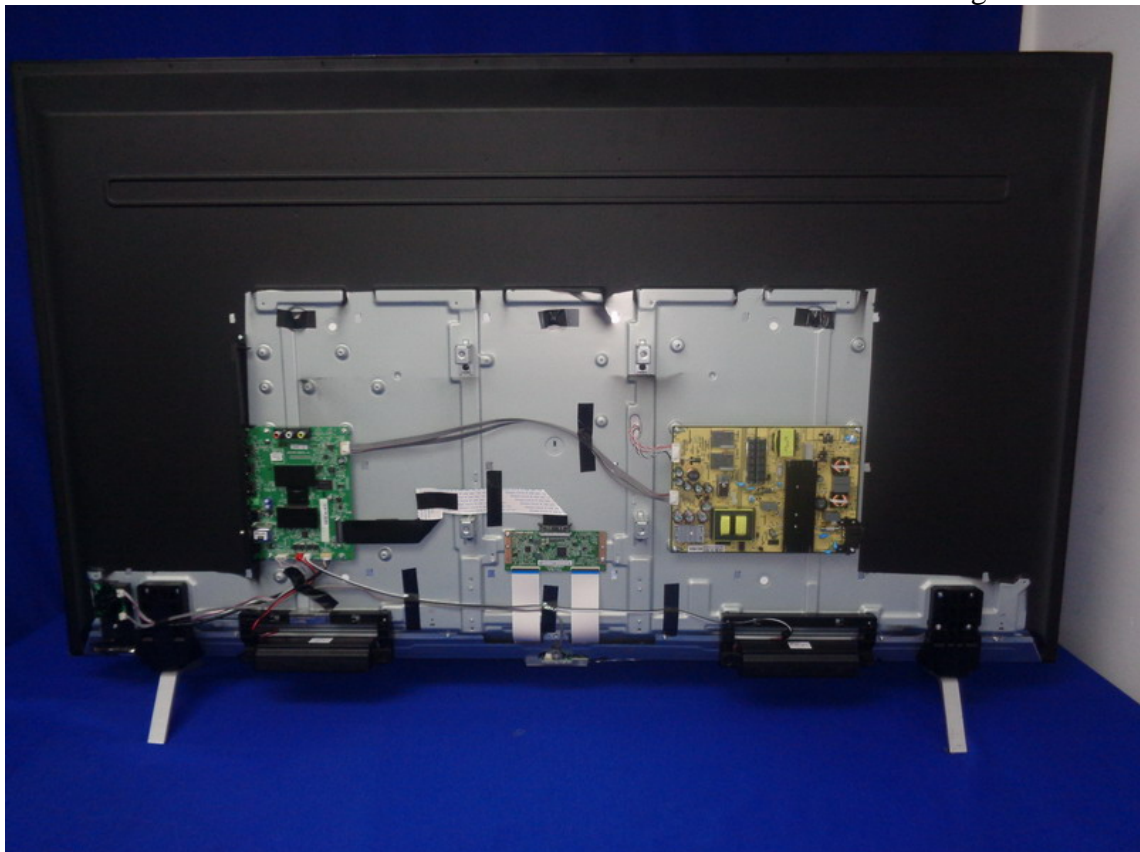


Figure 7
Frontside of the Main Board

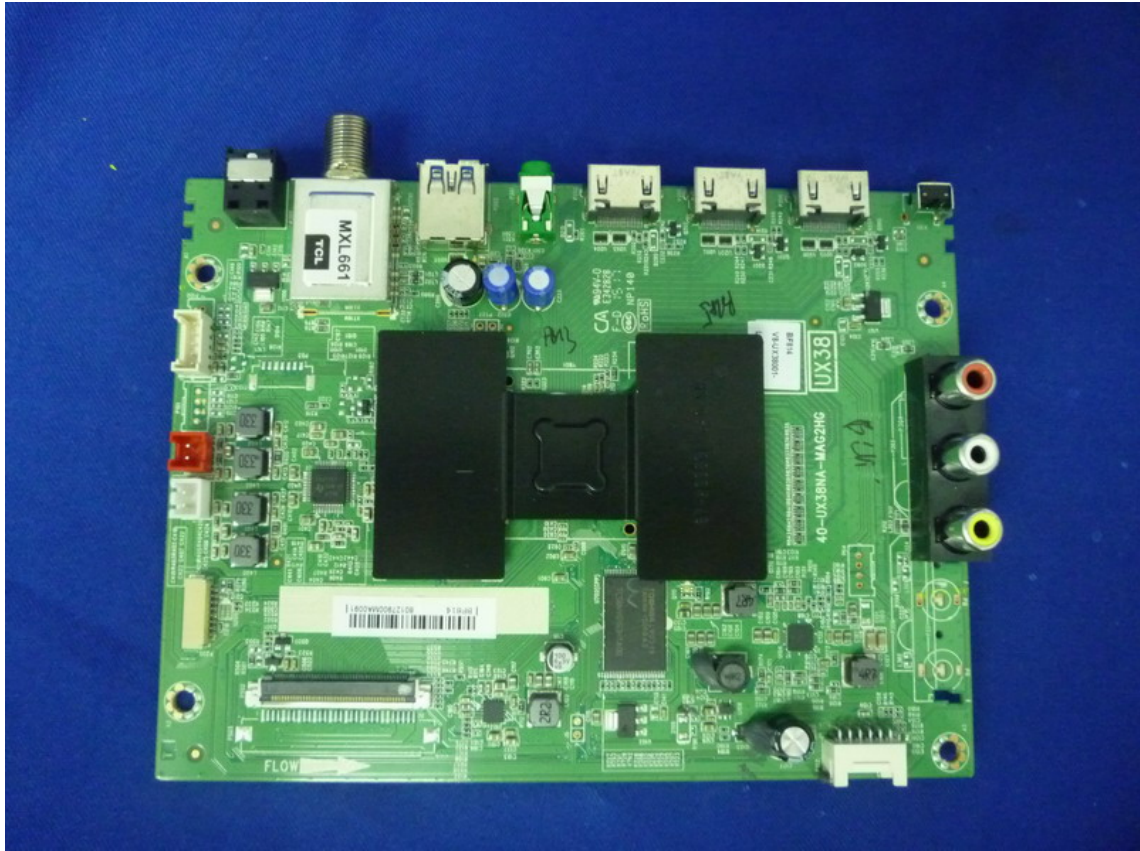


Figure 8
Backside of the Main Board

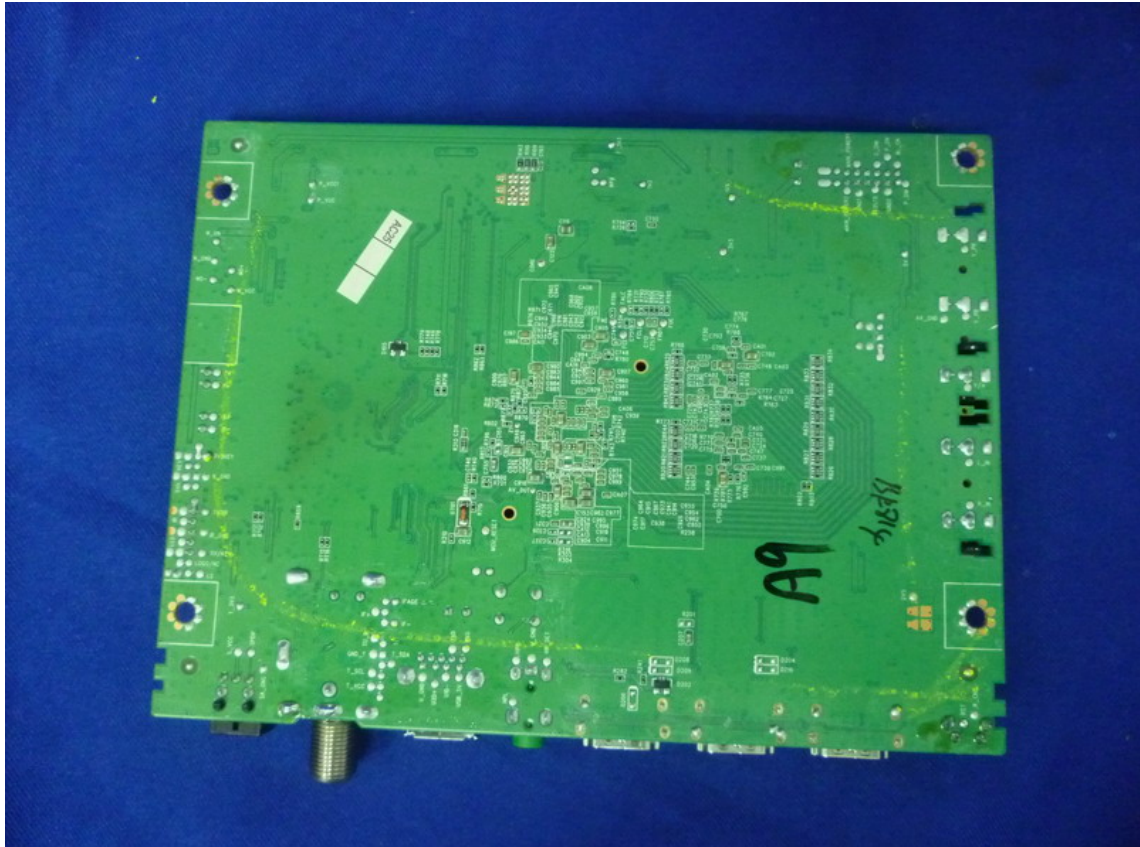


Figure 9
Frontside of the Power Board



Figure 10
Backside of the Power Board

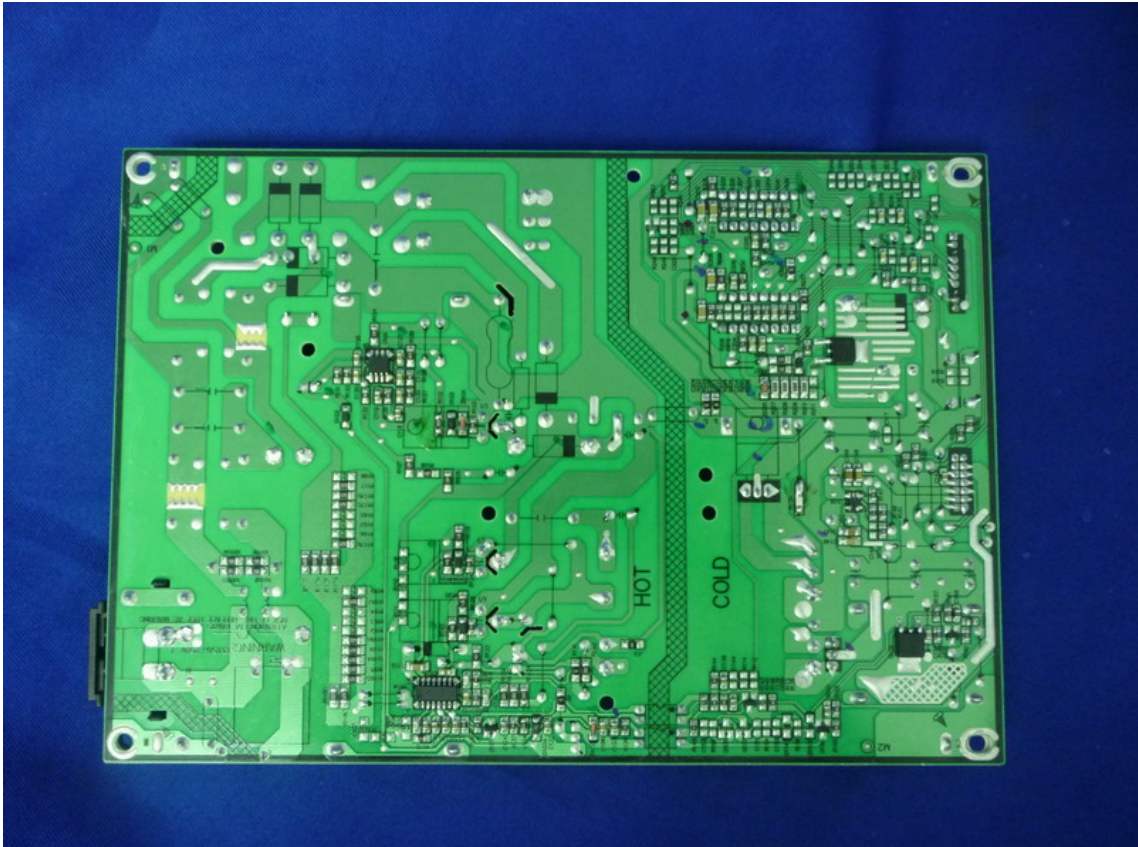


Figure 11
Frontside of the Power Board



Figure 12
Backside of the Power Board

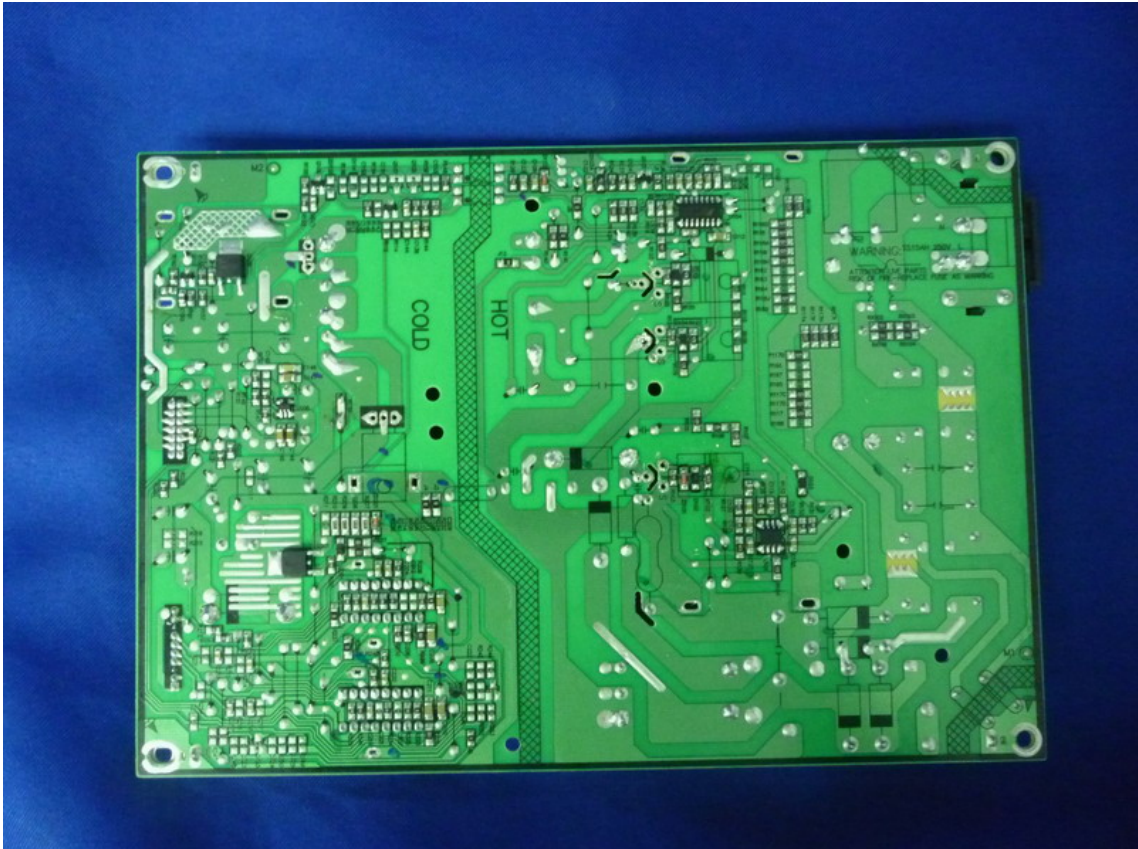


Figure 13
Frontside of the PCB Board

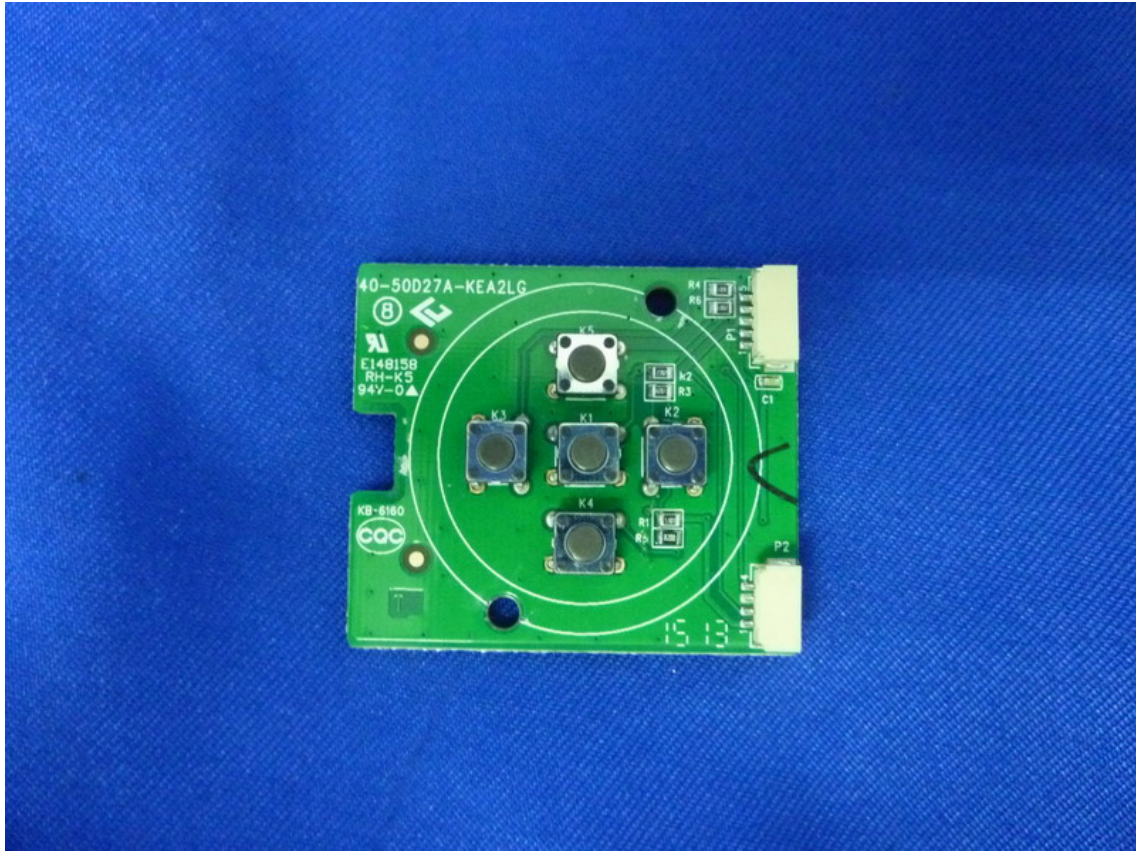


Figure 14
Backside of the PCB Board

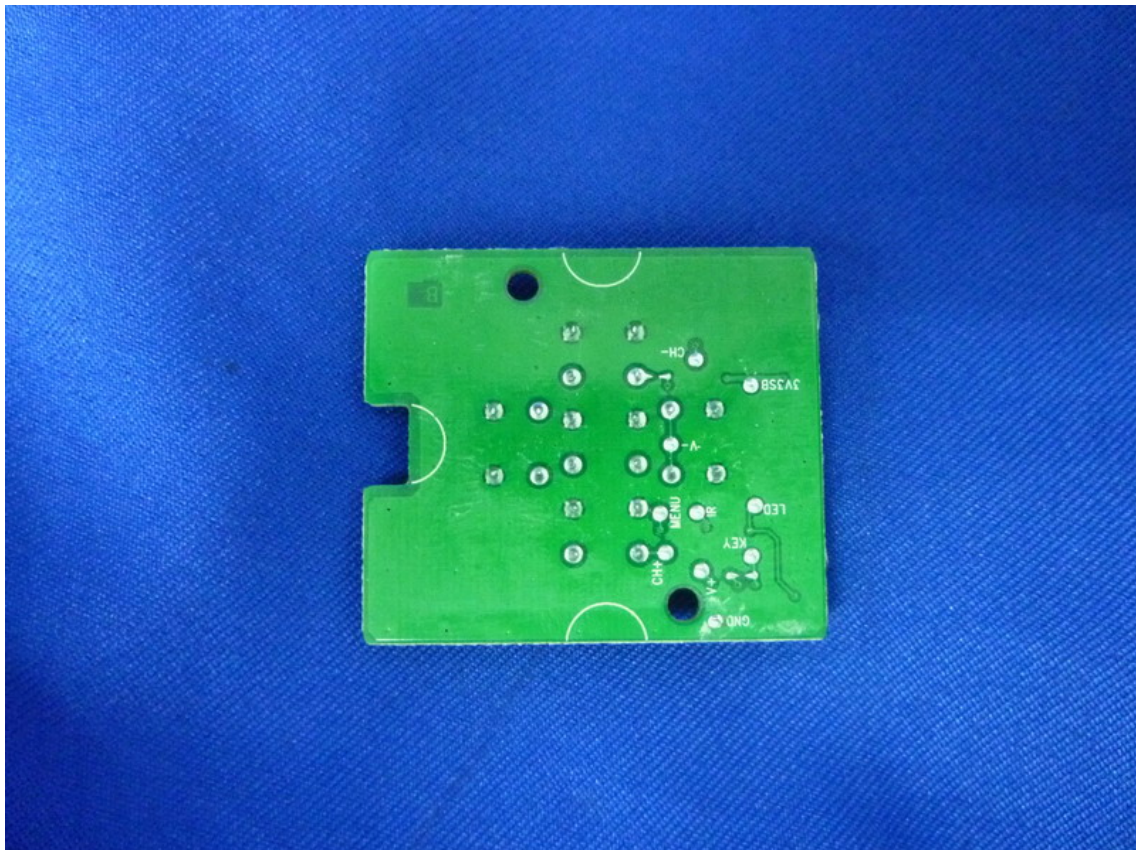


Figure 15
Frontside of the PCB Board

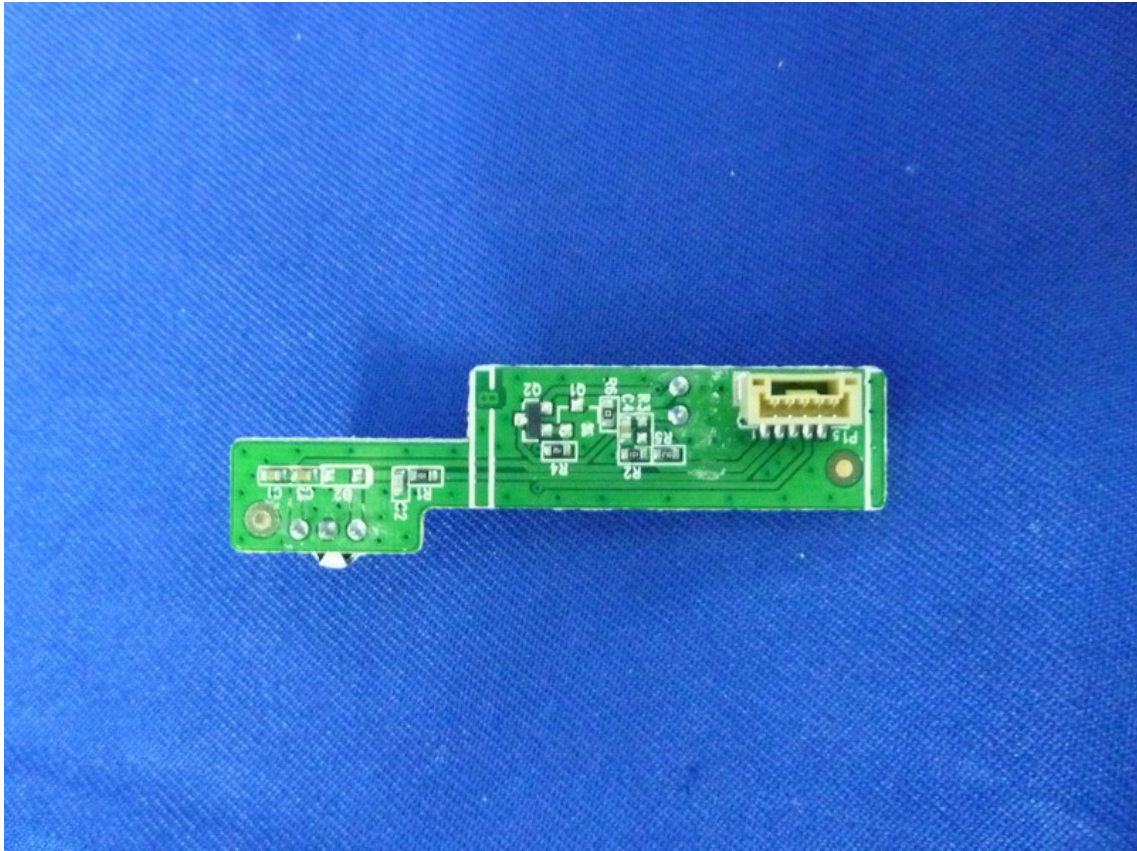


Figure 16
Backside of the PCB Board

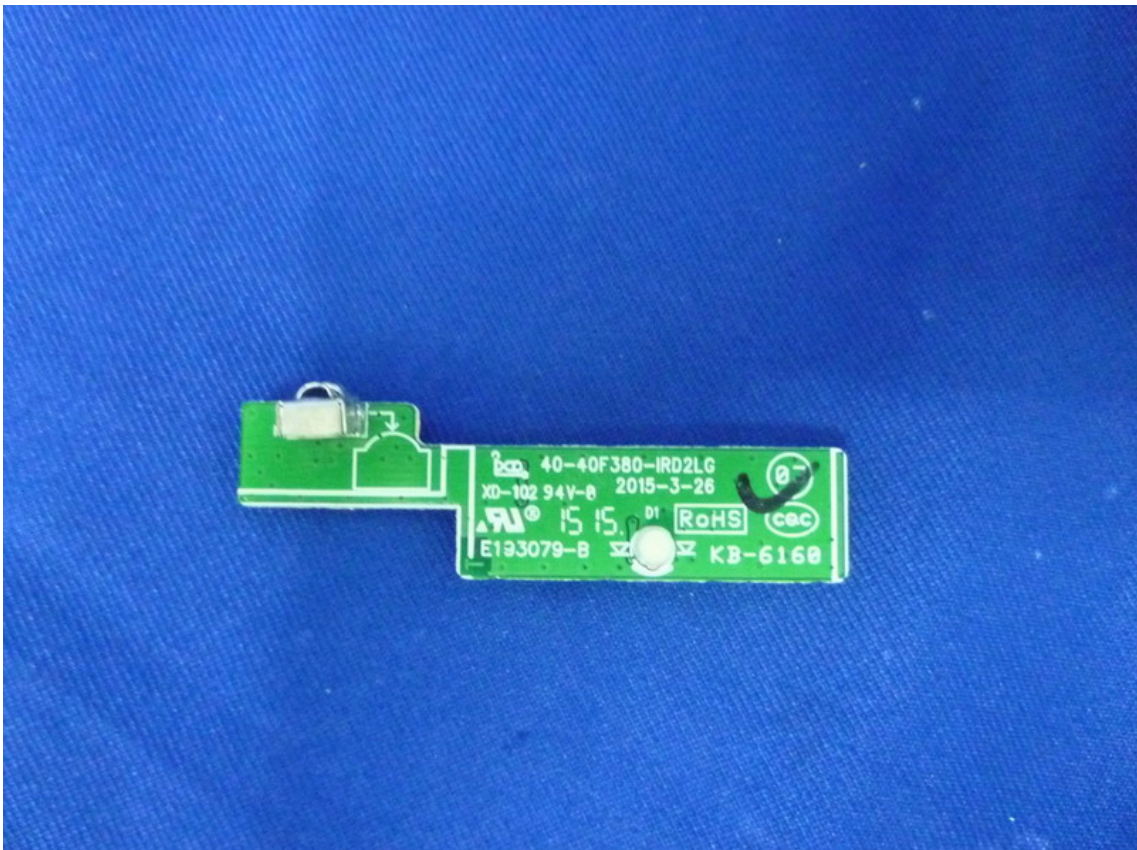


Figure 19
Frontside of the PCB Board

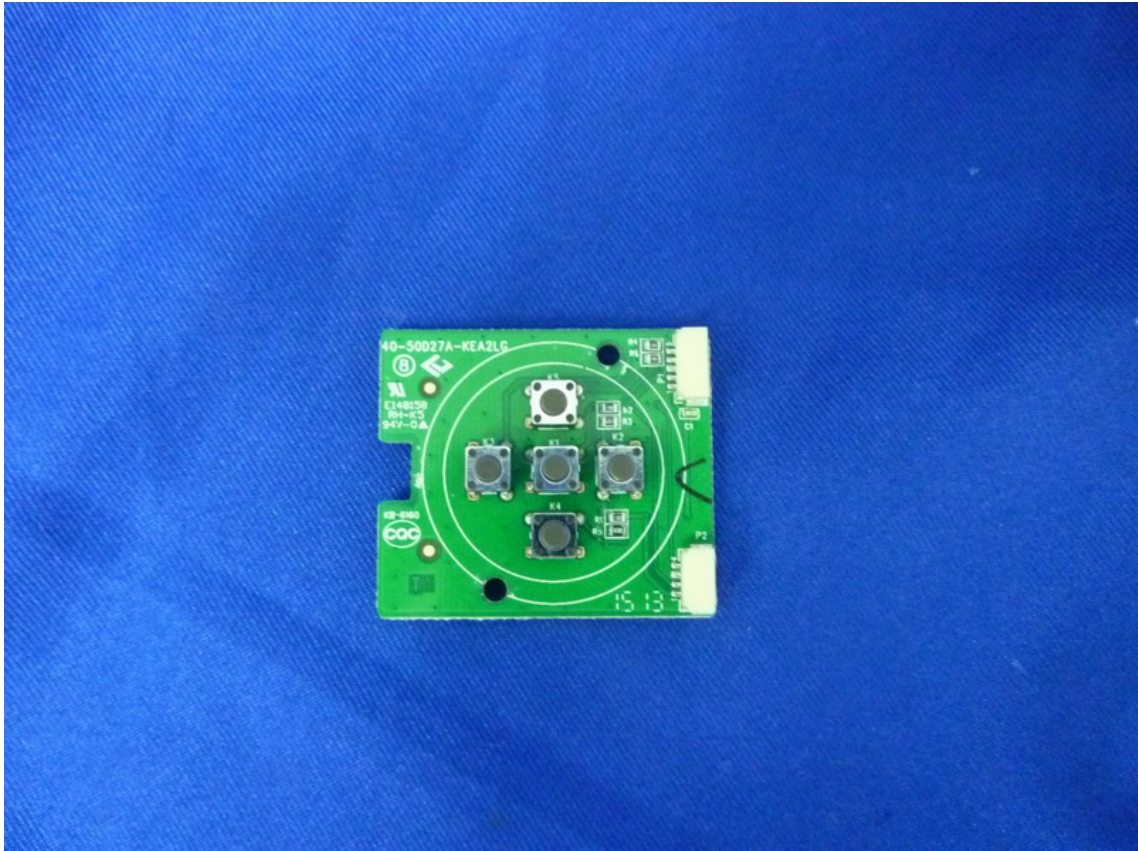


Figure 20
Backside of the PCB Board

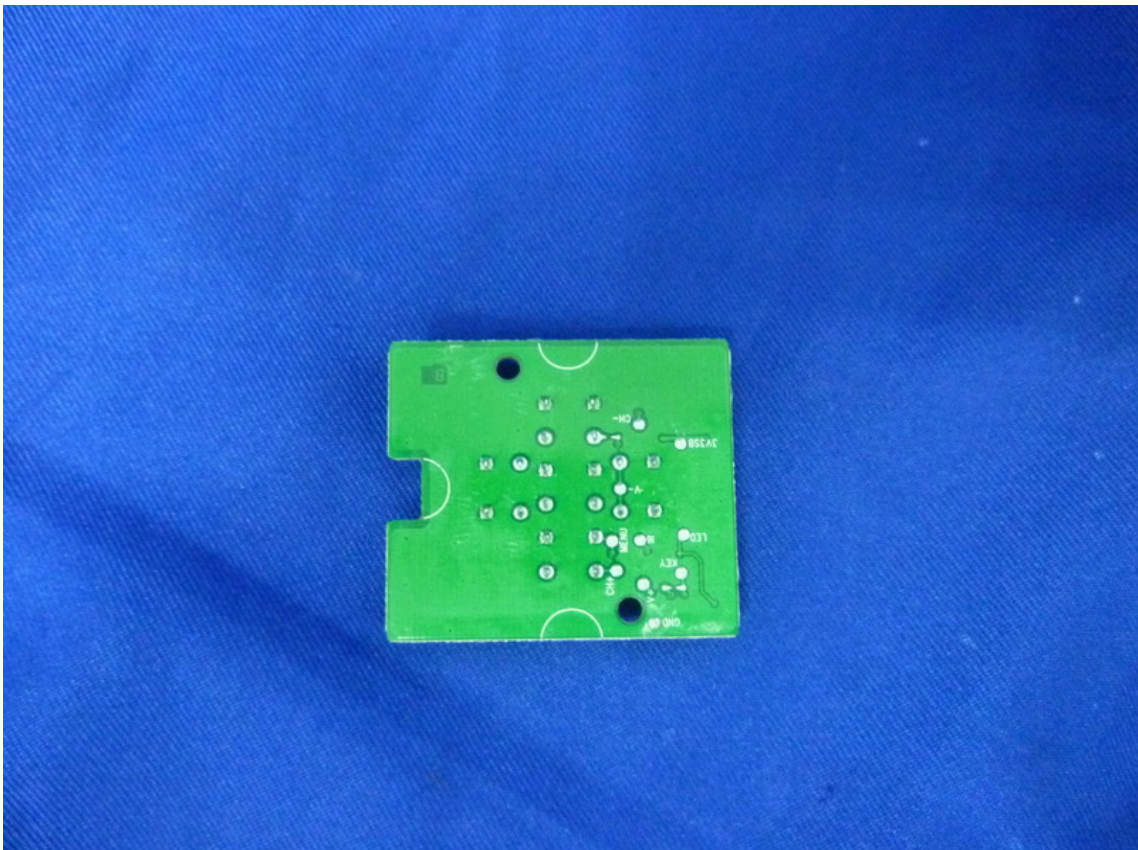


Figure 21
Frontside of the PCB Board

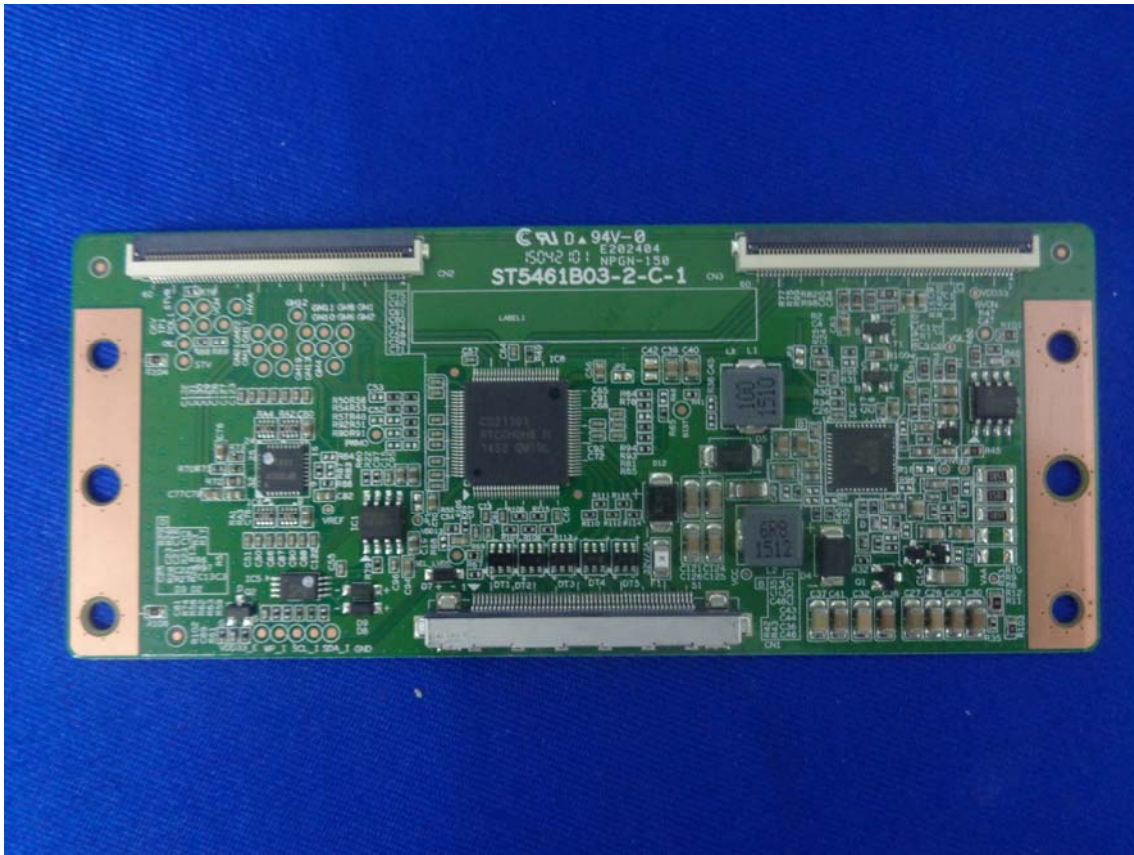


Figure 22
Backside of the PCB Board

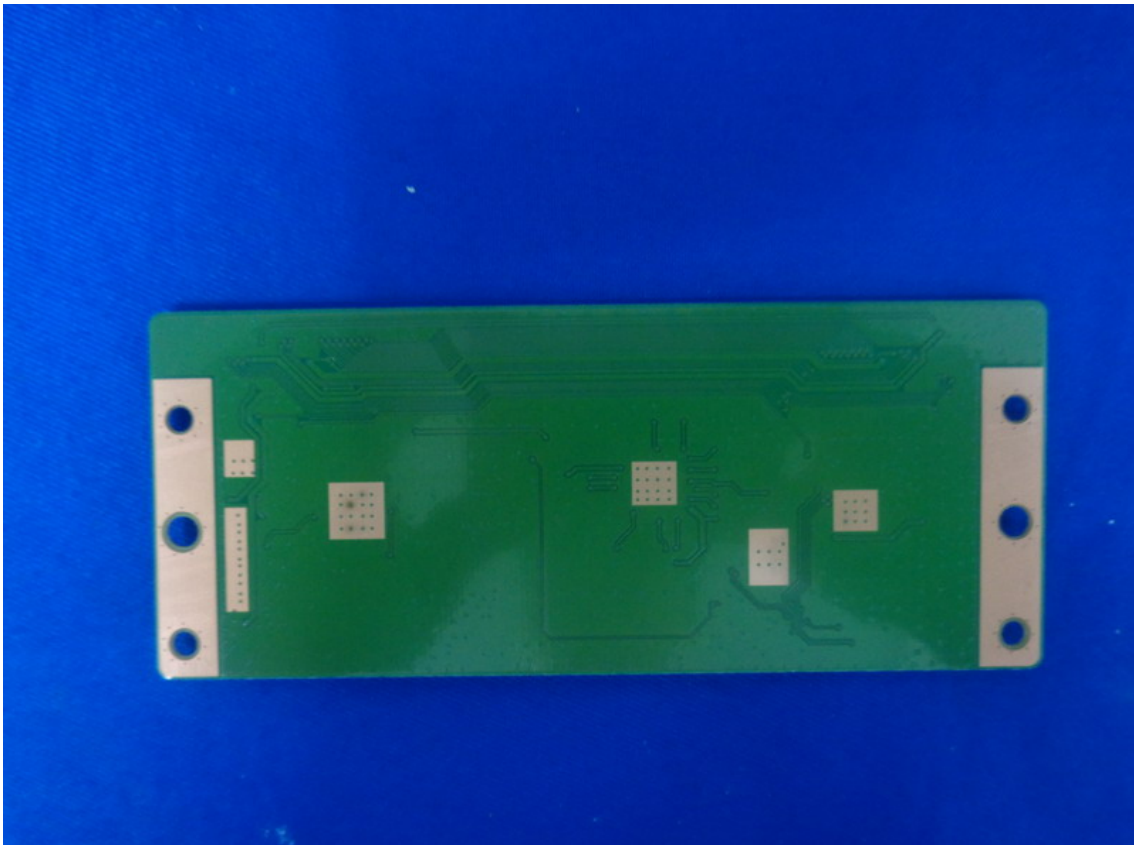


Figure 23
Frontside of the PCB Board

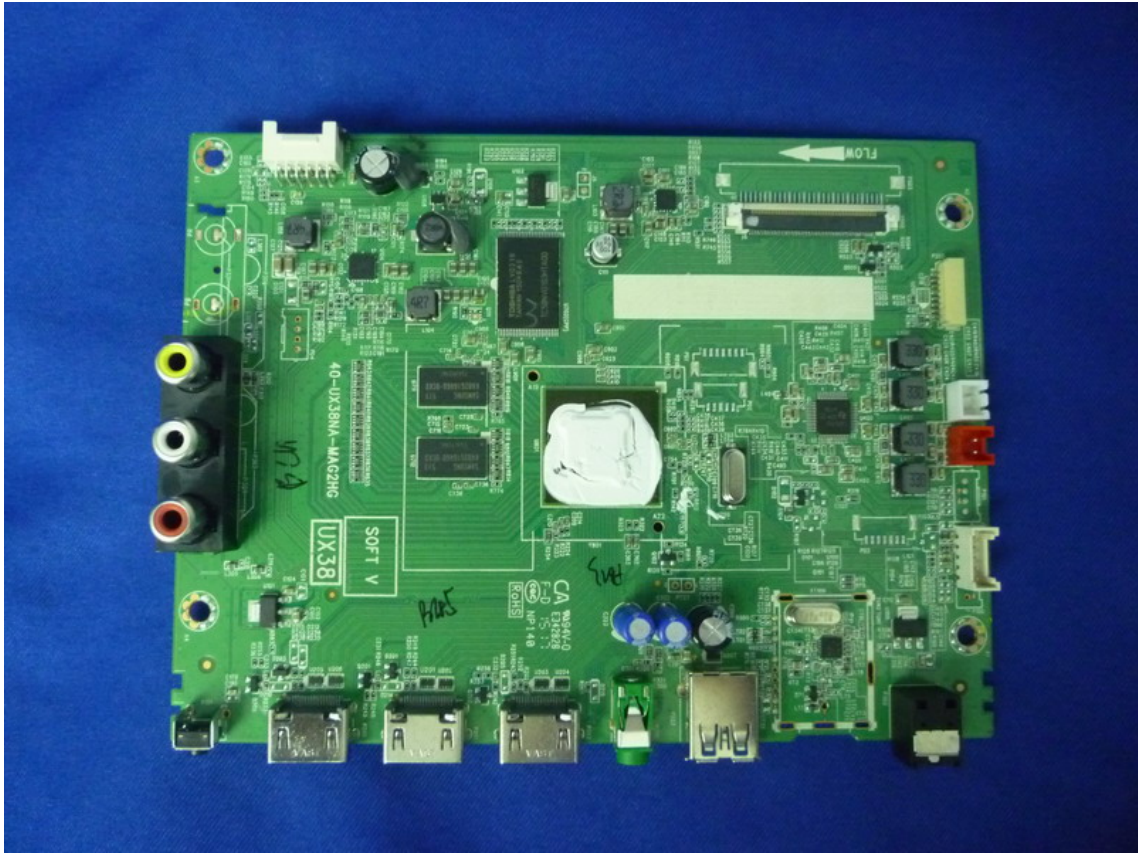


Figure 24
Backside of the PCB Board

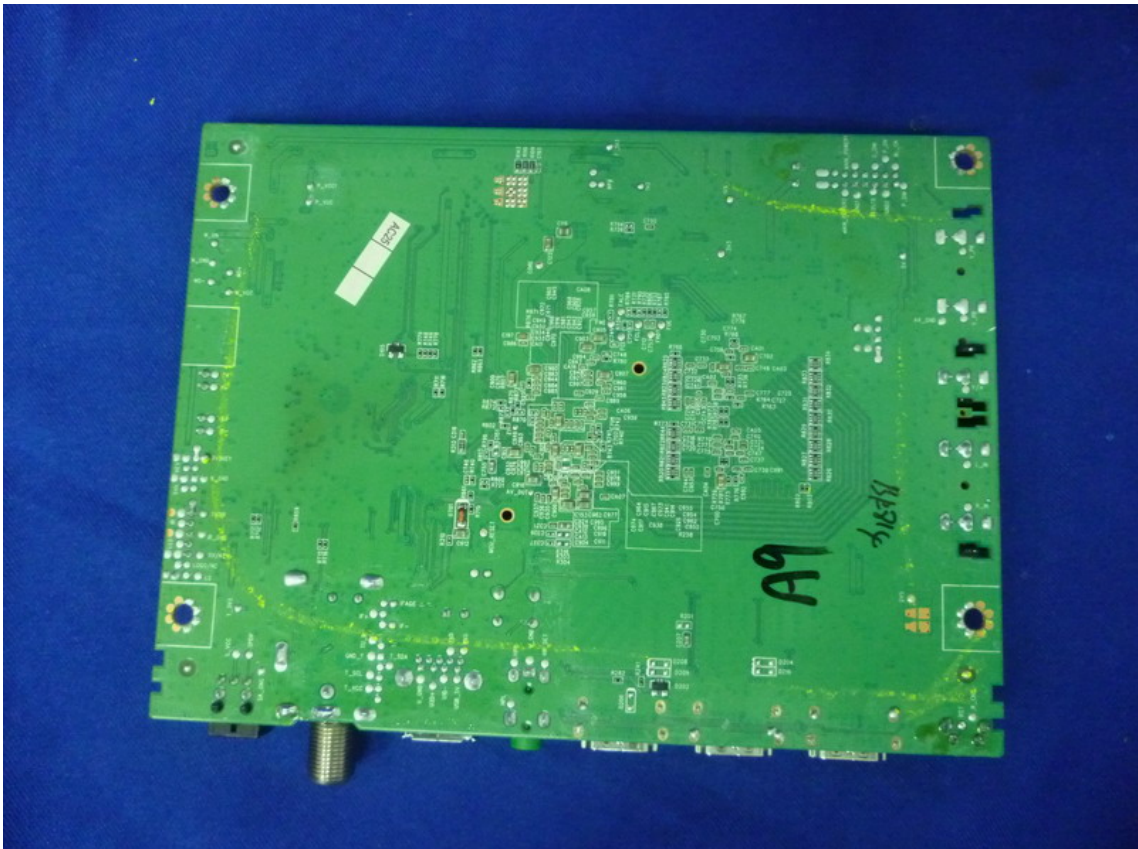


Figure 25
Frontside of the PCB Board



Figure 26
Backside of the PCB Board

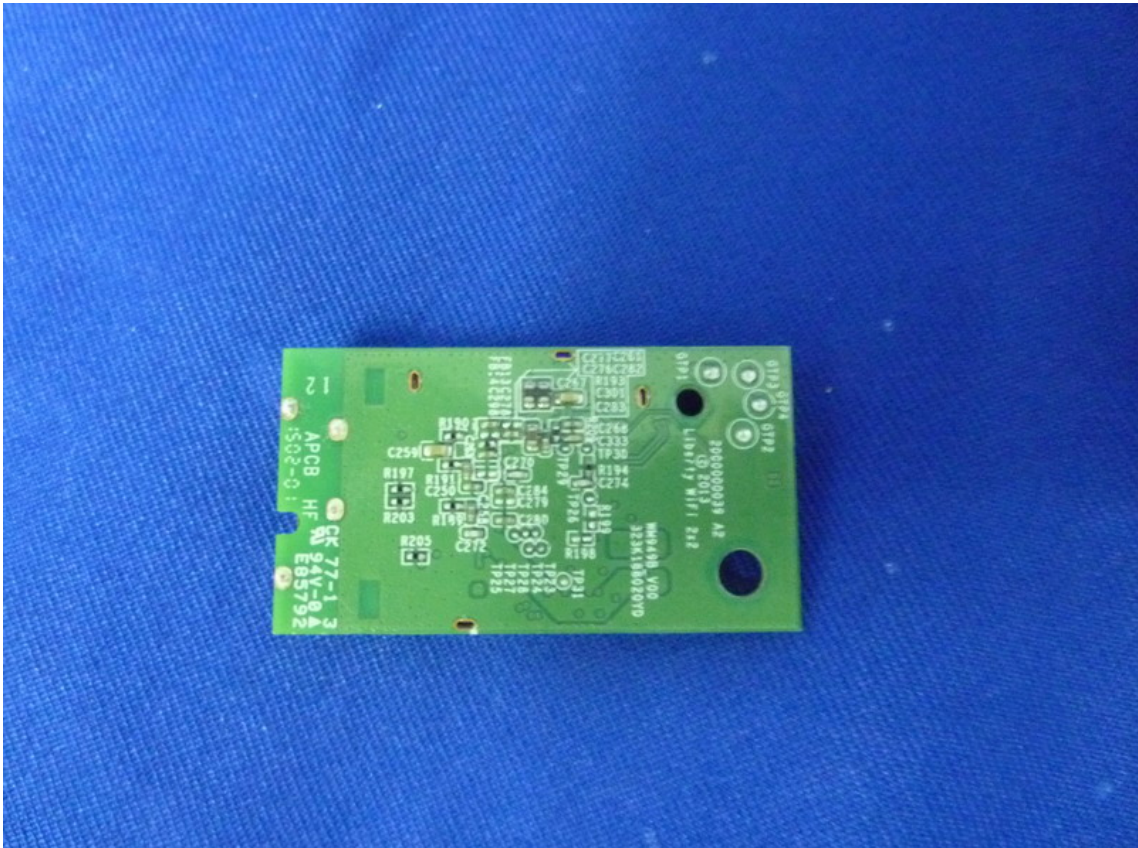


Figure 27
Speaker

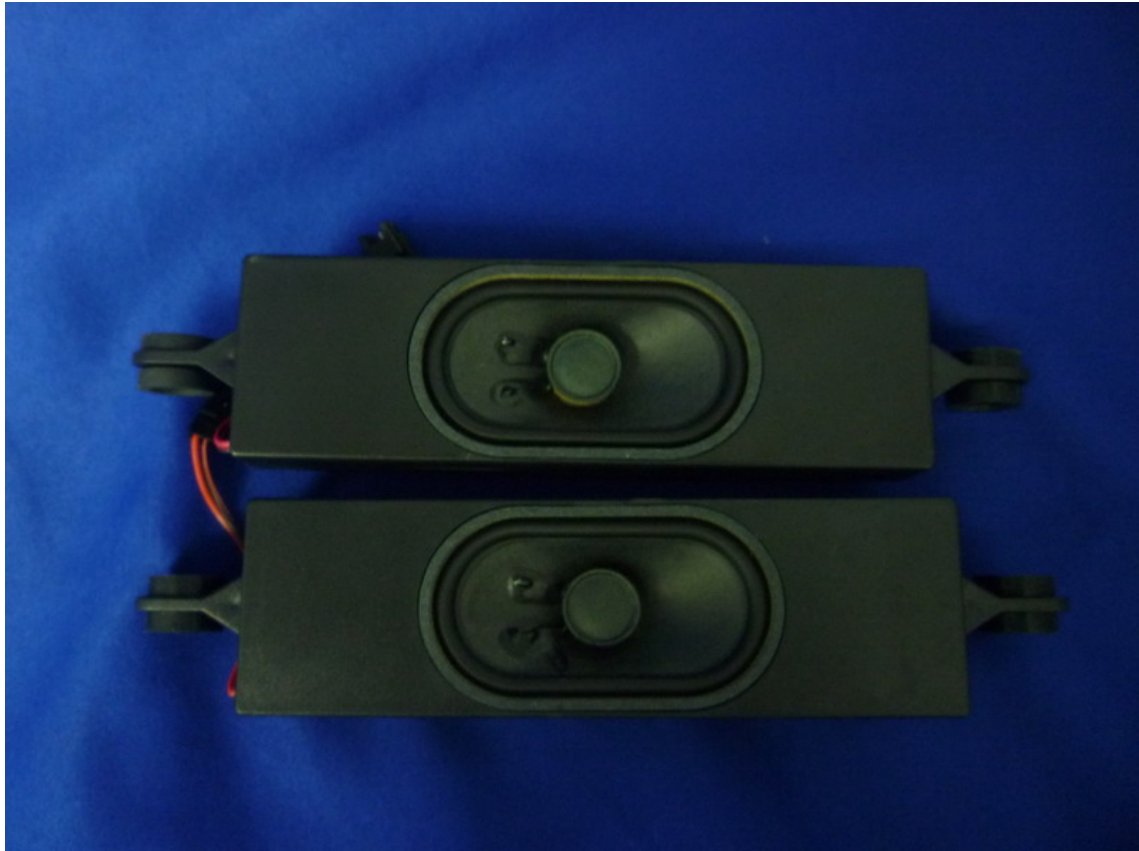


Figure 28
Speaker

