

Figure 5
Signal Port of the EUT



Figure 6
Inside Configuration of the EUT



Figure 7
Inside Configuration of the EUT



Figure 8
Inside Configuration of the EUT

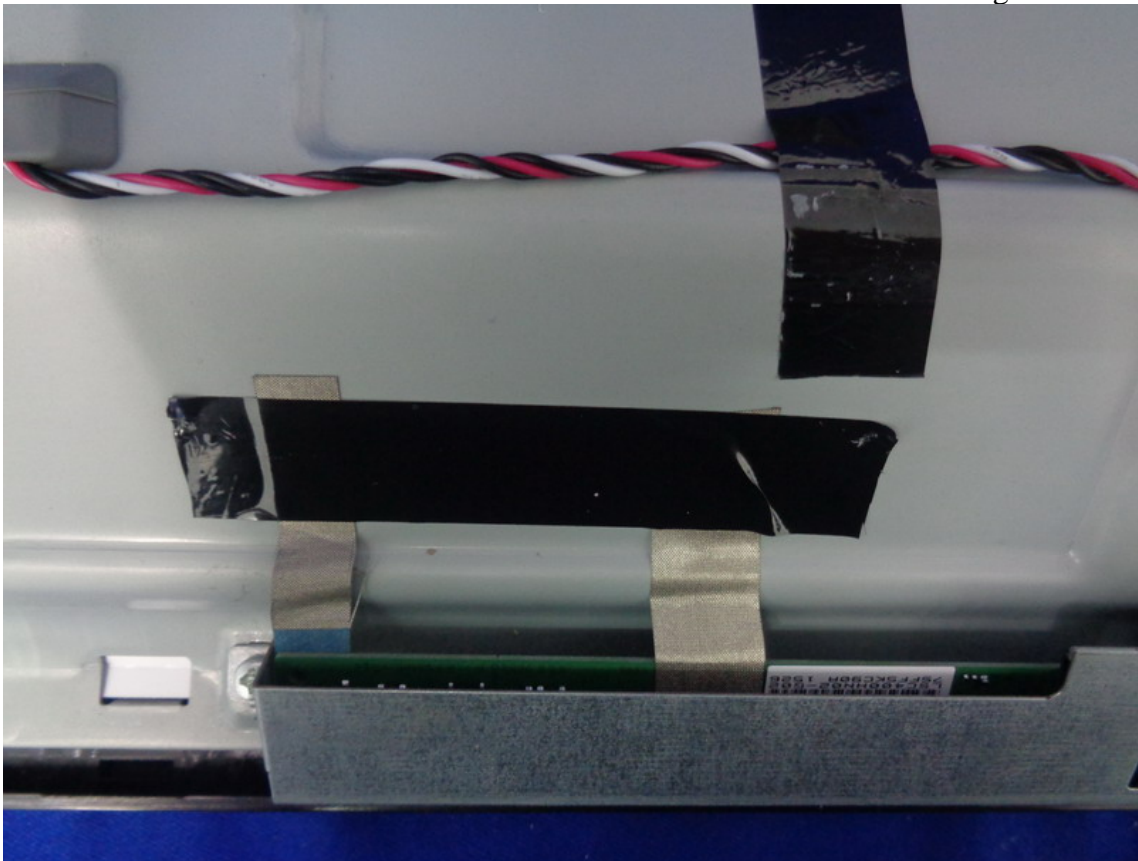


Figure 9
Inside Configuration of the EUT

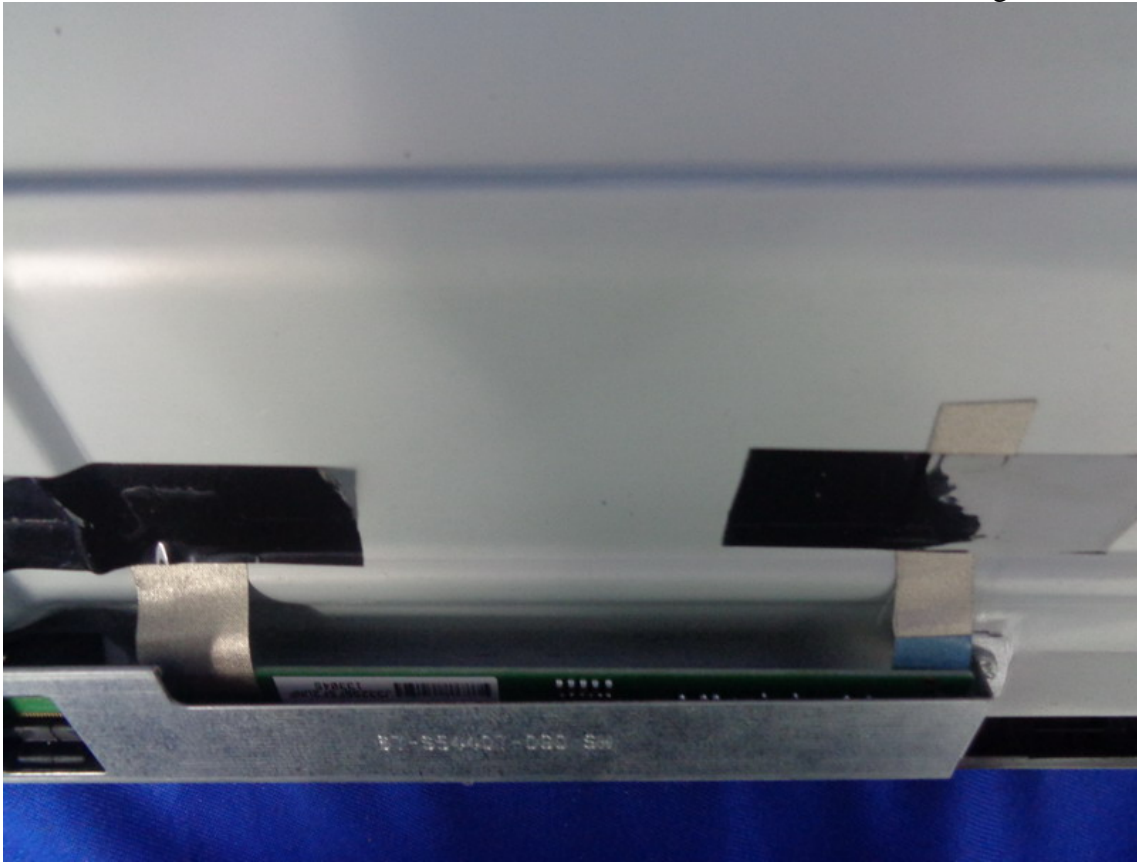


Figure 10
Inside Configuration of the EUT



Figure 11
Inside Configuration of the EUT

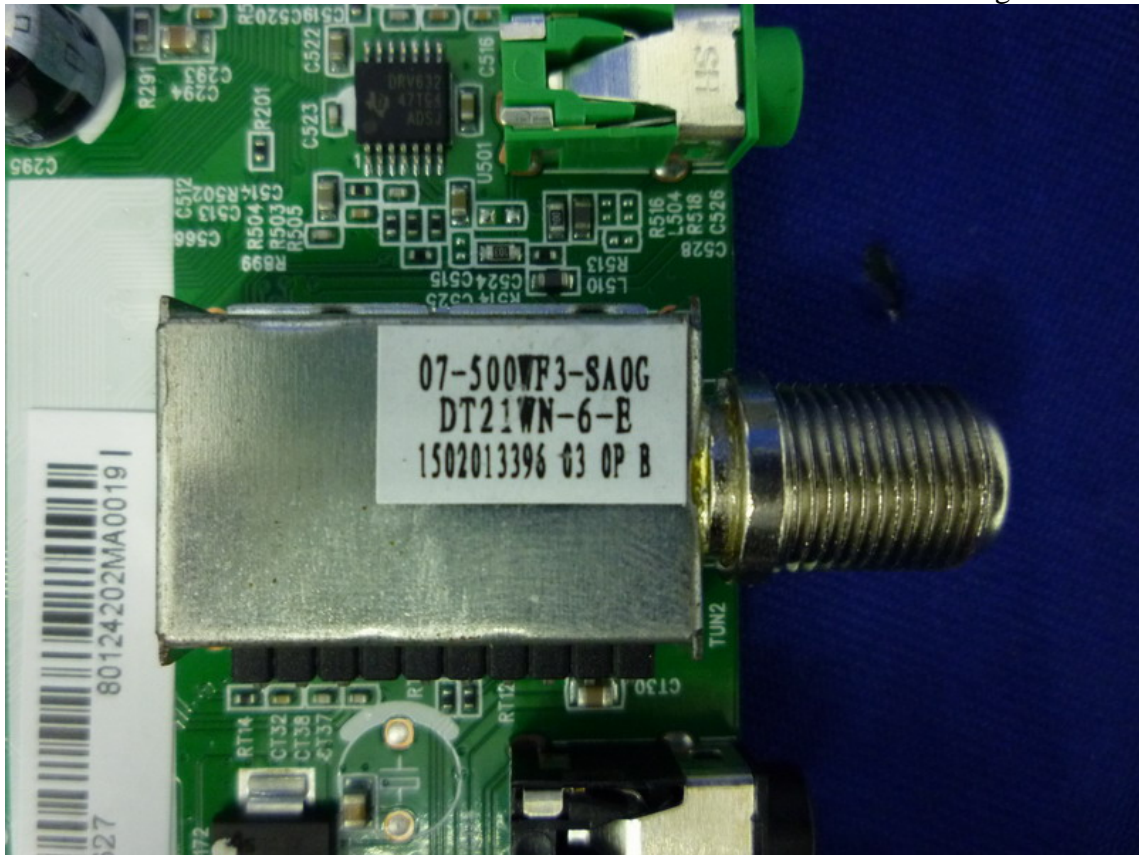


Figure 12
Inside Configuration of the EUT



Figure 13
Frontside of the LCD Panel



Figure 14
Frontside of the Main Board

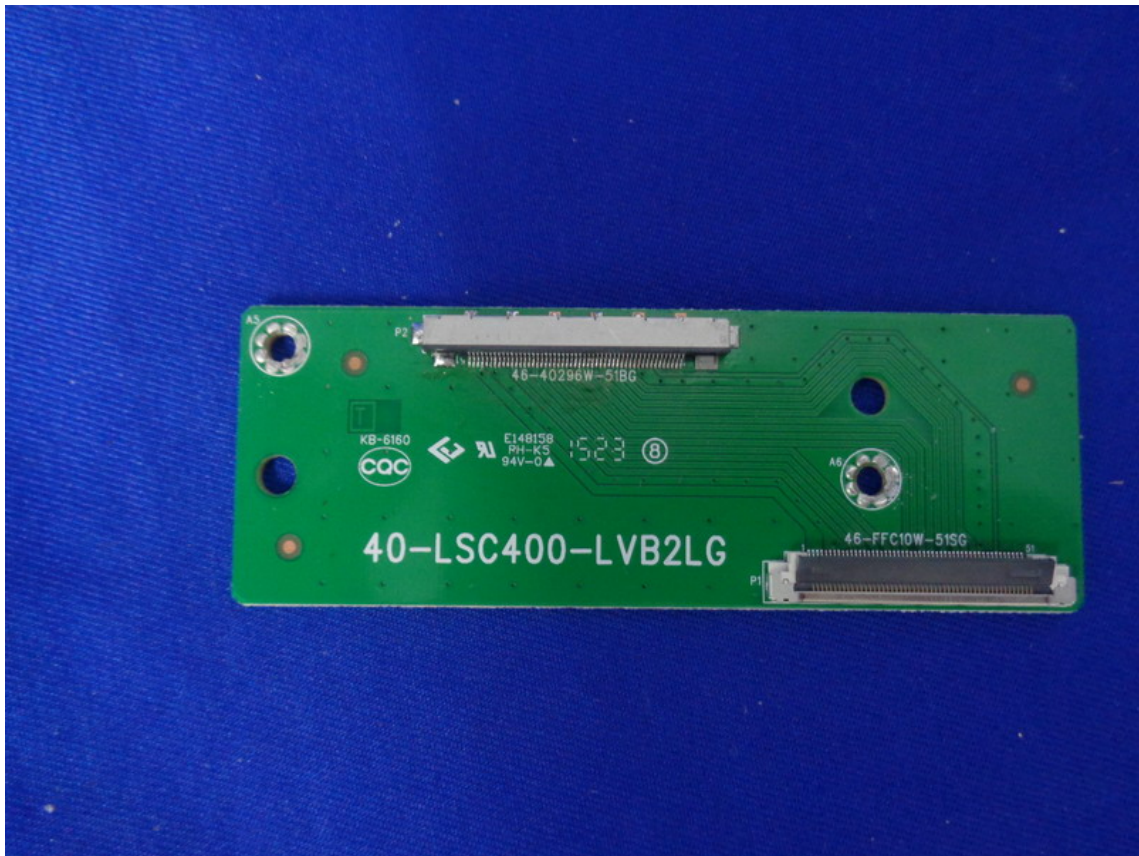


Figure 15
Backside of the Main Board

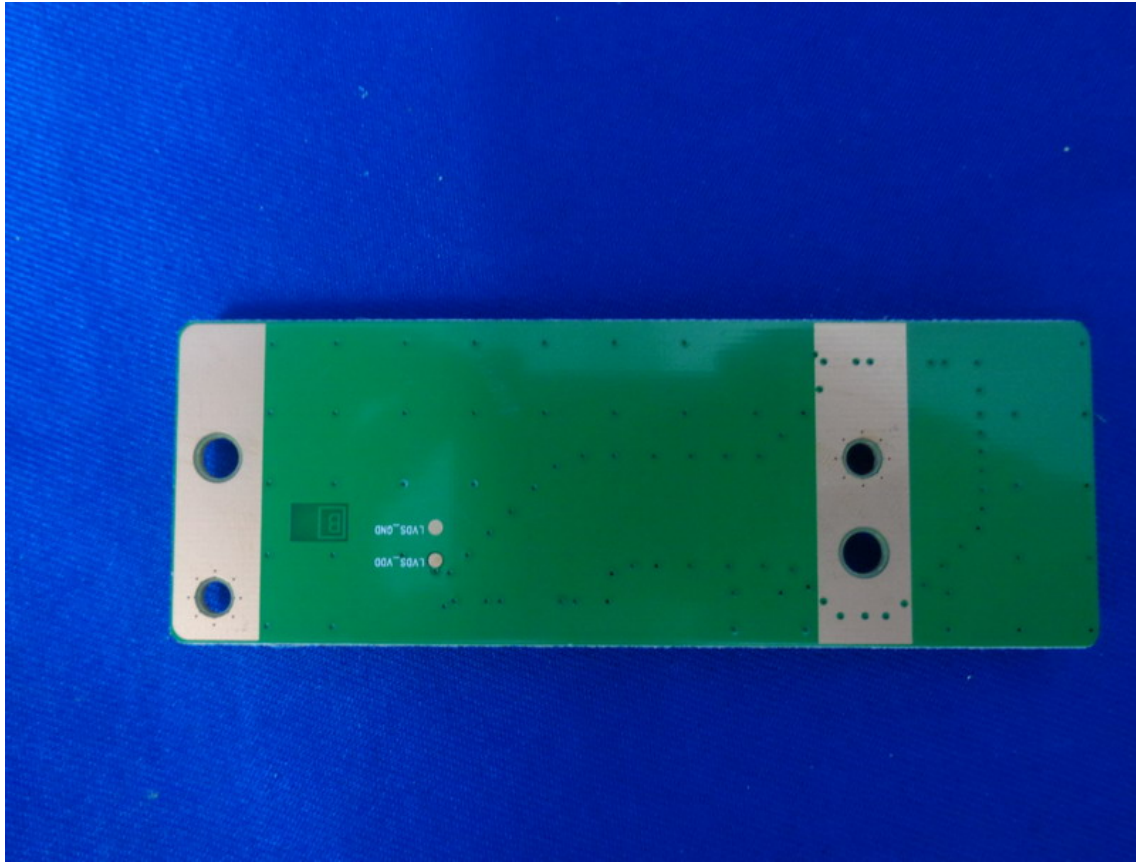


Figure 16
Frontside of the Main Board

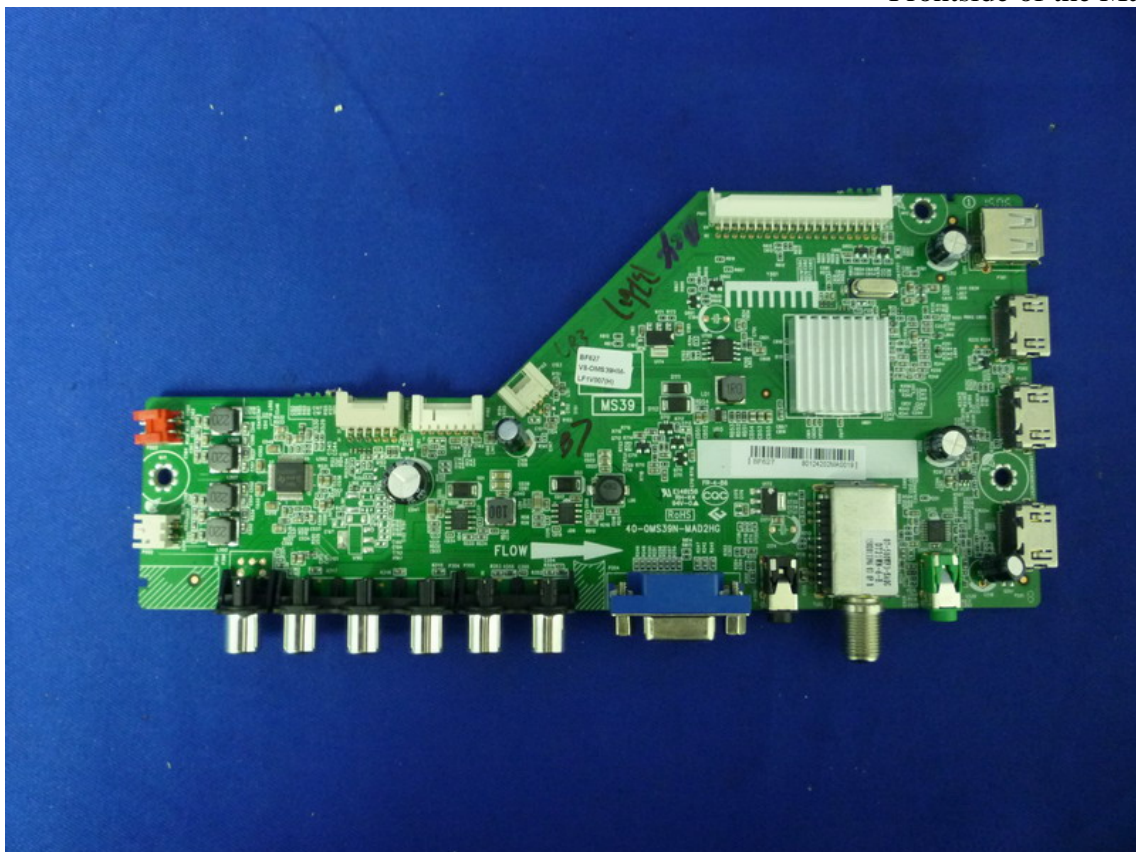


Figure 17
Backside of the Main Board

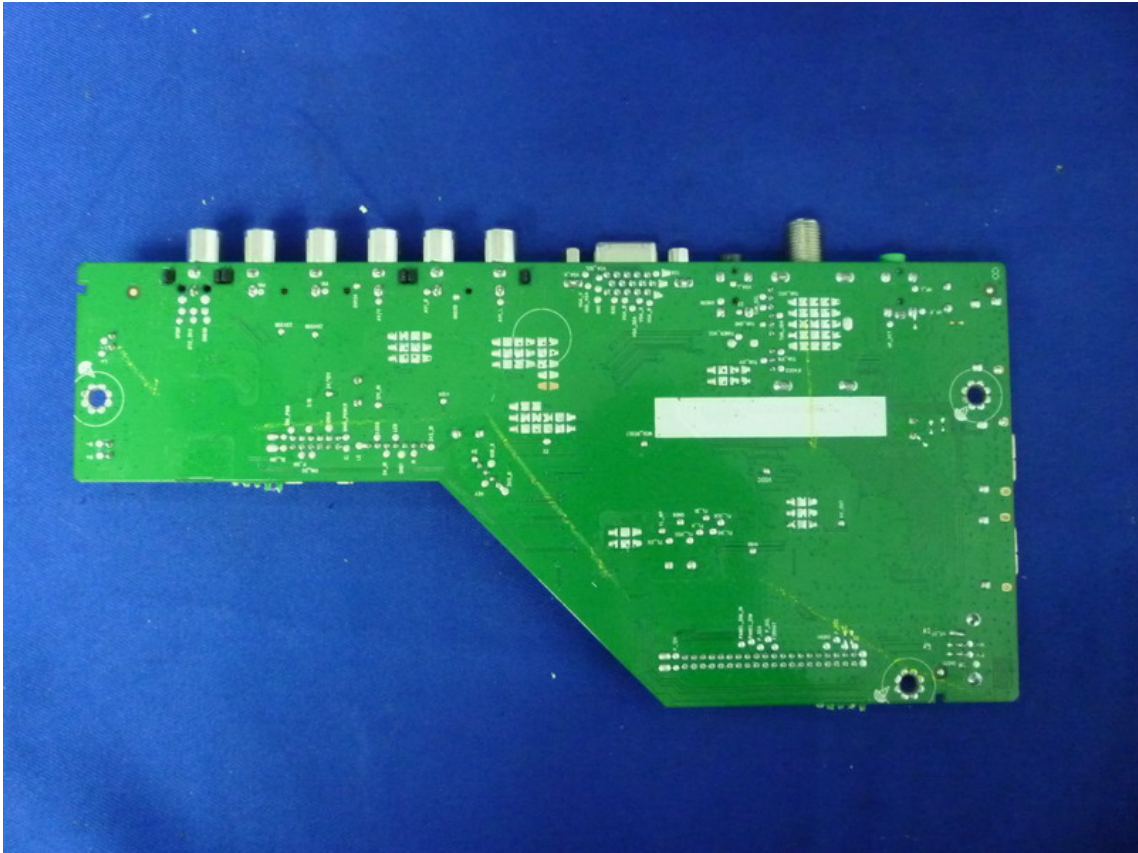


Figure 18
Frontside of the Main Board

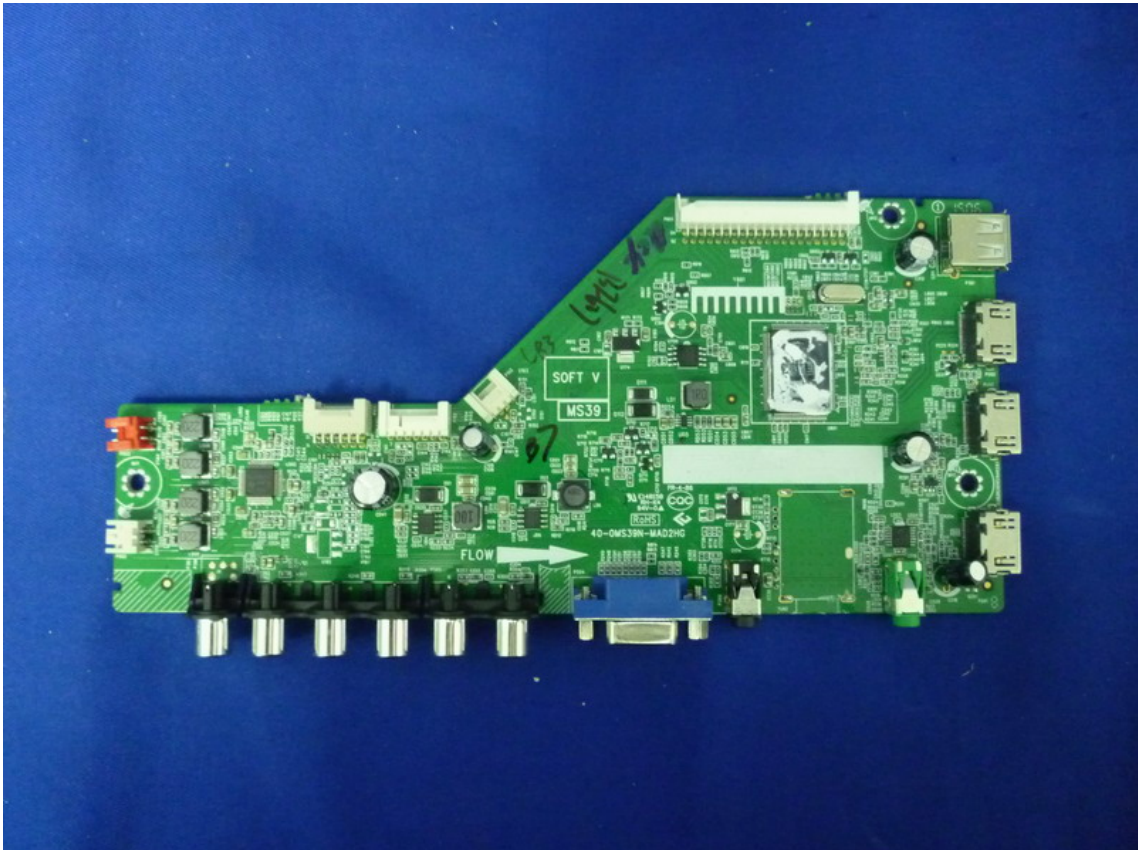


Figure 19
Backside of the Main Board

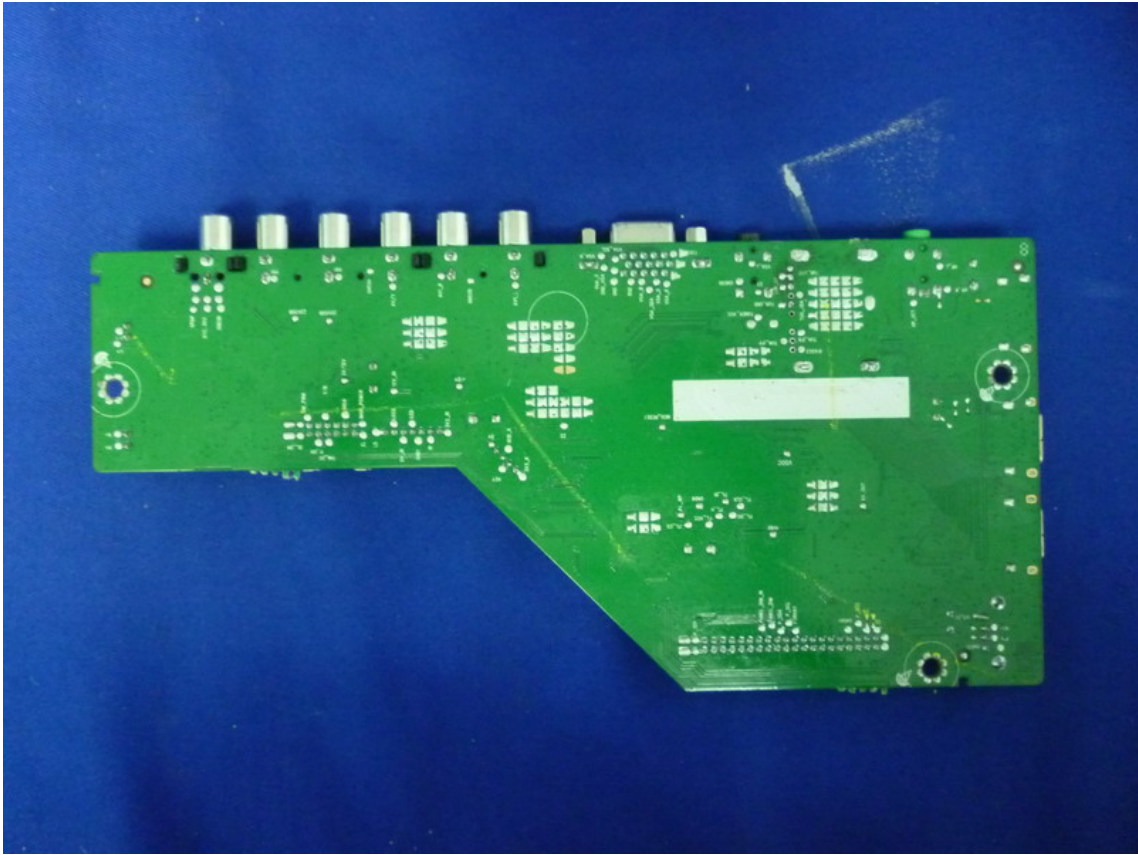


Figure 20
Frontside of the Power Board

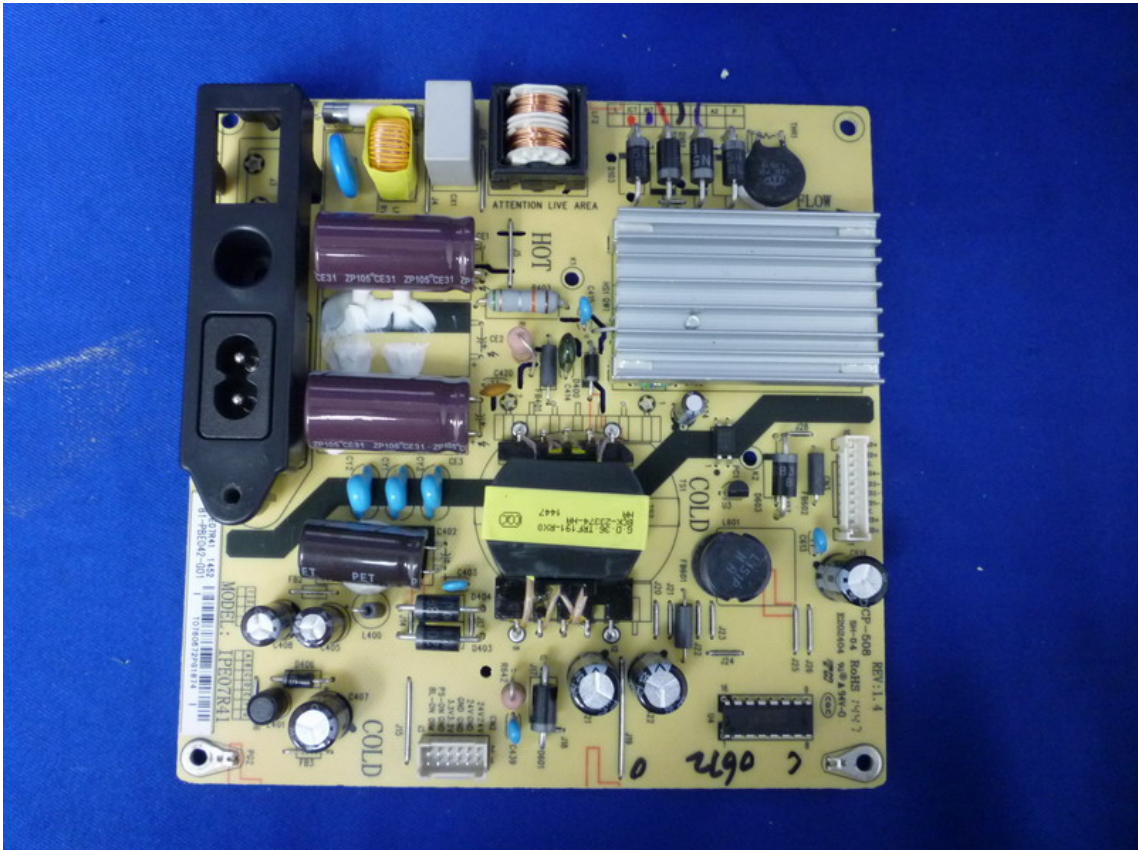


Figure 21
Backside of the Power Board

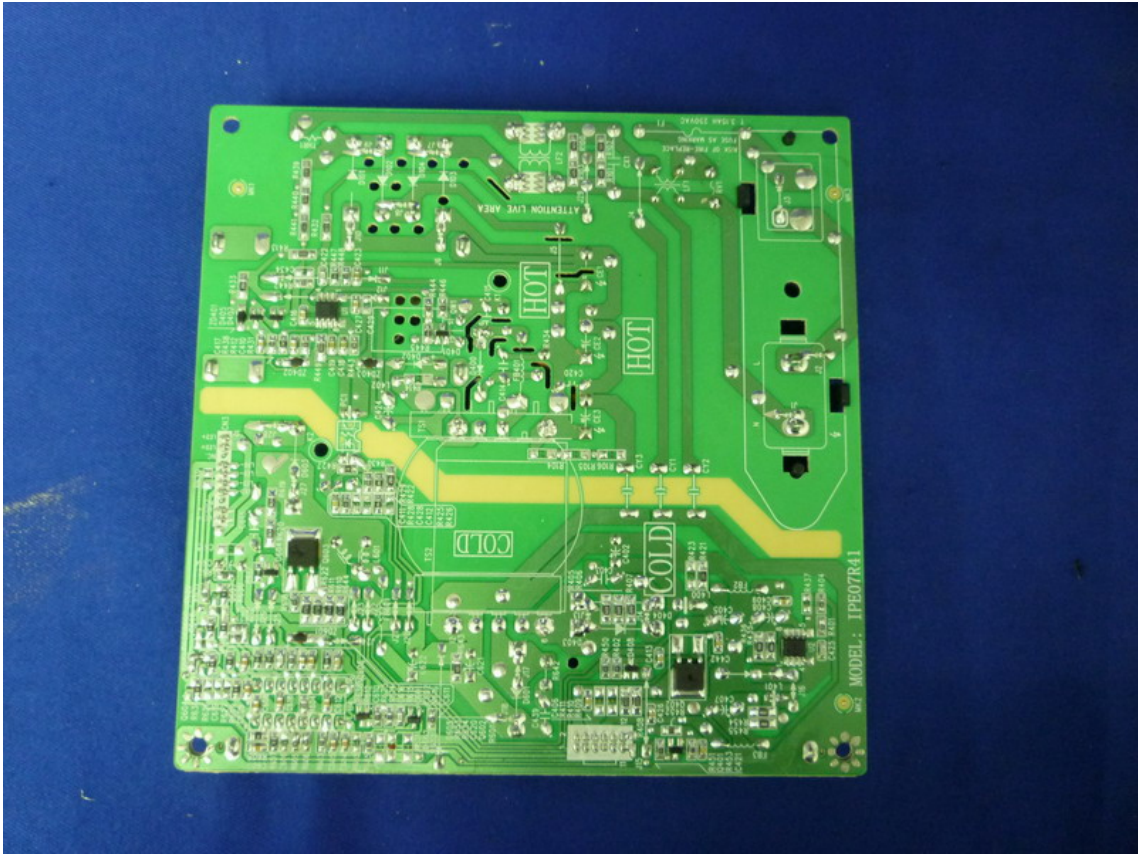


Figure 22
Frontside of the Power Board

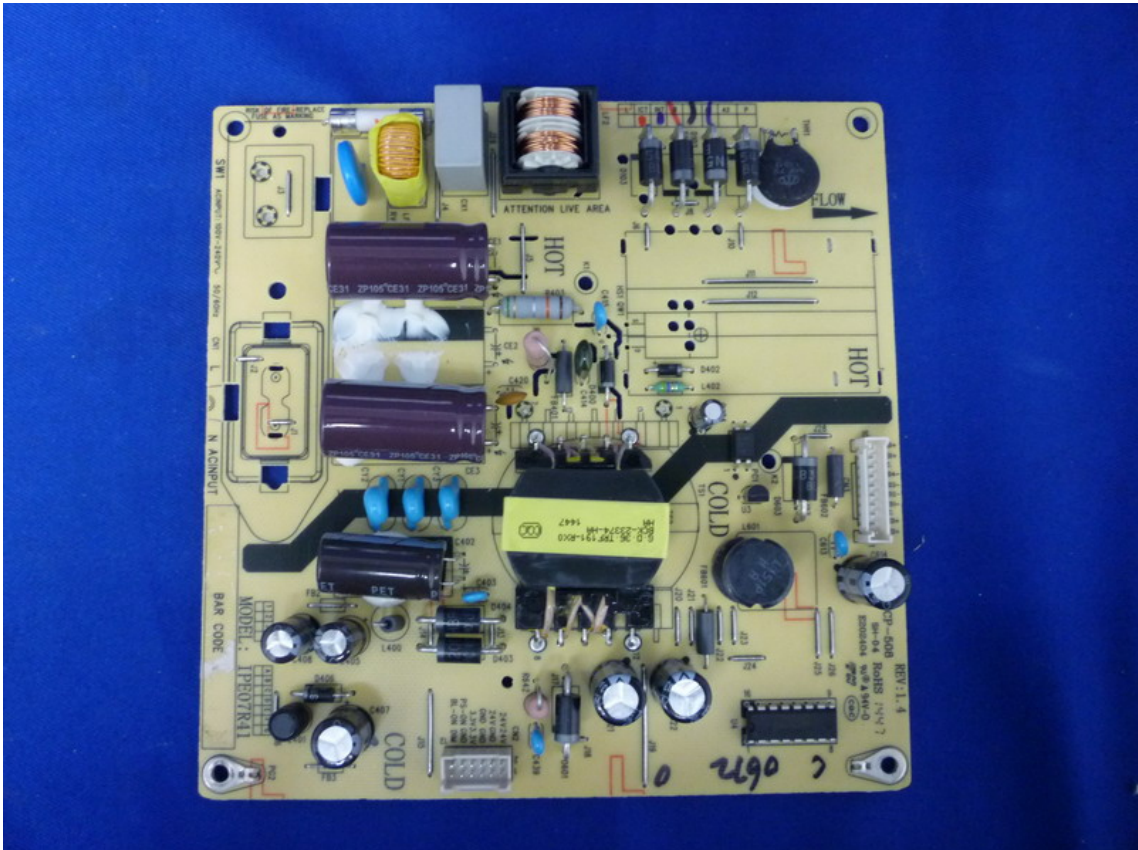


Figure 23
Backside of the Power Board

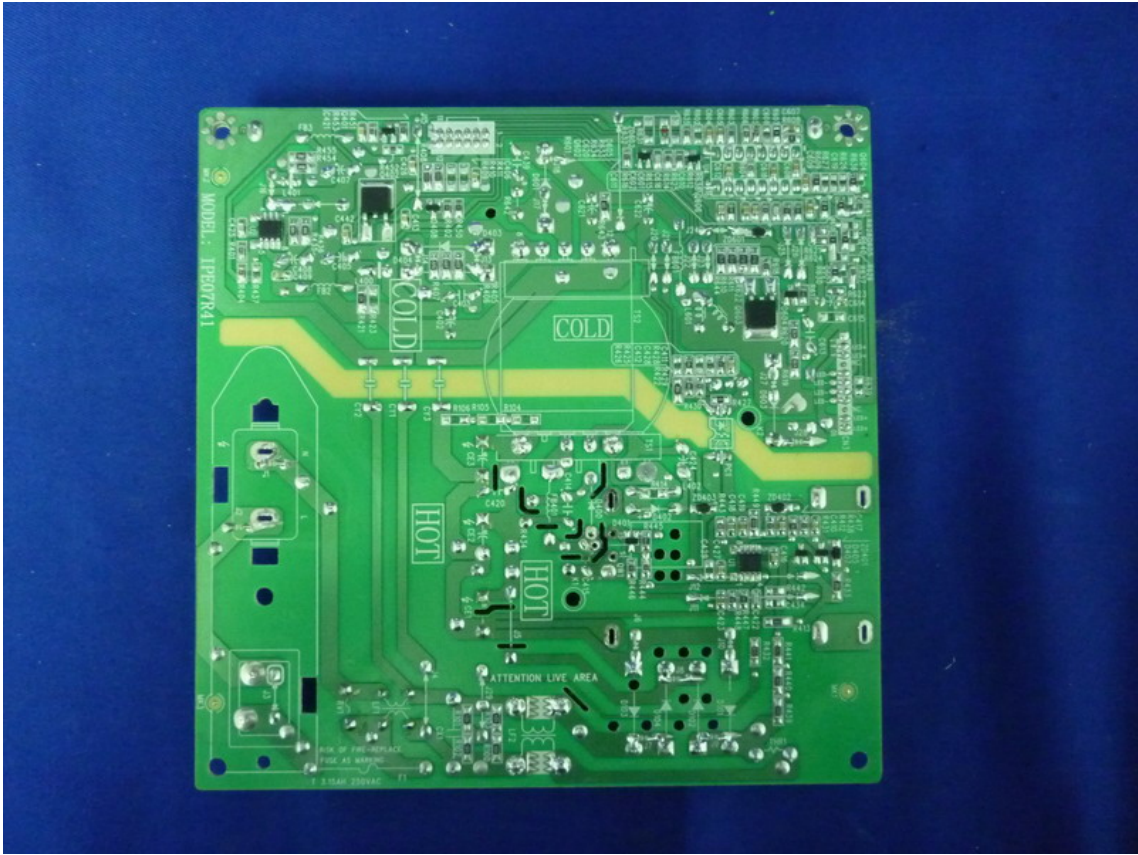


Figure 24
Frontside of the PCB Board

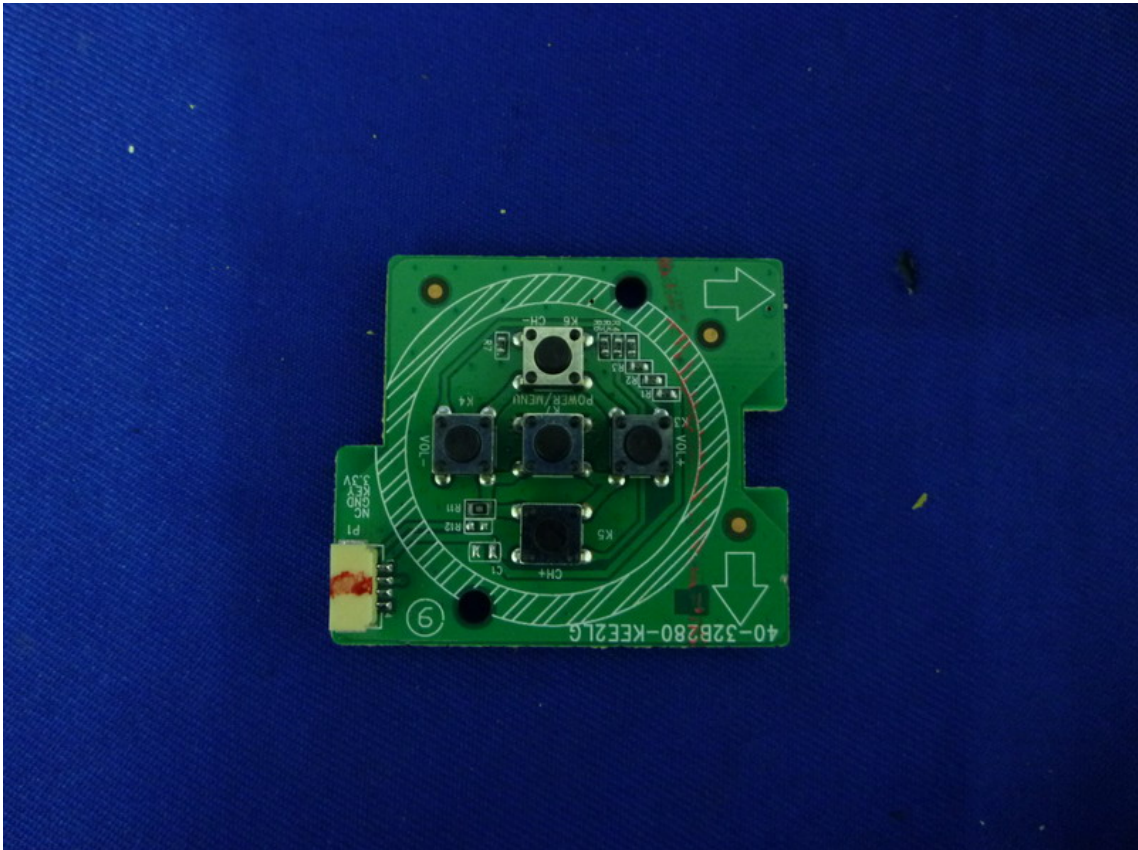


Figure 25
Backside of the PCB Board

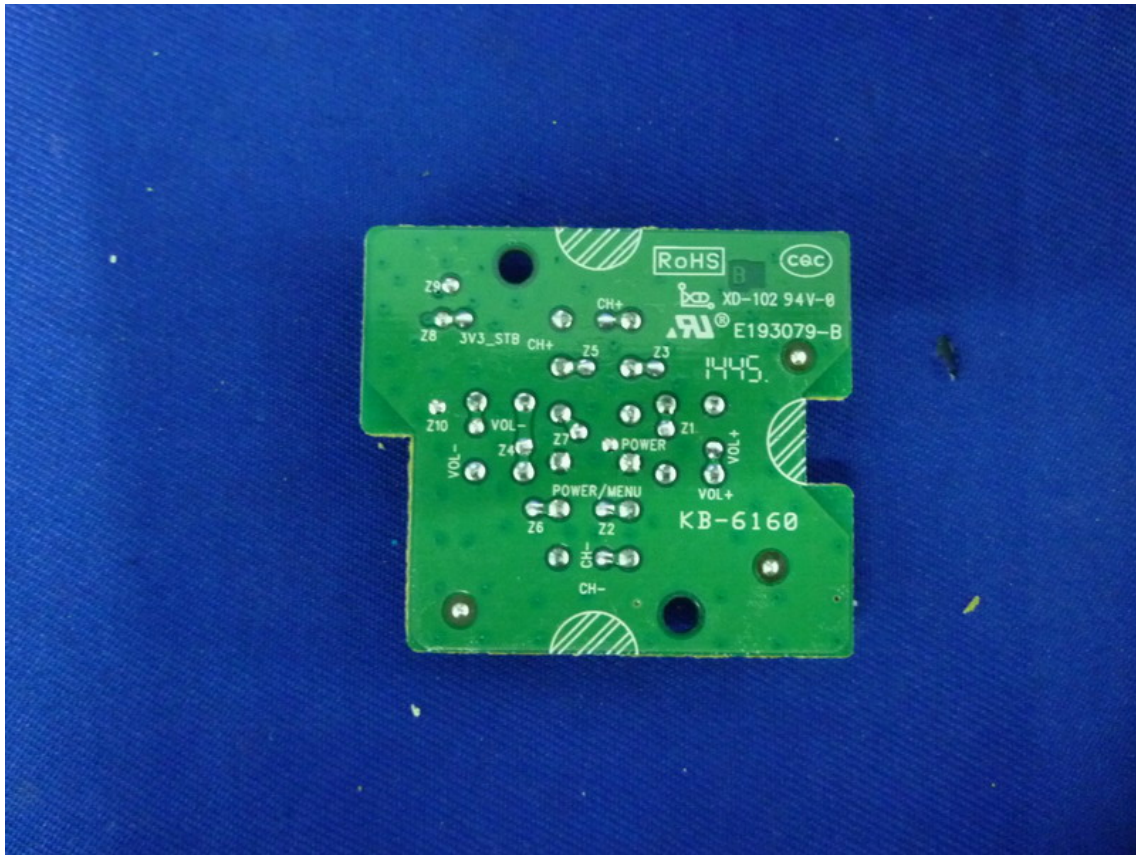


Figure 26
Frontside of the PCB Board



Figure 27
Backside of the PCB Board

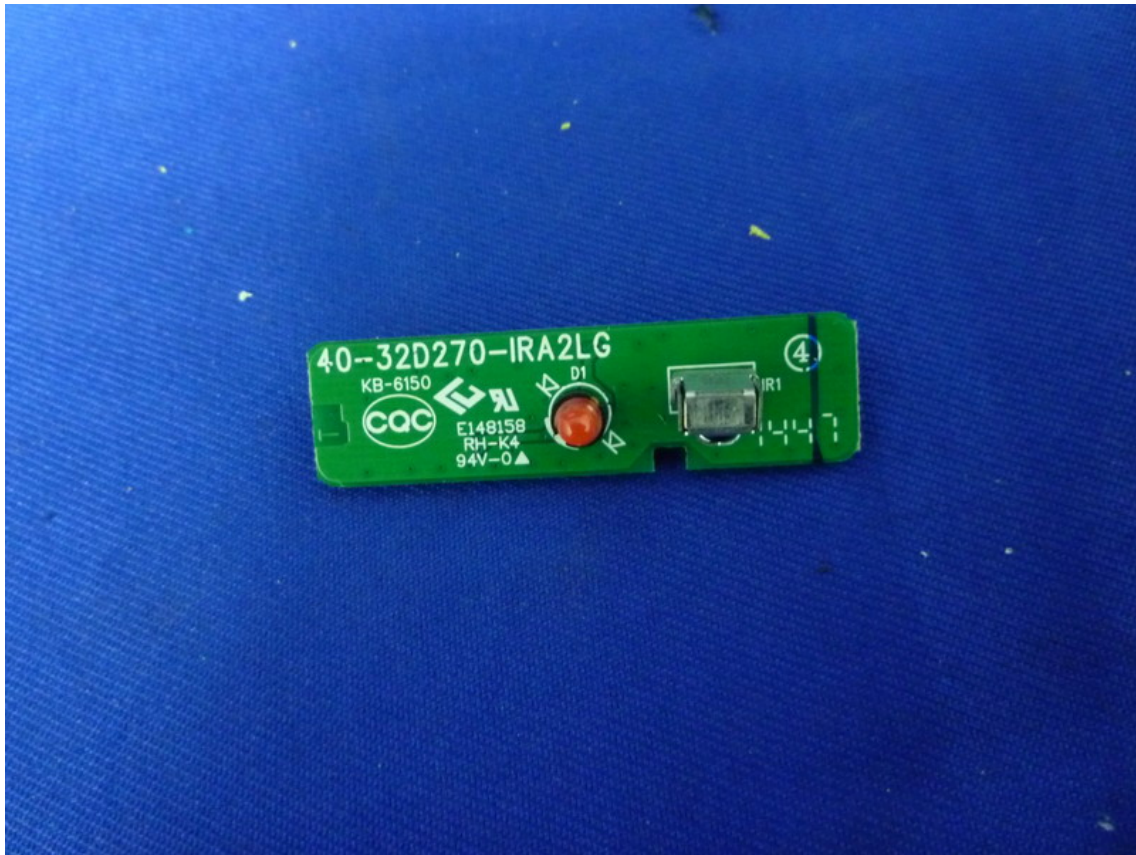


Figure 28
Speaker

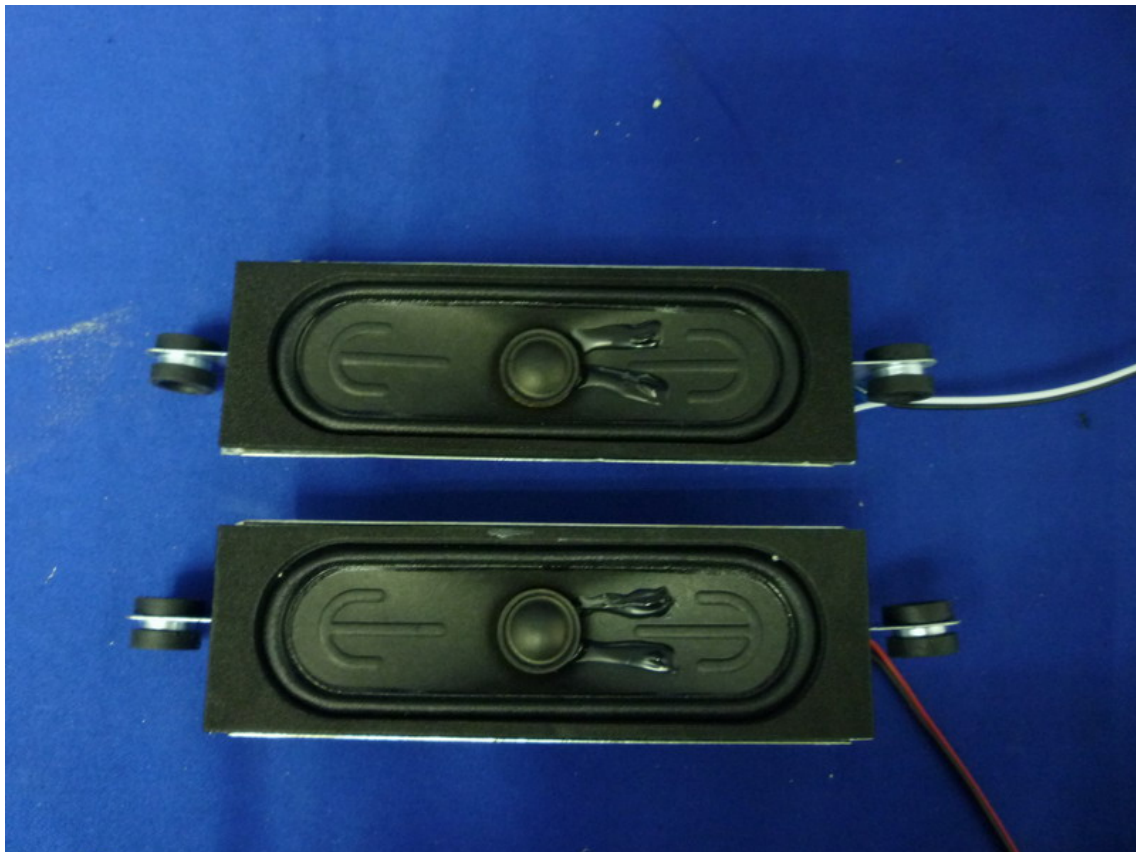


Figure 29
Speaker

