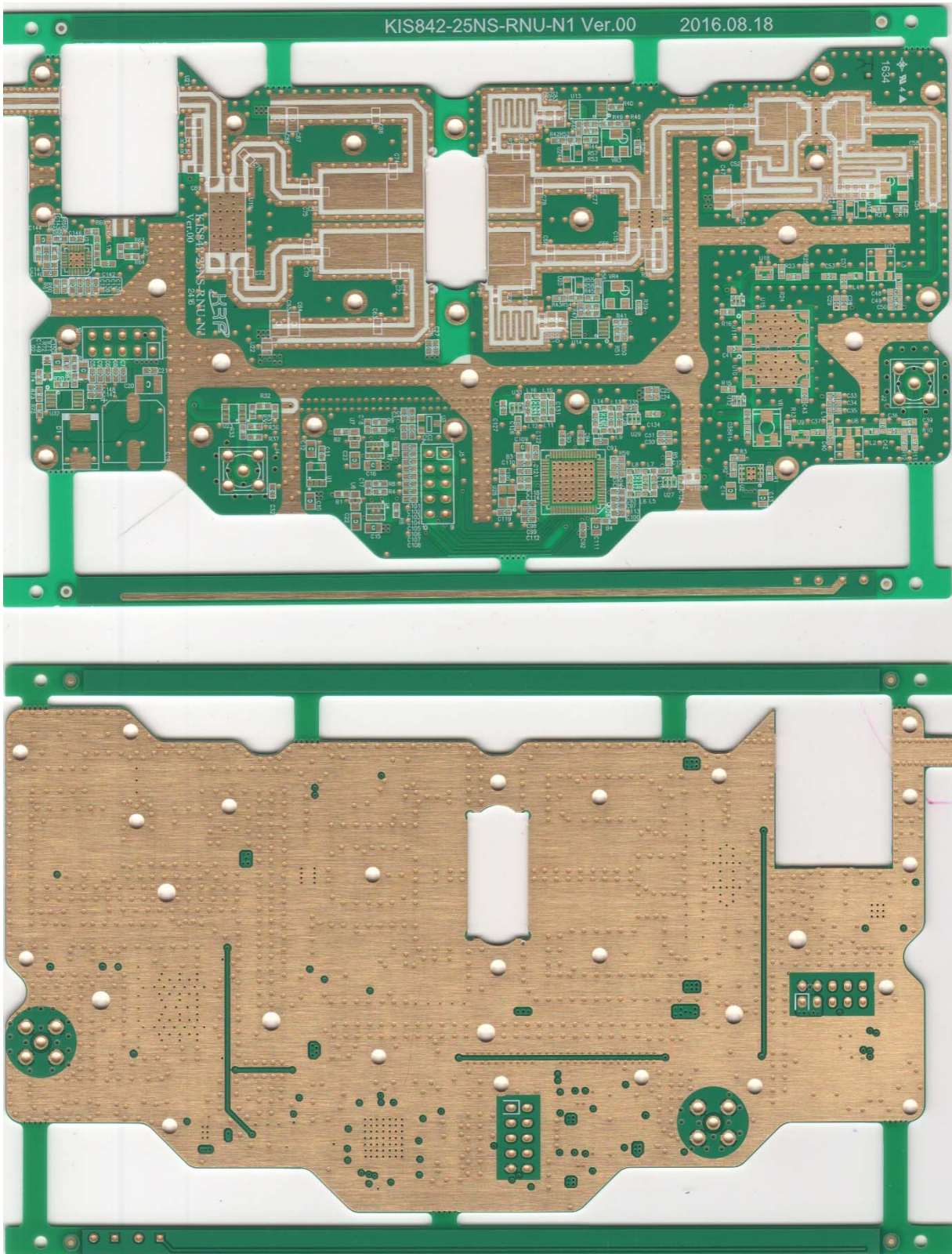


## INTERNAL PHOTOGRAPHS

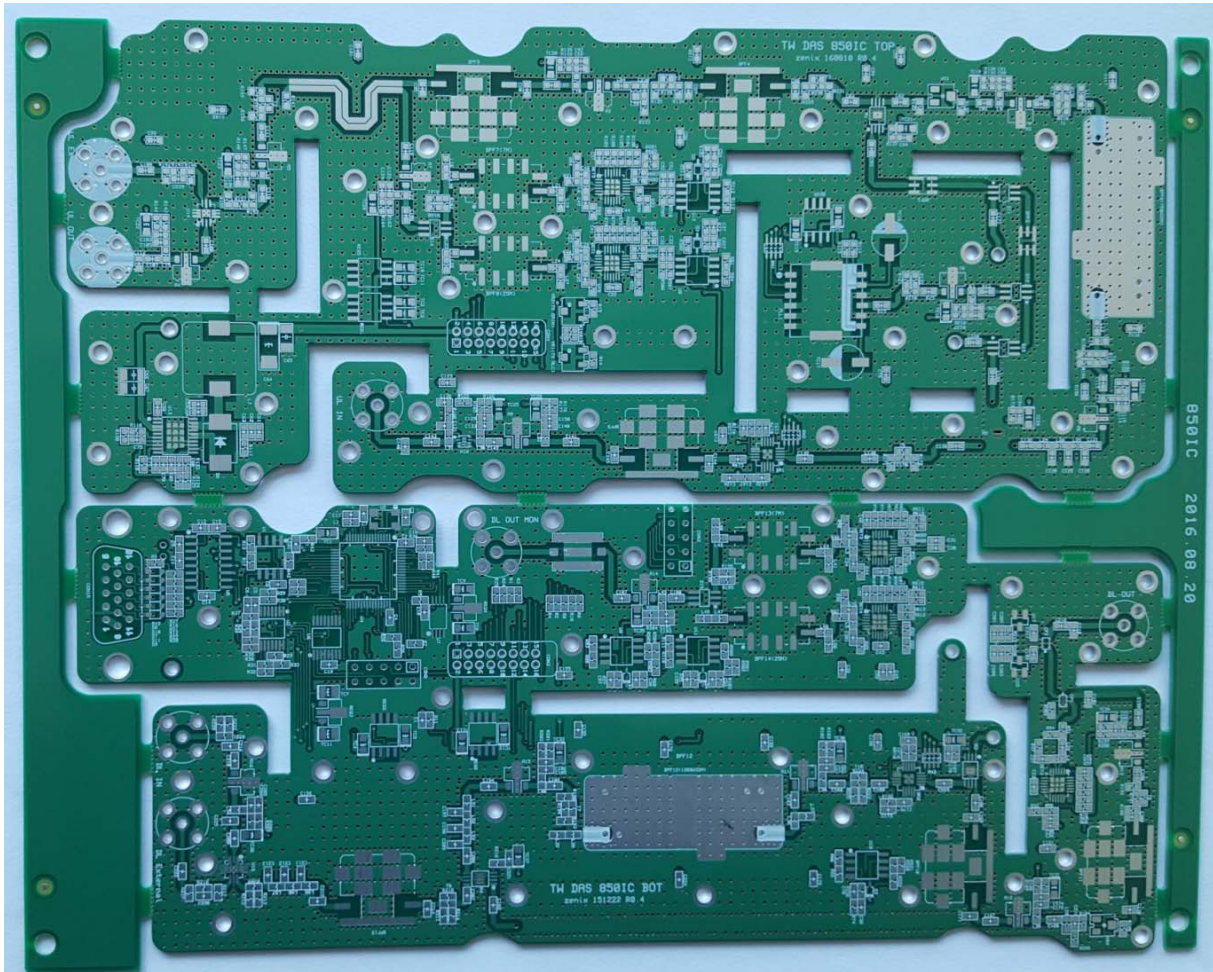
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PA PCB Bare Top & Bottom

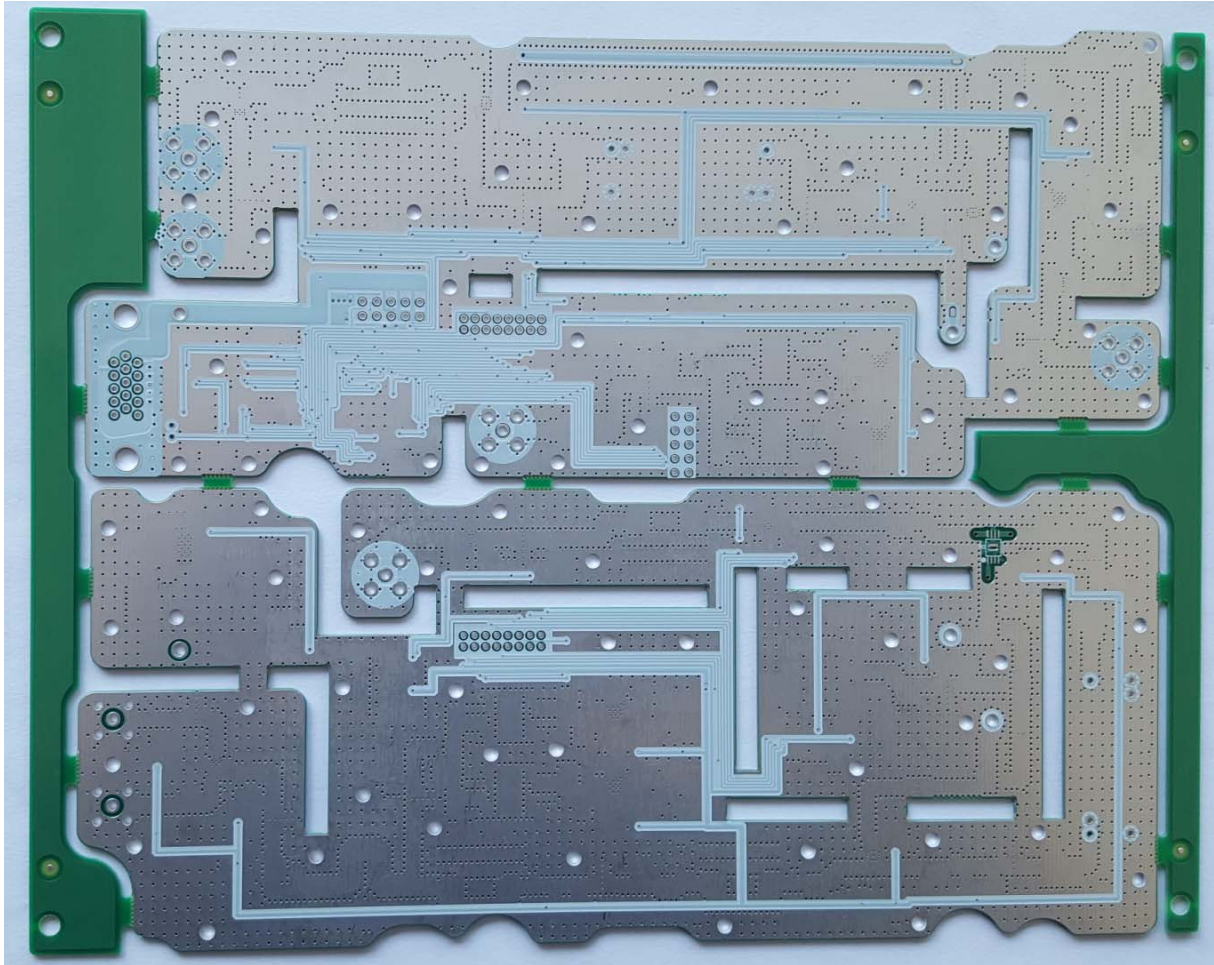




UDC Bare Top

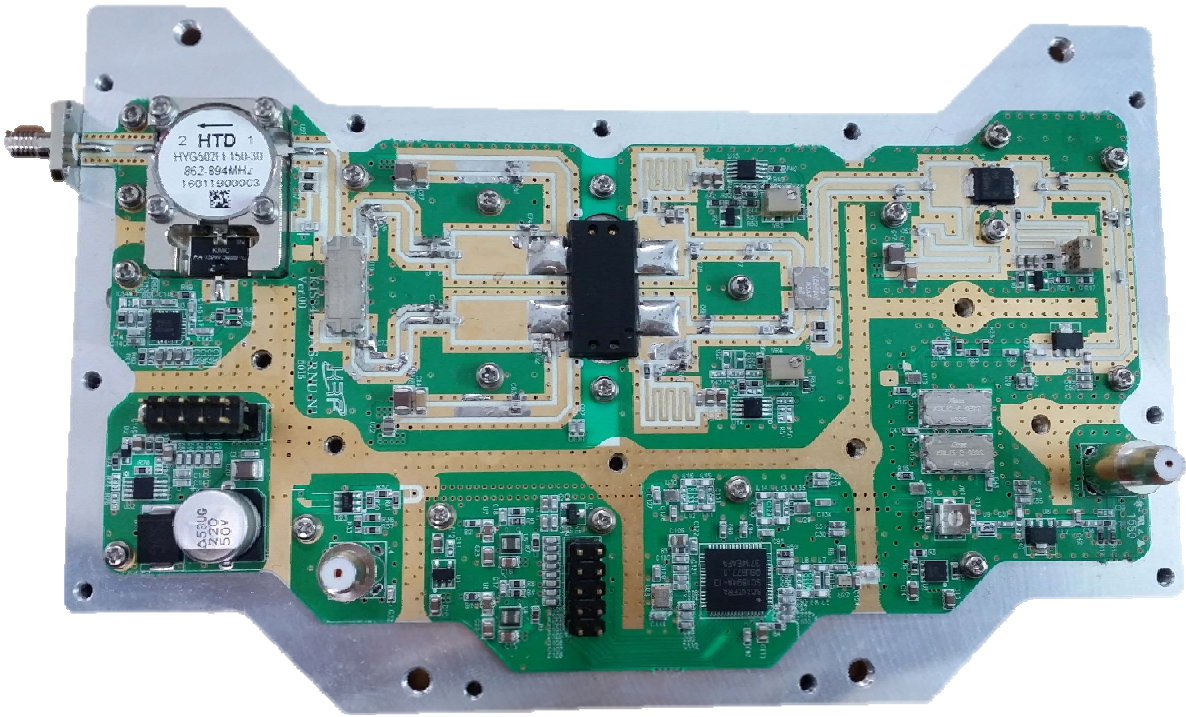


**UDC Bare Bottom**





**ASSEMBLE PA**



**ASSEMBLE UDC**

