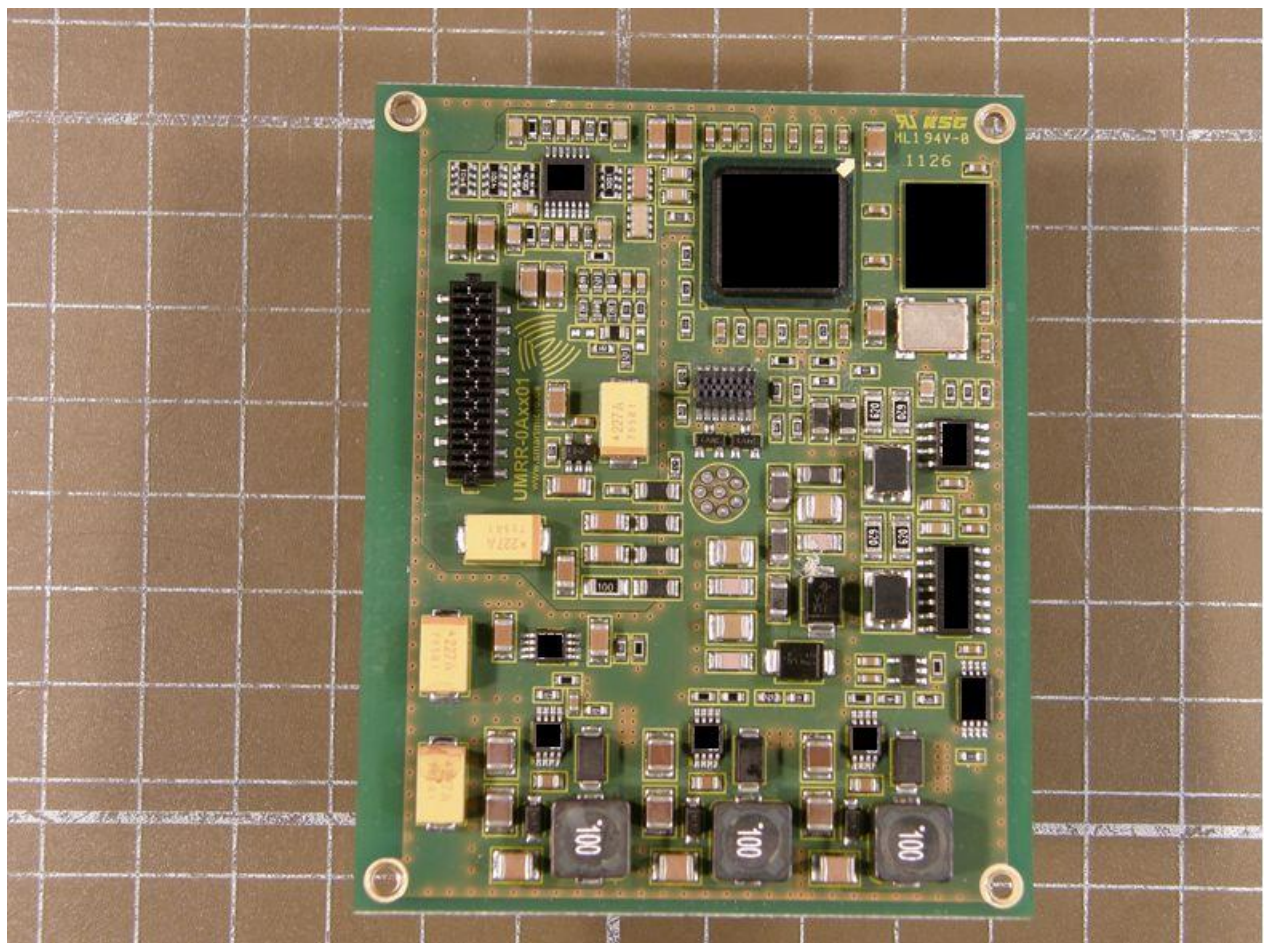


Annex no. 2

Internal Photographs

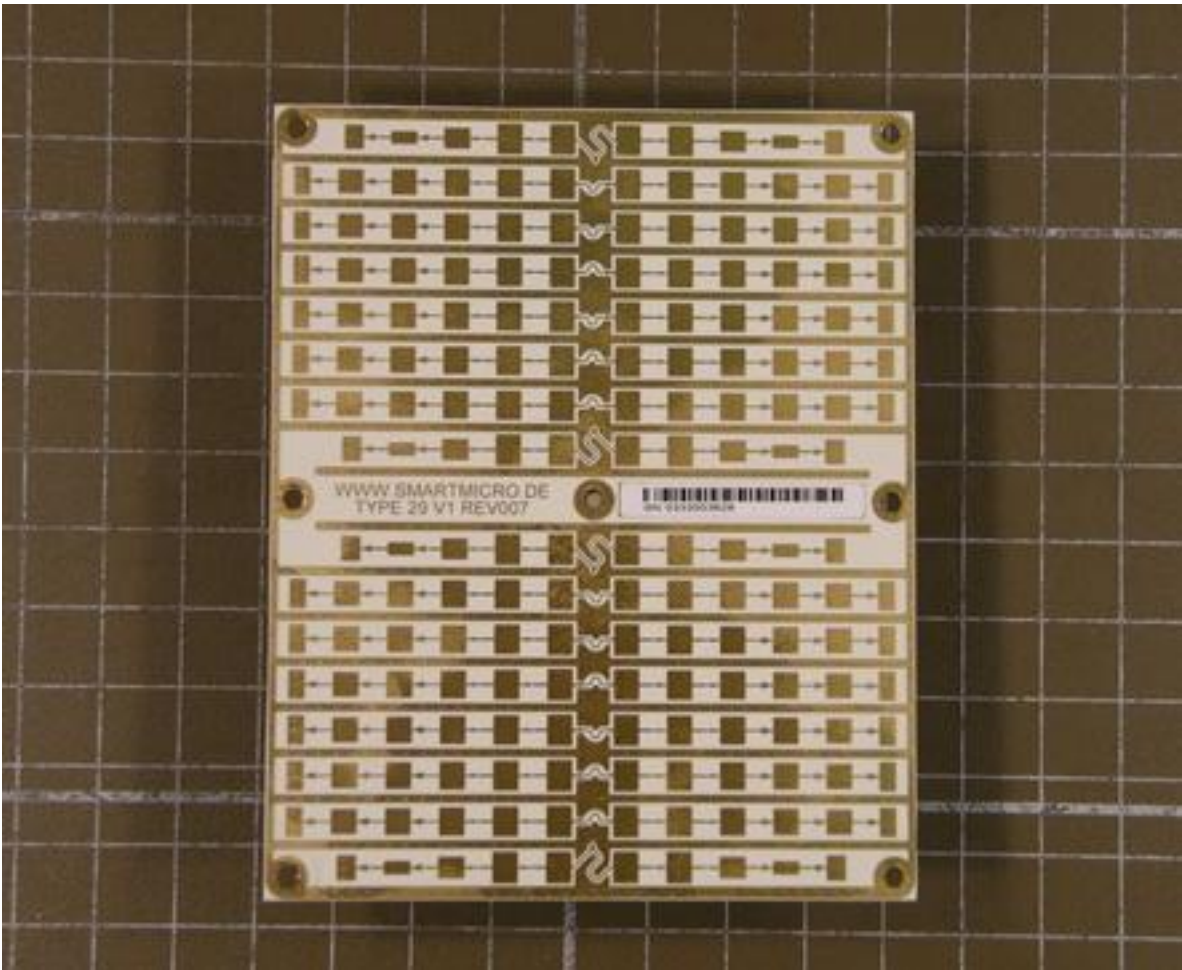
DSP Board (upper side) (UMRR-0Axxxx-1Dxxxx-05xxxx)



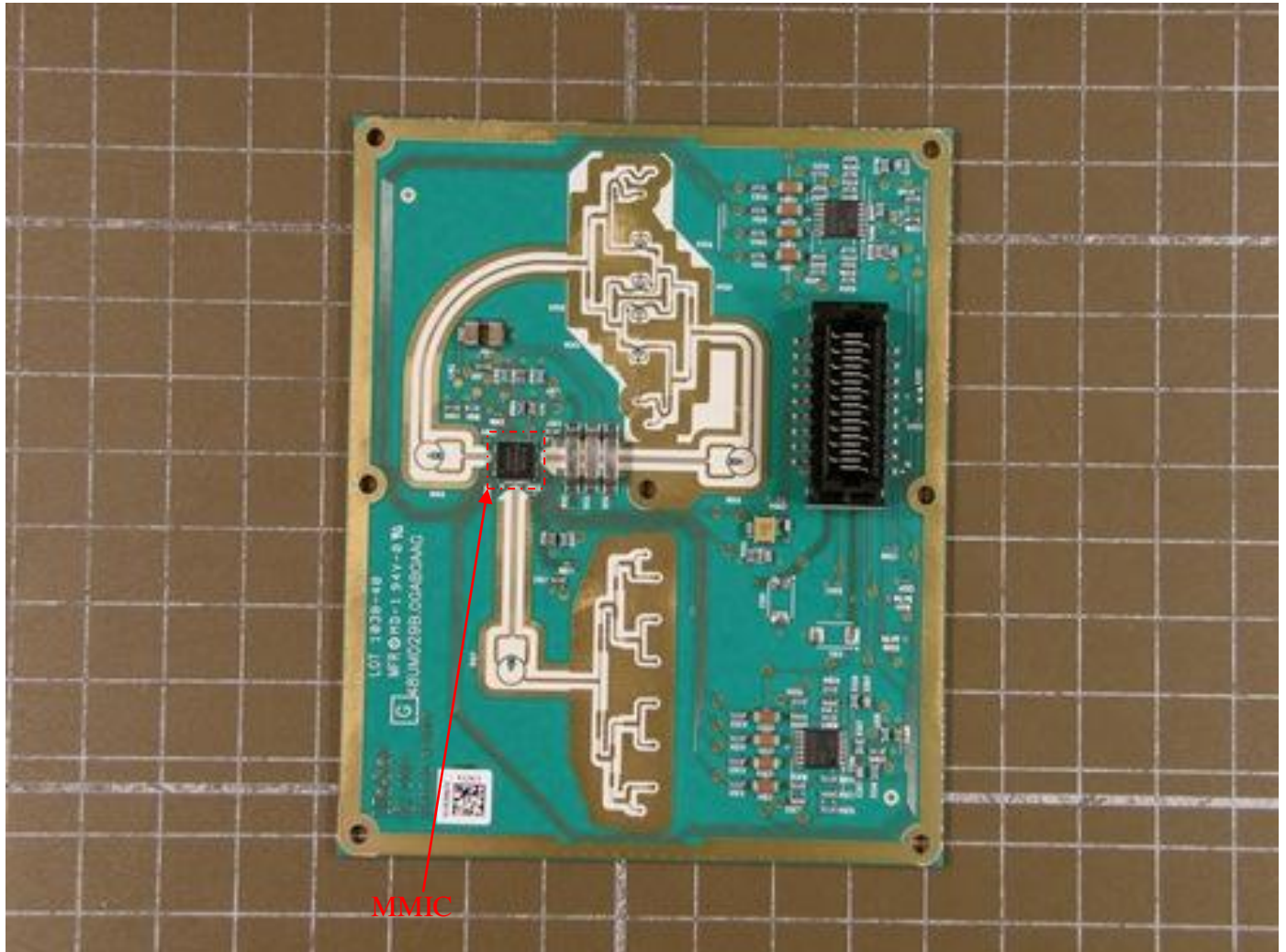
DSP Board (lower side) (UMRR-0Axxxx-1Dxxxx-05xxxx)



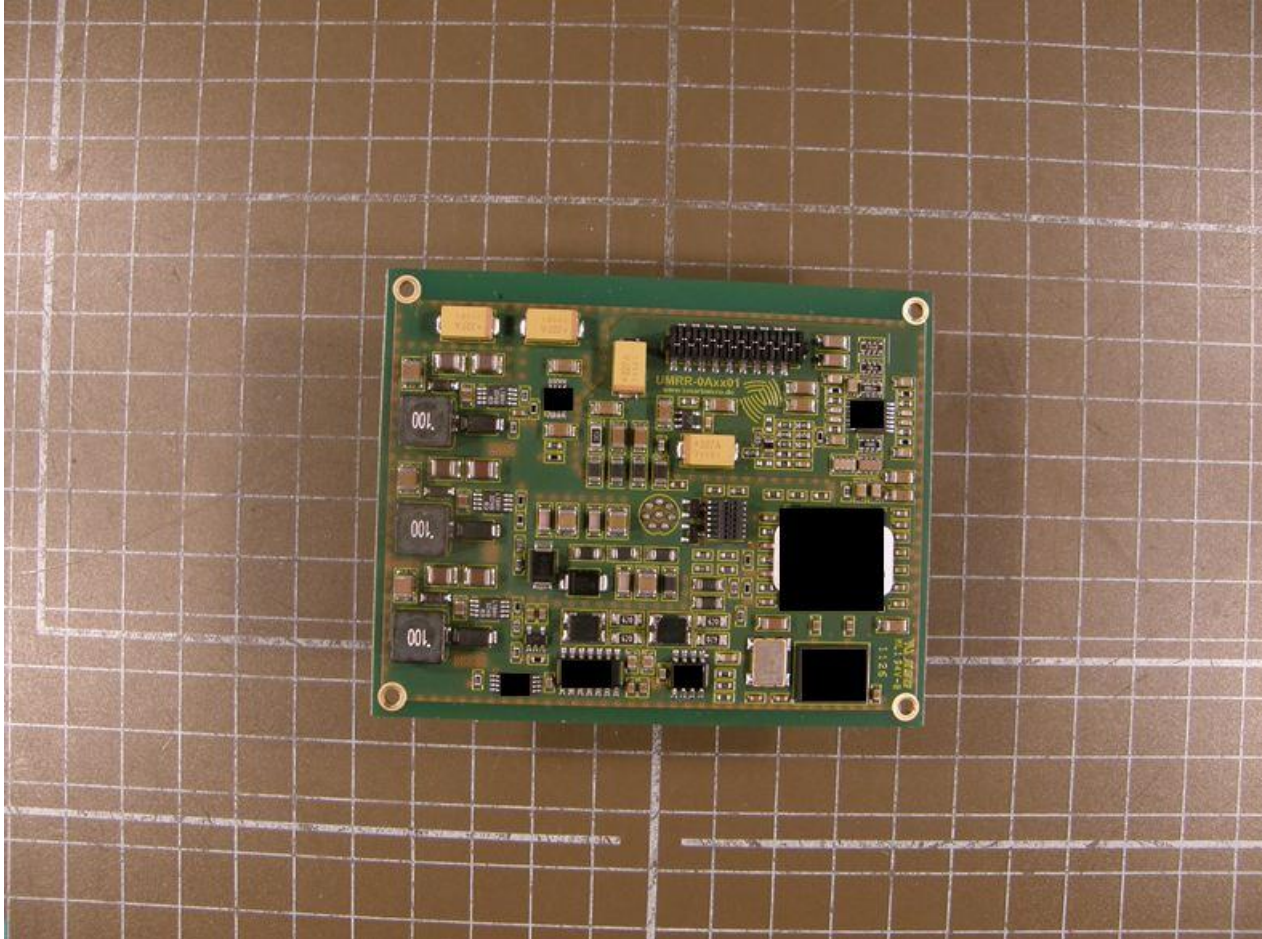
RF Transceiver board (upper side) (UMRR-0Axxxx-1Dxxxx-05xxxx)



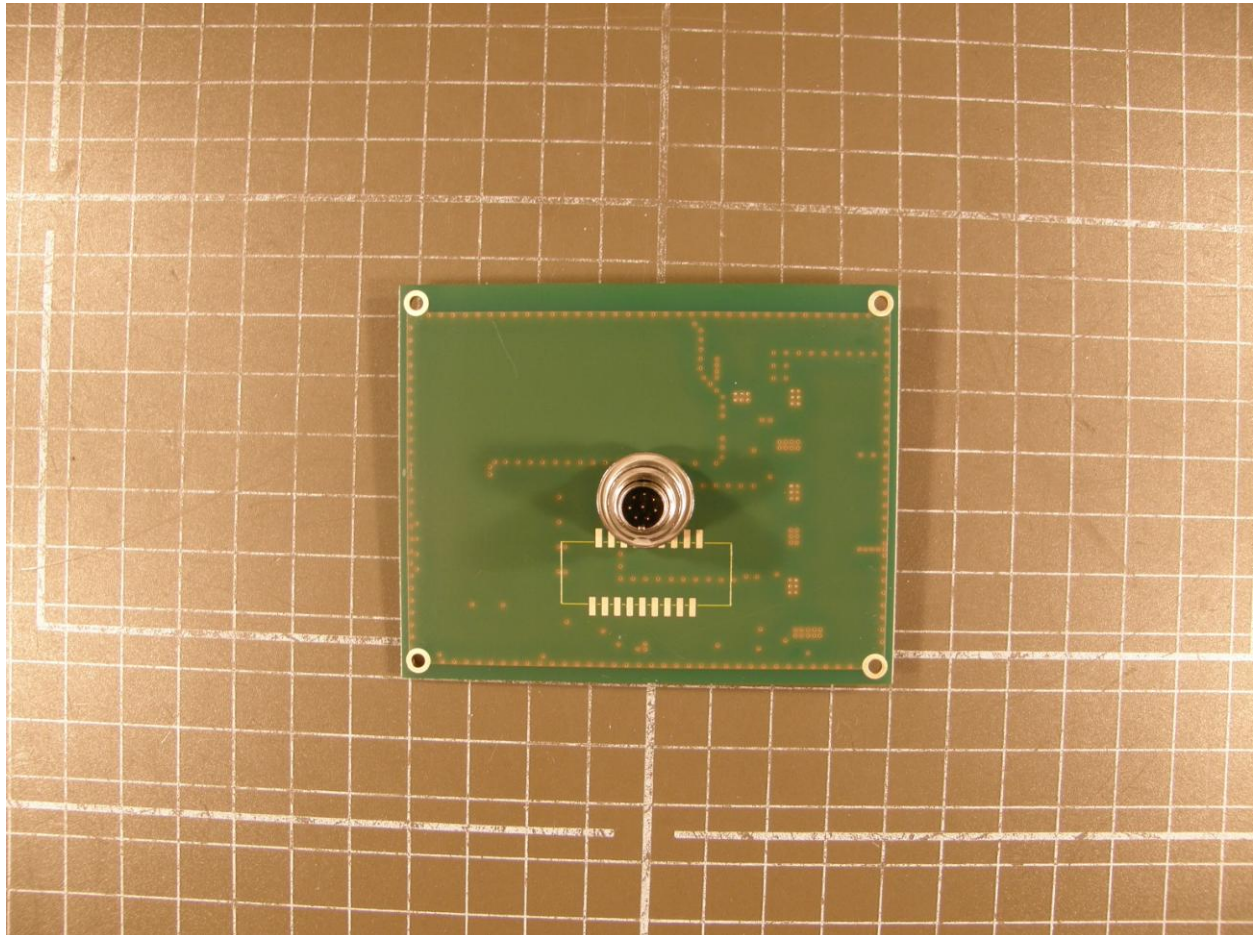
RF Transceiver board (lower side) (UMRR-0Axxxx-1Dxxxx-05xxxx)



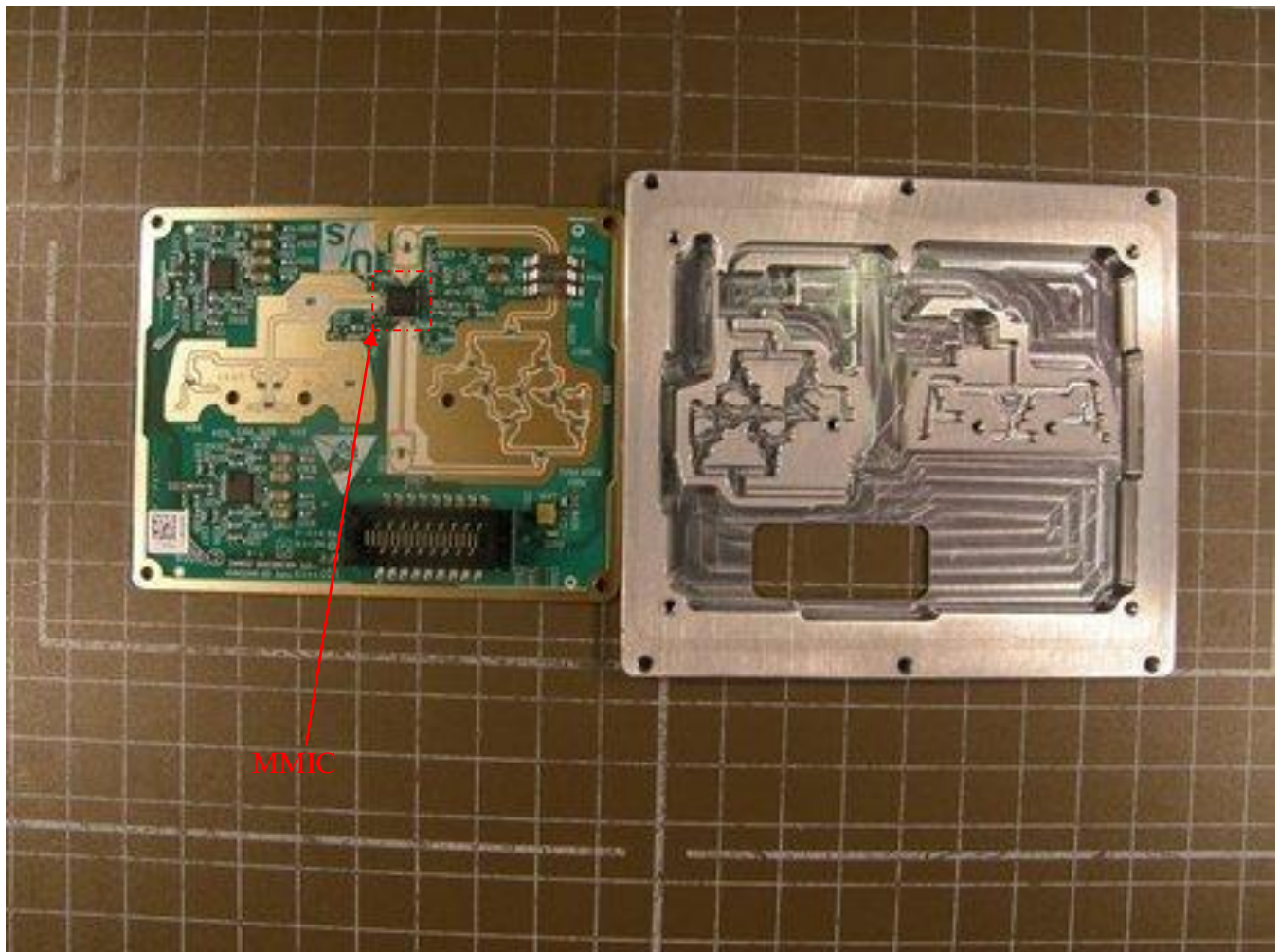
DSP Board (upper side) (UMRR-0Axxxx-1Exxxx-0306xx)



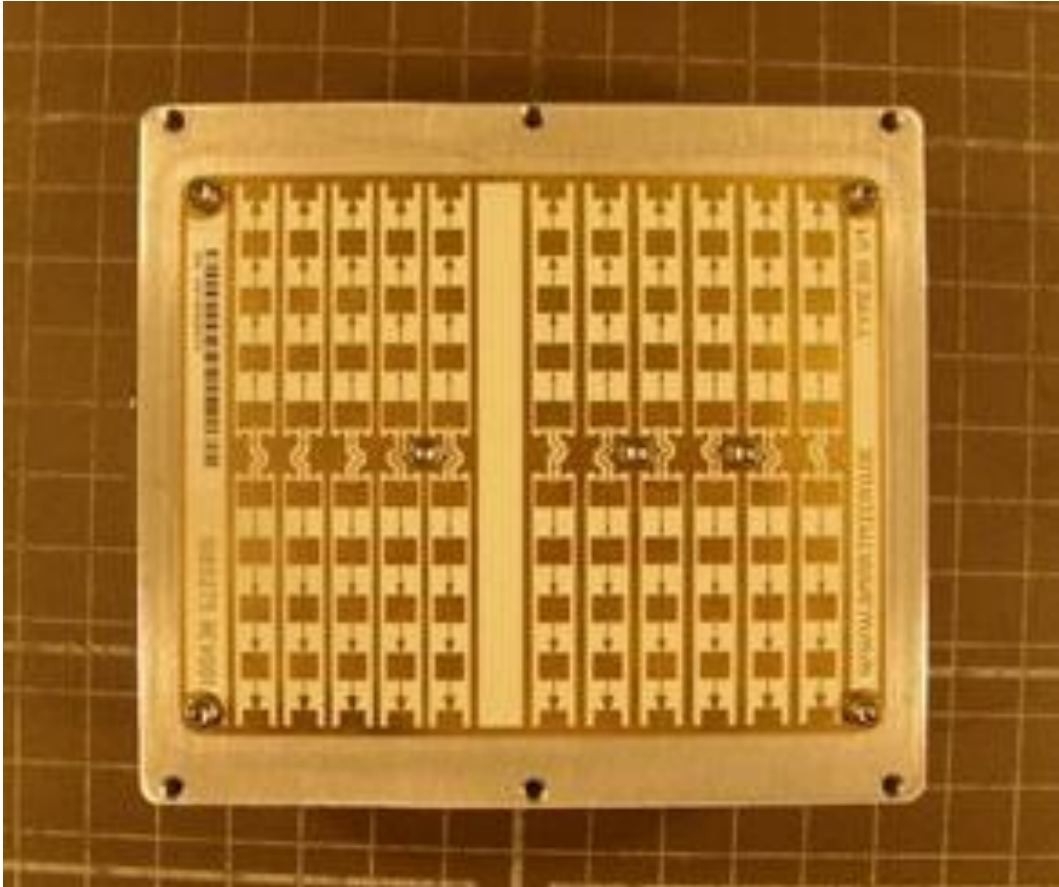
DSP Board (lower side) (UMRR-0Axxxx-1Exxxx-0306xx)



RF Transceiver board (upper side) (UMRR-0Axxxx-1Exxxx-0306xx)



RF Transceiver board (lower side) (UMRR-0Axxxx-1Exxxx-0306xx)



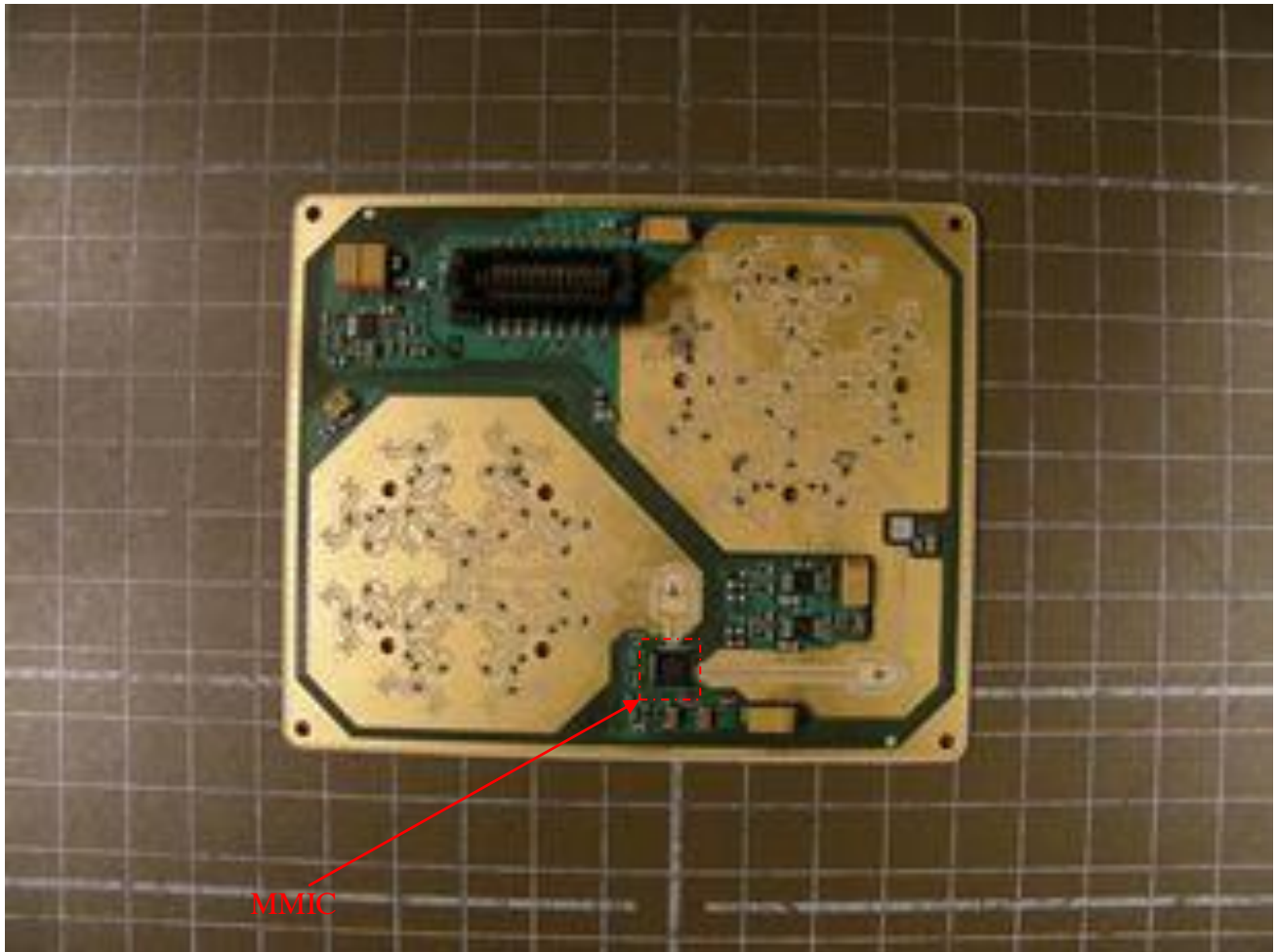
DSP Board (upper side) (UMRR-0Axxxx-22xxxx-05xxxx)



DSP Board (lower side) (UMRR-0Axxxx-22xxxx-05xxxx)



RF Transceiver board (upper side) (UMRR-0Axxxx-22xxxx-05xxxx)



RF Transceiver board (lower side) (UMRR-0Axxxx-22xxxx-05xxxx)

