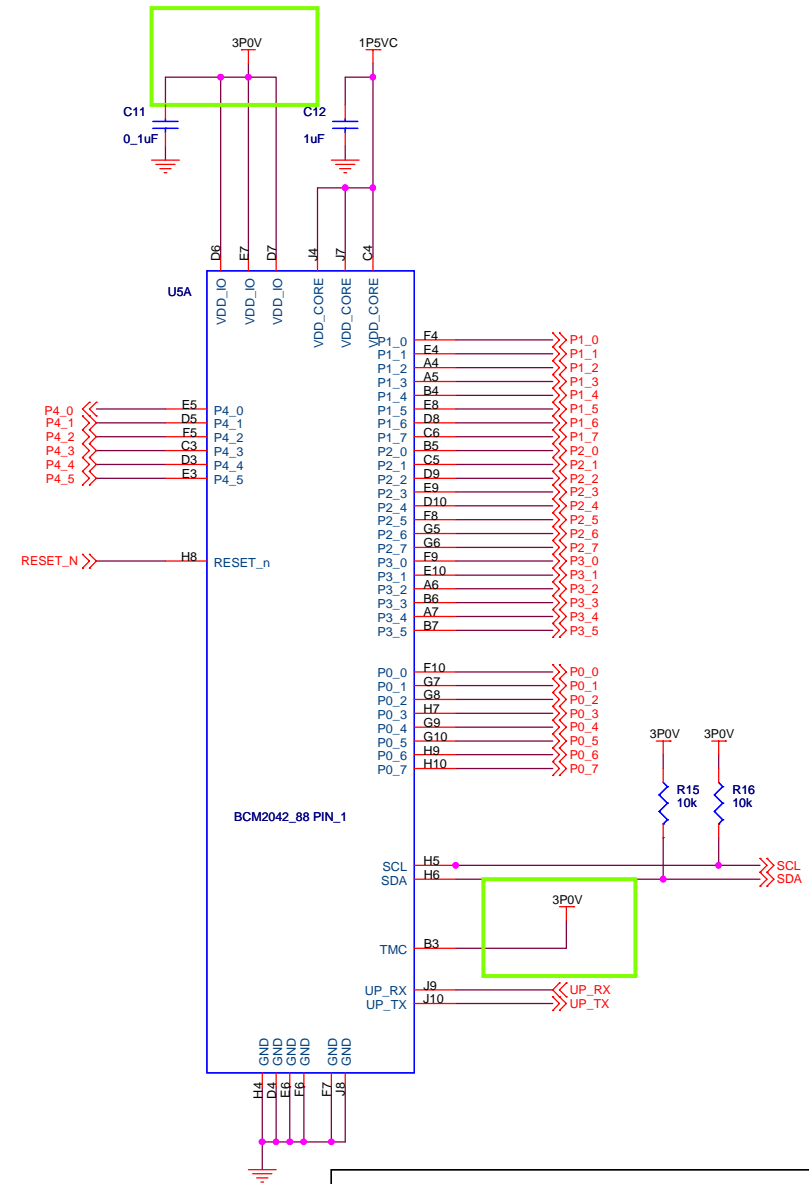
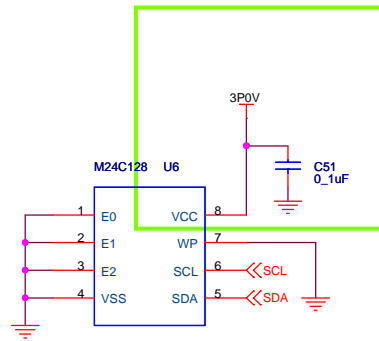
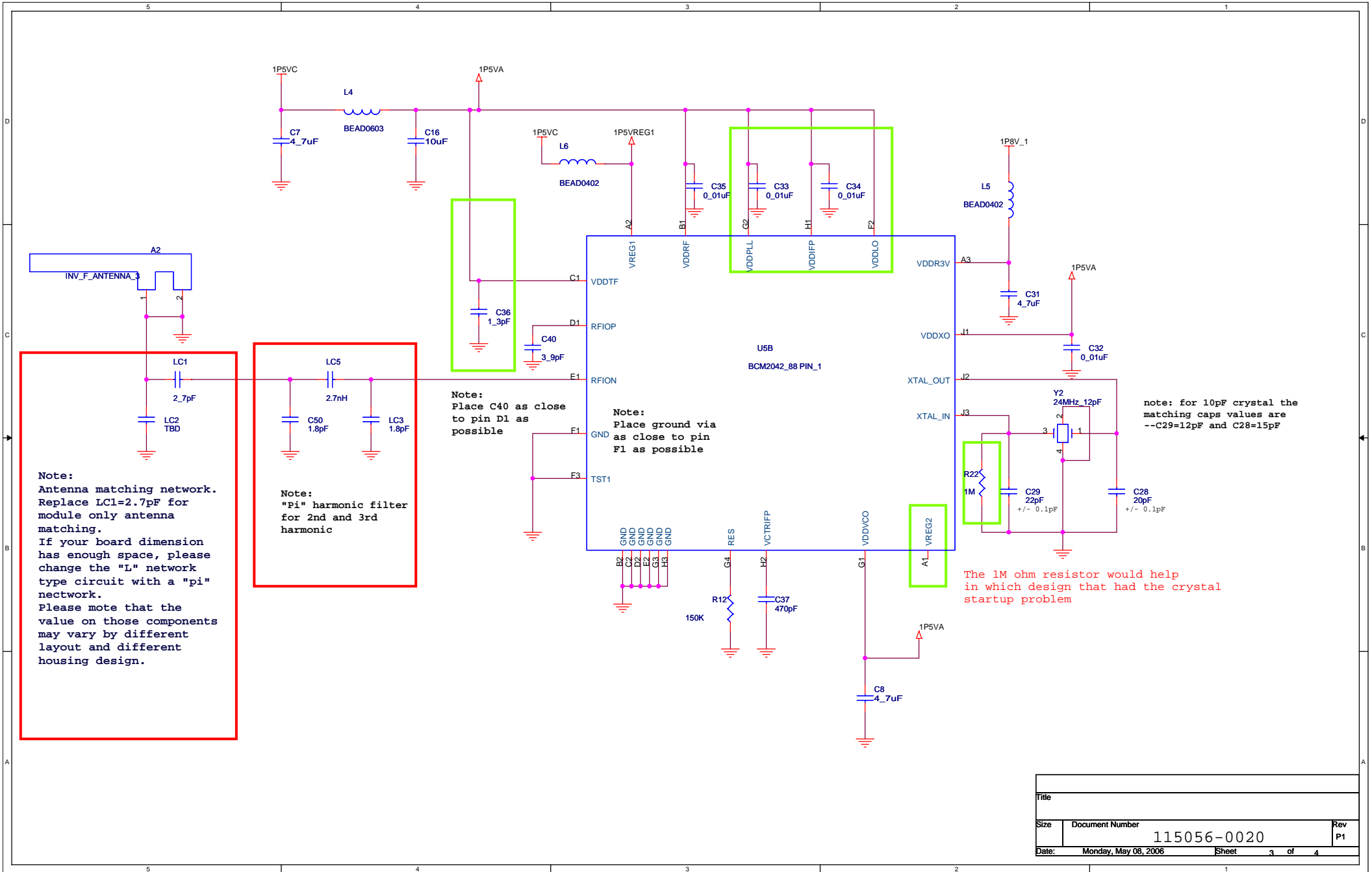


Note: The switching regulator is configured for 2 battery cells in serial.



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Note:
Antenna matching network.
Replace LC1=2.7pF for
module only antenna
matching.
If your board dimension
has enough space, please
change the "L" network
type circuit with a "pi"
network.
Please note that the
value on those components
may vary by different
layout and different
housing design.

Note:
"Pi" harmonic filter
for 2nd and 3rd
harmonic

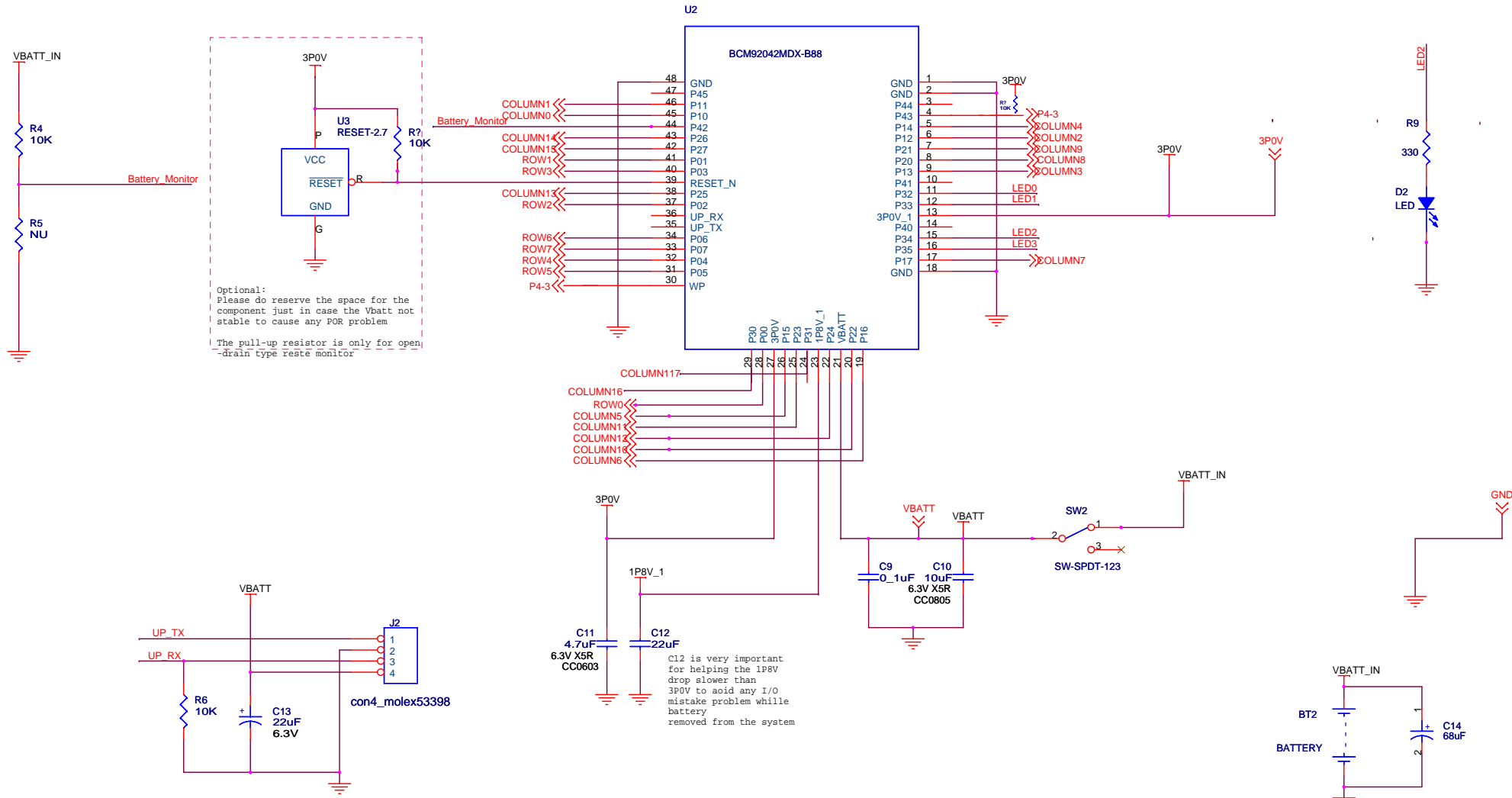
Note:
Place C40 as close
to pin D1 as
possible

Note:
Place ground via
as close to pin
F1 as possible

The 1M ohm resistor would help
in which design that had the crystal
startup problem

note: for 10pF crystal the
matching caps values are
--C29=12pF and C28=15pF

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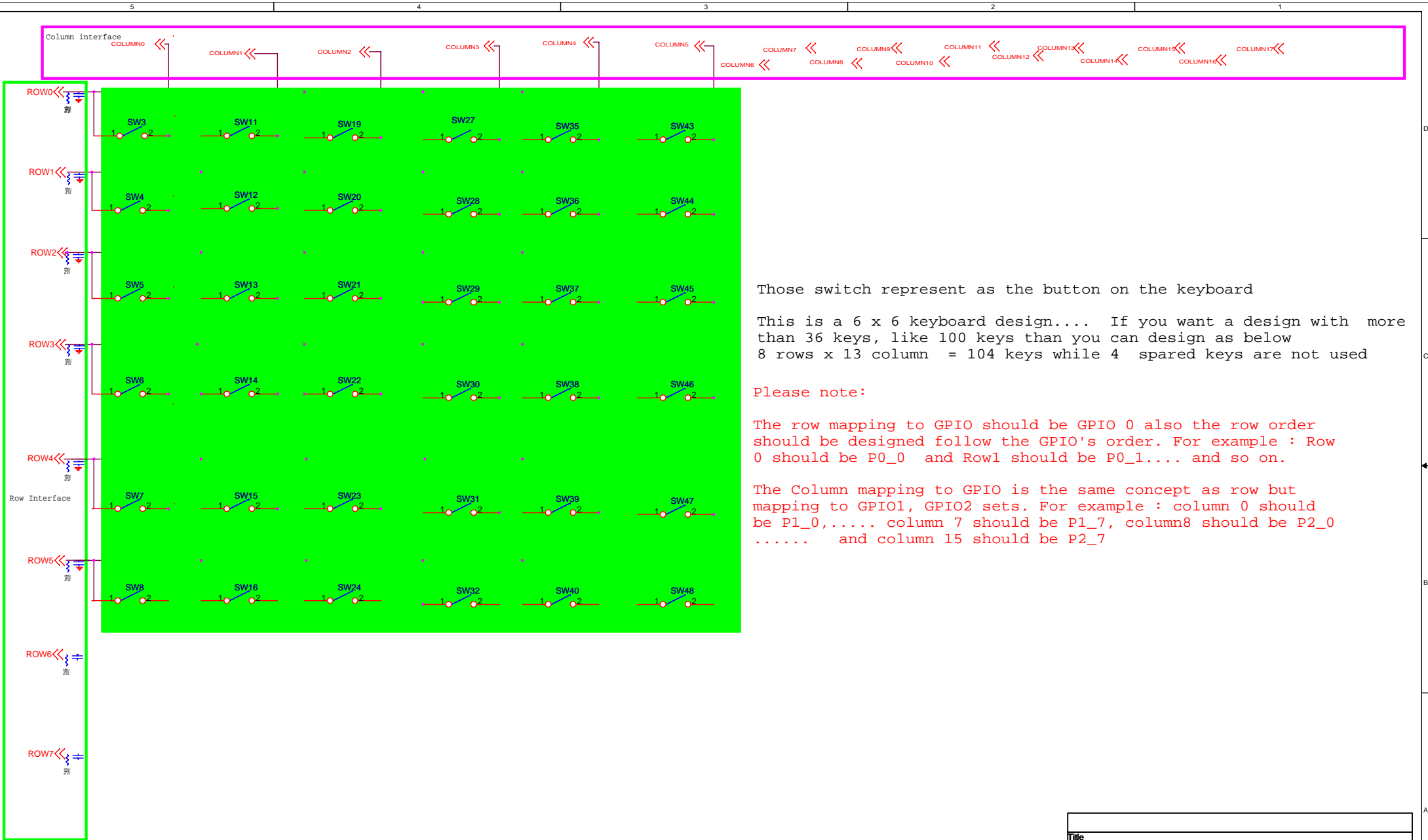
Optional:
Please do reserve the space for the component just in case the Vbatt not stable to cause any POR problem
The pull-up resistor is only for open-drain type reset monitor

C12 is very important for helping the 1P8V drop slower than 3P0V to avoid any I/O mistake problem while battery removed from the system

Note: Serial Interface Port

NOTE: 2 AA BATTERIES in series

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Those switch represent as the button on the keyboard

This is a 6 x 6 keyboard design.... If you want a design with more than 36 keys, like 100 keys than you can design as below
 8 rows x 13 column = 104 keys while 4 spared keys are not used

Please note:

The row mapping to GPIO should be GPIO 0 also the row order should be designed follow the GPIO's order. For example : Row 0 should be P0_0 and Row1 should be P0_1.... and so on.

The Column mapping to GPIO is the same concept as row but mapping to GPIO1, GPIO2 sets. For example : column 0 should be P1_0,..... column 7 should be P1_7, column8 should be P2_0 and column 15 should be P2_7

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