

Main chip part:

The chip MP1482 supplies the board main voltage +3.3V. +3.3V generate +2.5V by an audion Q1, +1.2V by a MOS tube U16. These three voltage driver the main chip BCM6358R. A 64MHz crystal oscillator provides the clock to the main chip. The main chip provides a 133MHz clock with DDR SDRAM and provides a 33MHz PCI clock with the BCM43222 and parallel flash

Wireless part:

The BCM43222 wireless chip required 2 voltages. One is the board main voltage +3.3V, the other is a +1.2V alone generated by MP1482. A 20MHz crystal oscillator provides the clock to the wireless chip. The wireless chip provides an 802.11b/g/n wireless solution. The maximum support rate is 270Mbps. The wireless chip and the main chip connection via PCI bus, it is a 33MHz clock bus.

100M eth port:

BCM6358R support 4 100M Ethernet port.