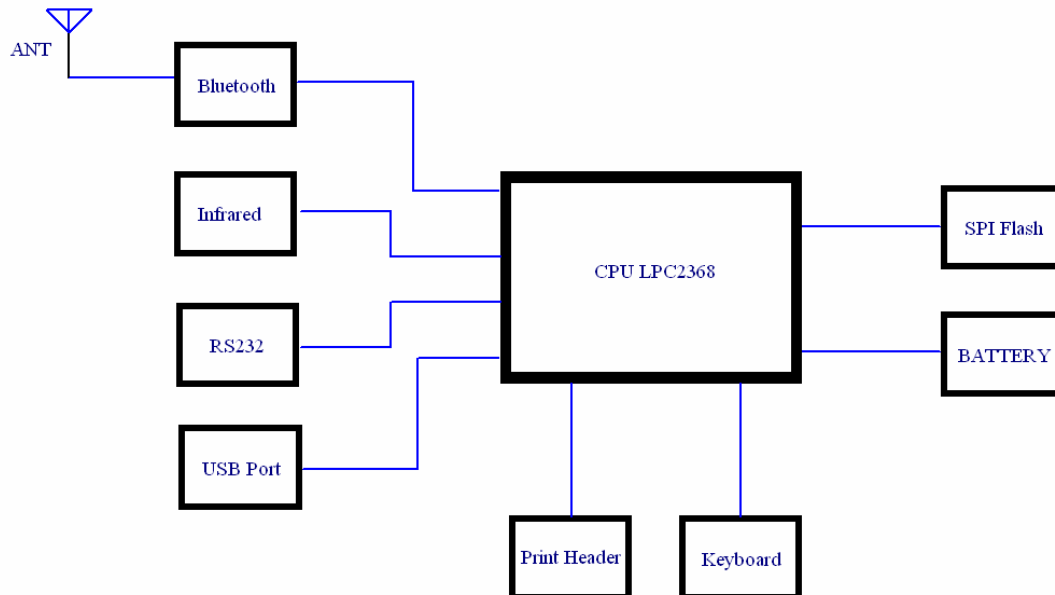


M03 description

1 Block diagram

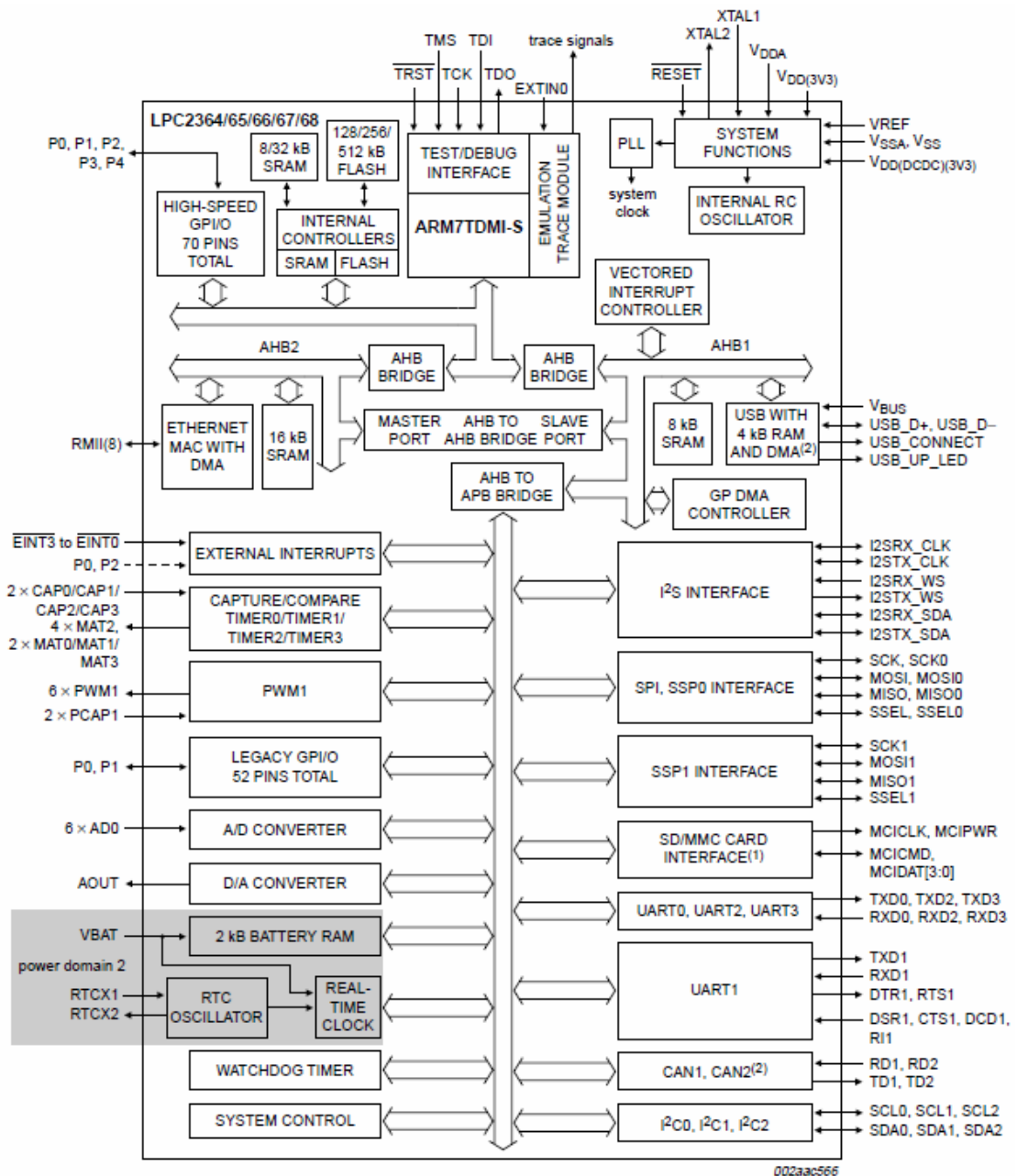


2 Main IC description

M03 adopted LPC2368 of NXP.

IC Basic function description

- 2.1 ARM7TDMI-S processor, running at up to 72 MHz.
- 2.2 Up to 512 kB on-chip flash program memory
- 2.3 56 kB of SRAM on the ARM local bus for high performance CPU access.
- 2.4 USB 2.0 full-speed device with on-chip PHY and associated DMA controller
- 2.5 SPI controller.
- 2.6 10-bit ADC with input multiplexing among 6 pins.
- 2.7 WatchDog Timer (WDT).
- 2.8 Single 3.3 V power supply (3.0 V to 3.6 V).
- 2.9 On-chip power-on reset.
- 2.10 On-chip PLL allows CPU operation up to the maximum CPU rate without the need for a high frequency crystal.
- 2.11 IC Block diagram:



3 Bluetooth Module

- 3.1 Frequency range: 2.4GHz -2.48GHz unlicensed ISM band
- 3.2 Bluetooth Specification: V2.0+EDR
- 3.3 Output Power Class: Class 2
- 3.4 Operating Voltage: 3.3V
- 3.5 Host Interface: UART
- 3.6 Flash Memory Size: 8Mbit
- 3.7 Dimension: 26.9mm (L) x 13 (W) mm x 2.2mm (H)

4 Power supply

Li-ion 8.4V 1800mAh