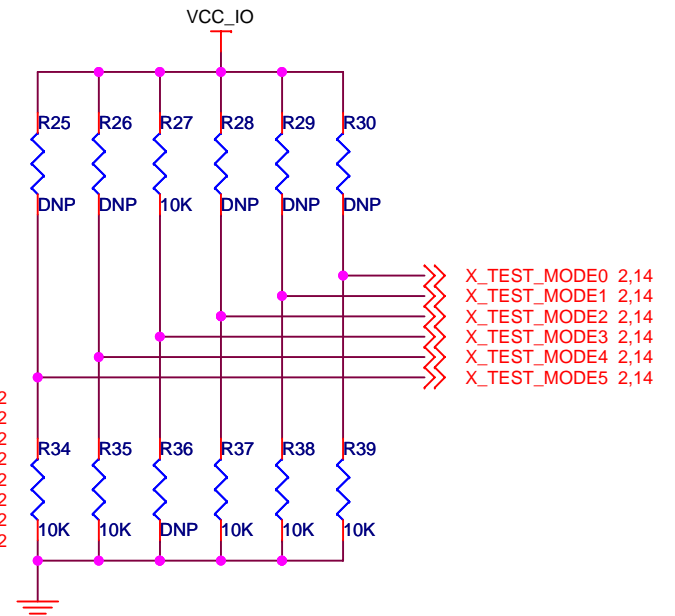
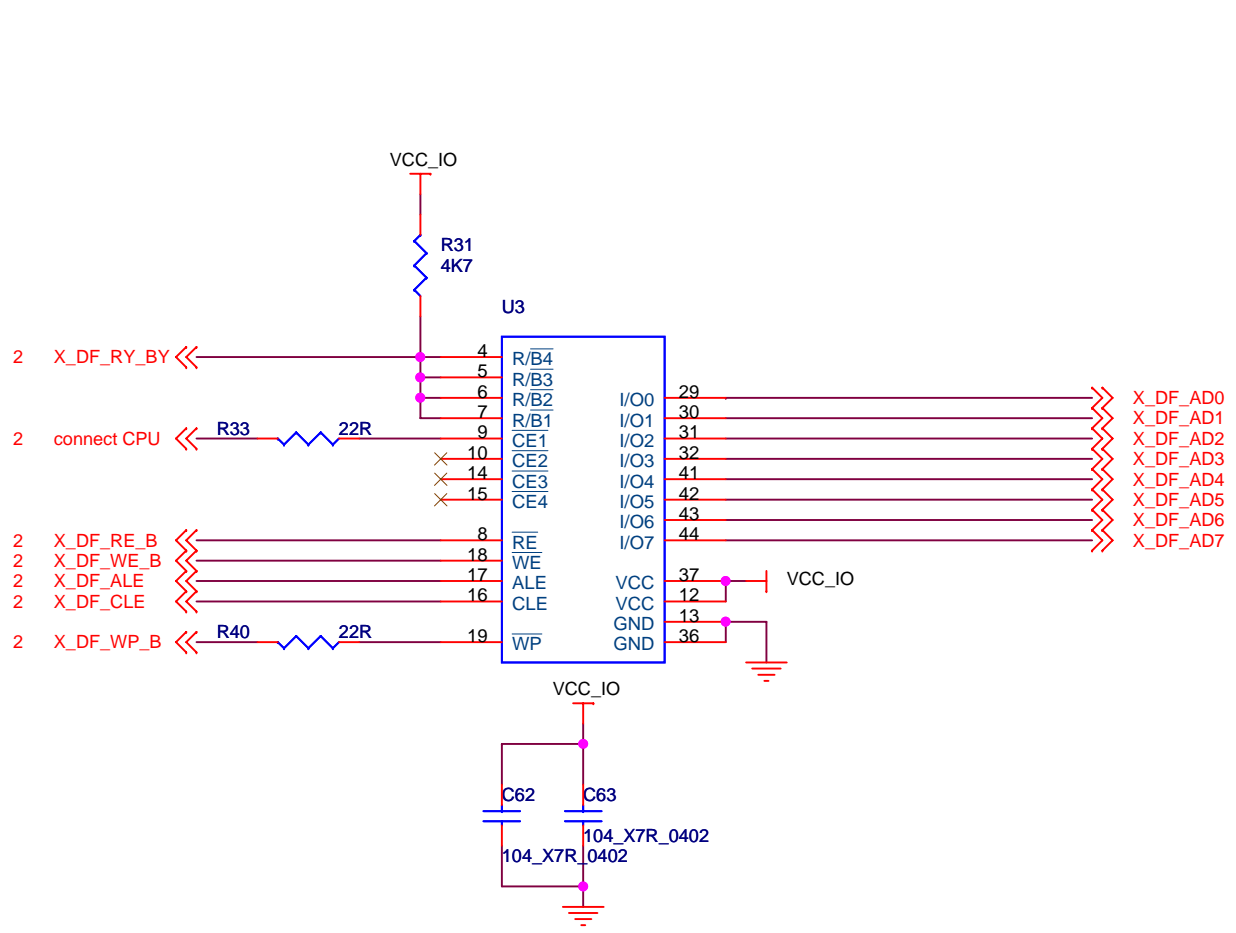


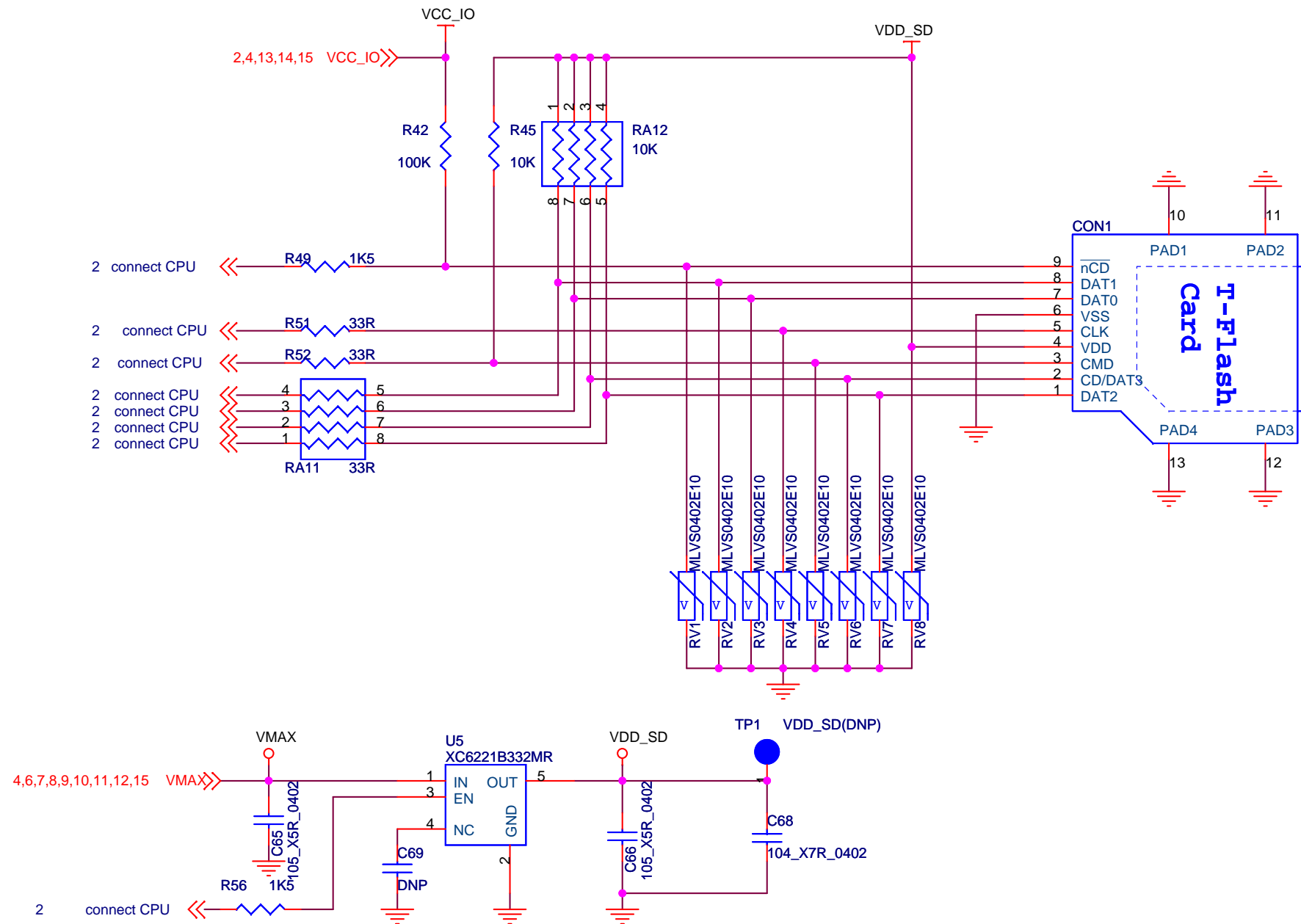
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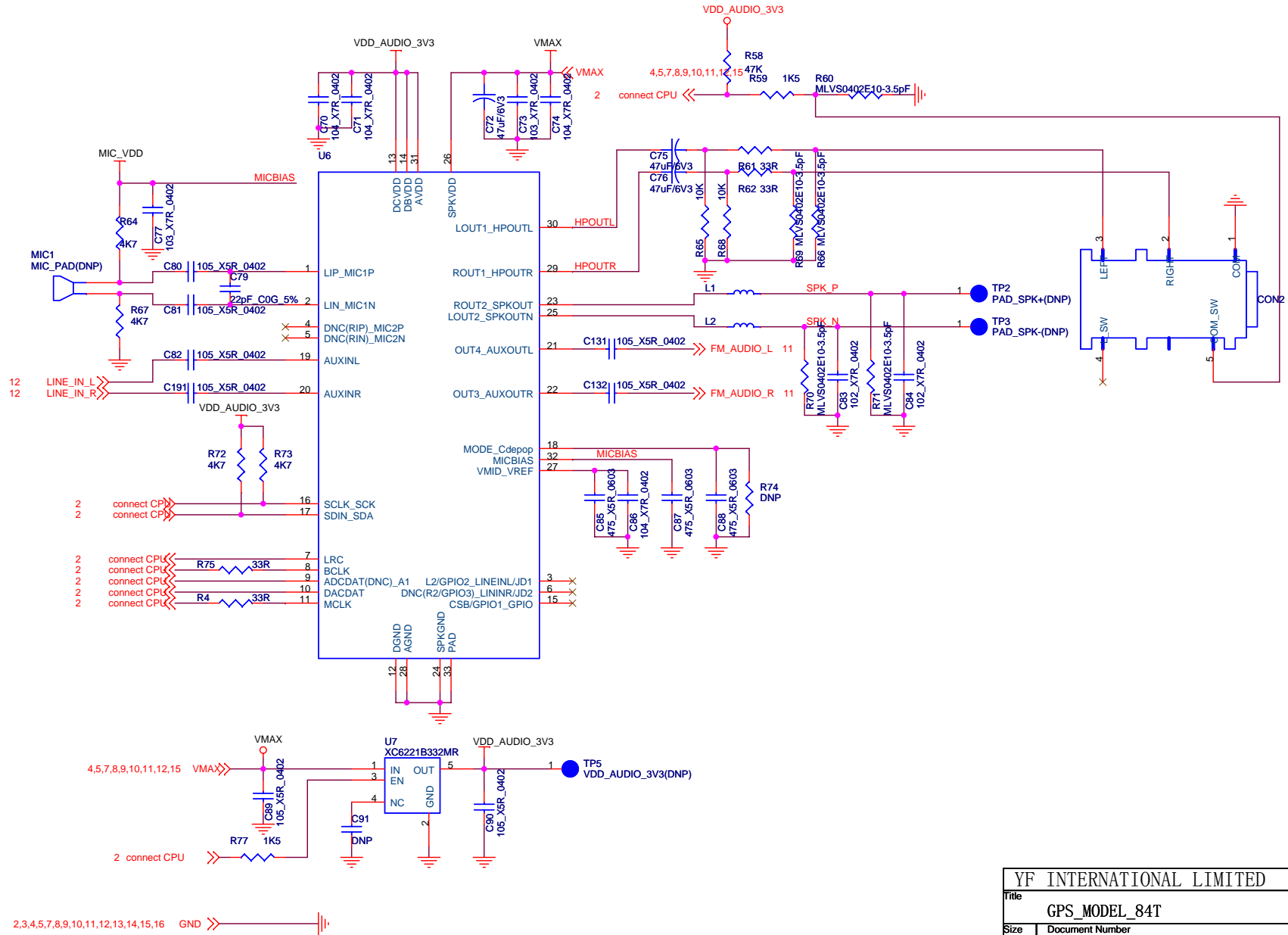
### Boot Configure for three options

Boot configuration	X_TEST_MODE[5:0]
Boundary Scan	6b110000
SLC Nand Boot 2KB Page 8 Bit	6b000101
<b>MLC Nand Boot 2KB Page 8 Bit(Default)</b>	<b>6b001000</b>
INAND Boot	6b001100
EFuse	6b110010

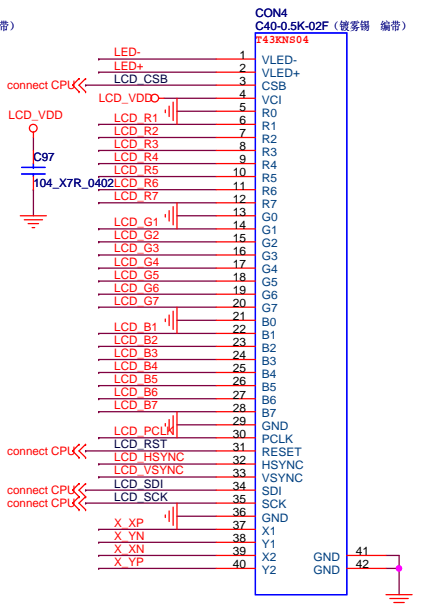
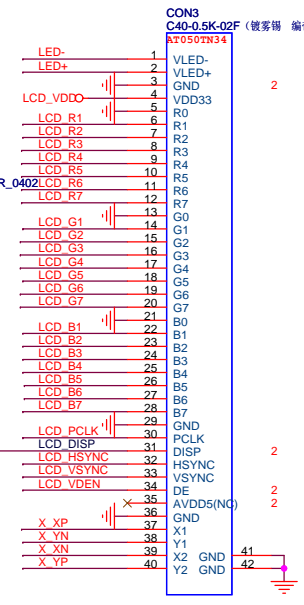
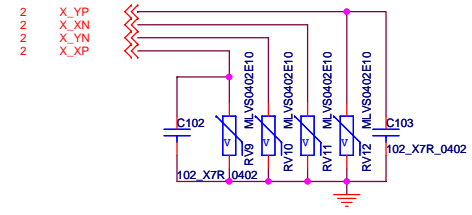
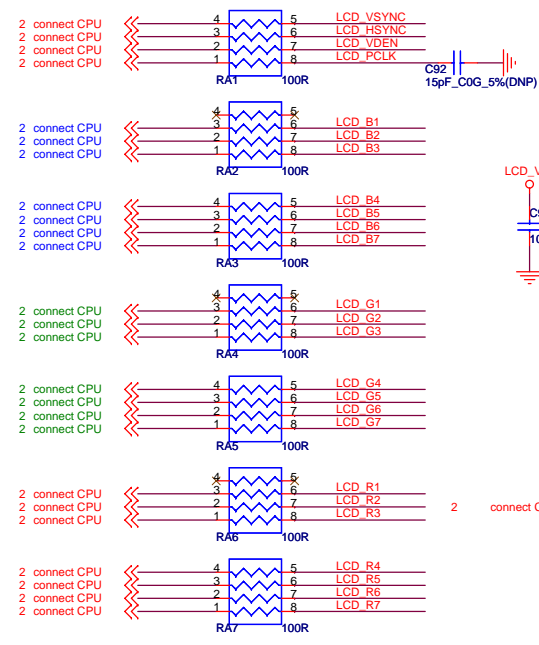
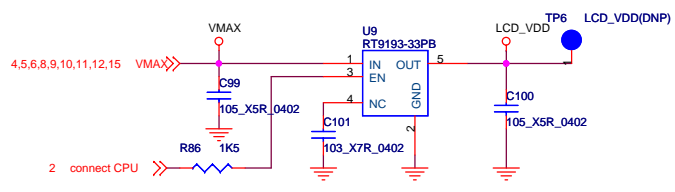
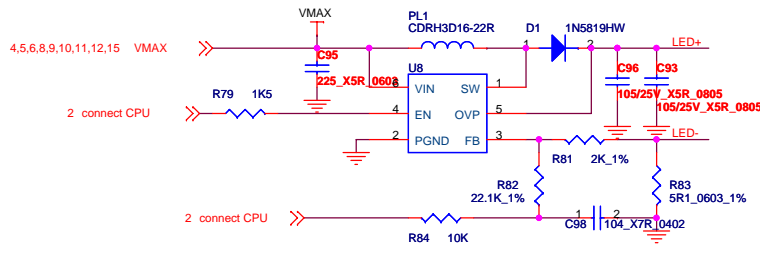
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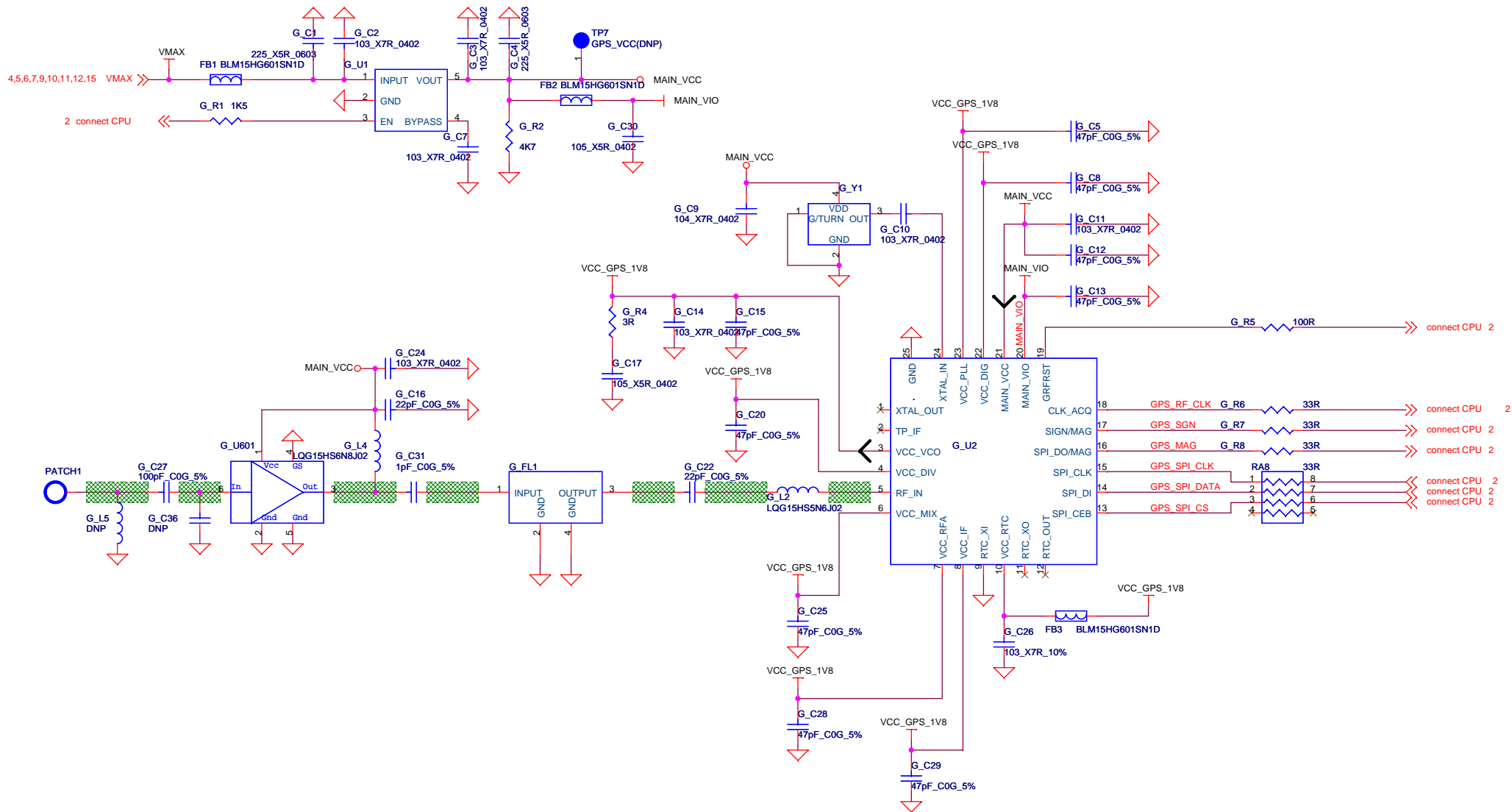
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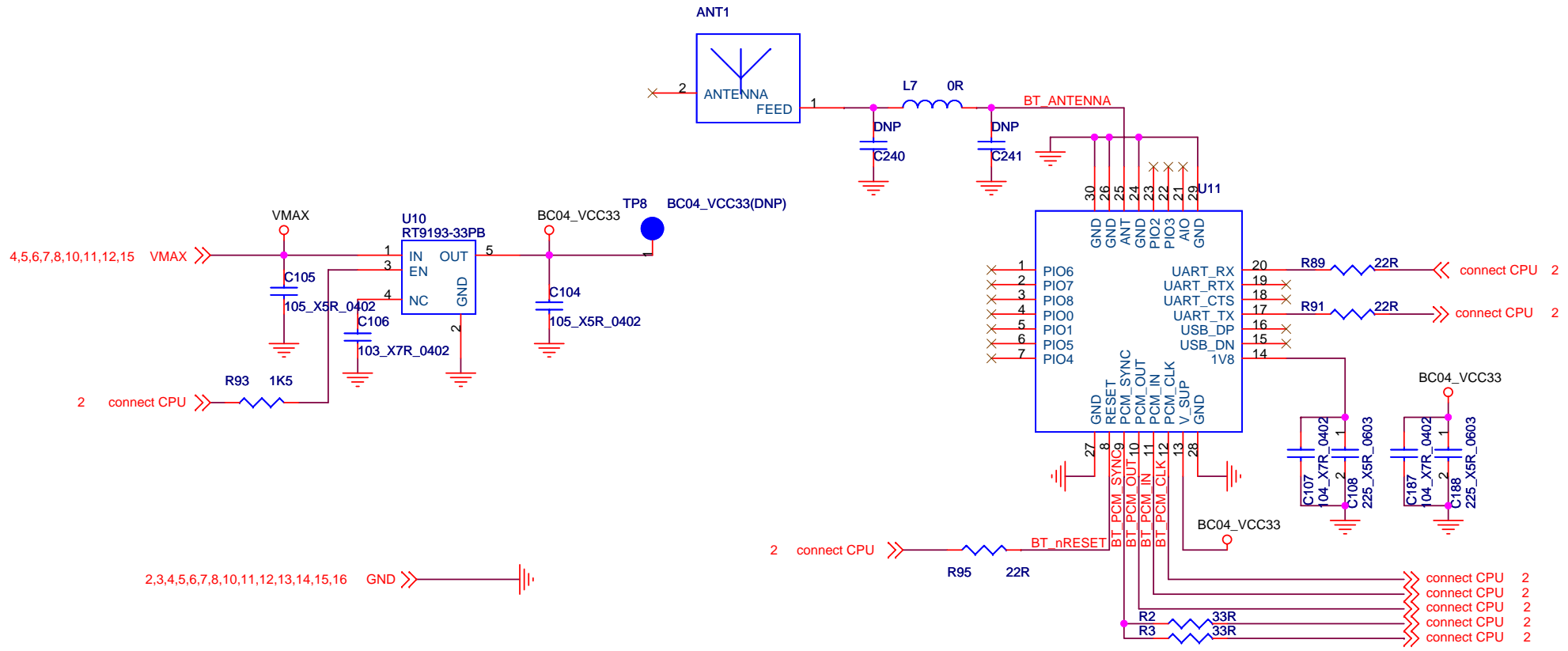
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File GPS_MODEL_84T			
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Customer	LCD部分电路原理图		
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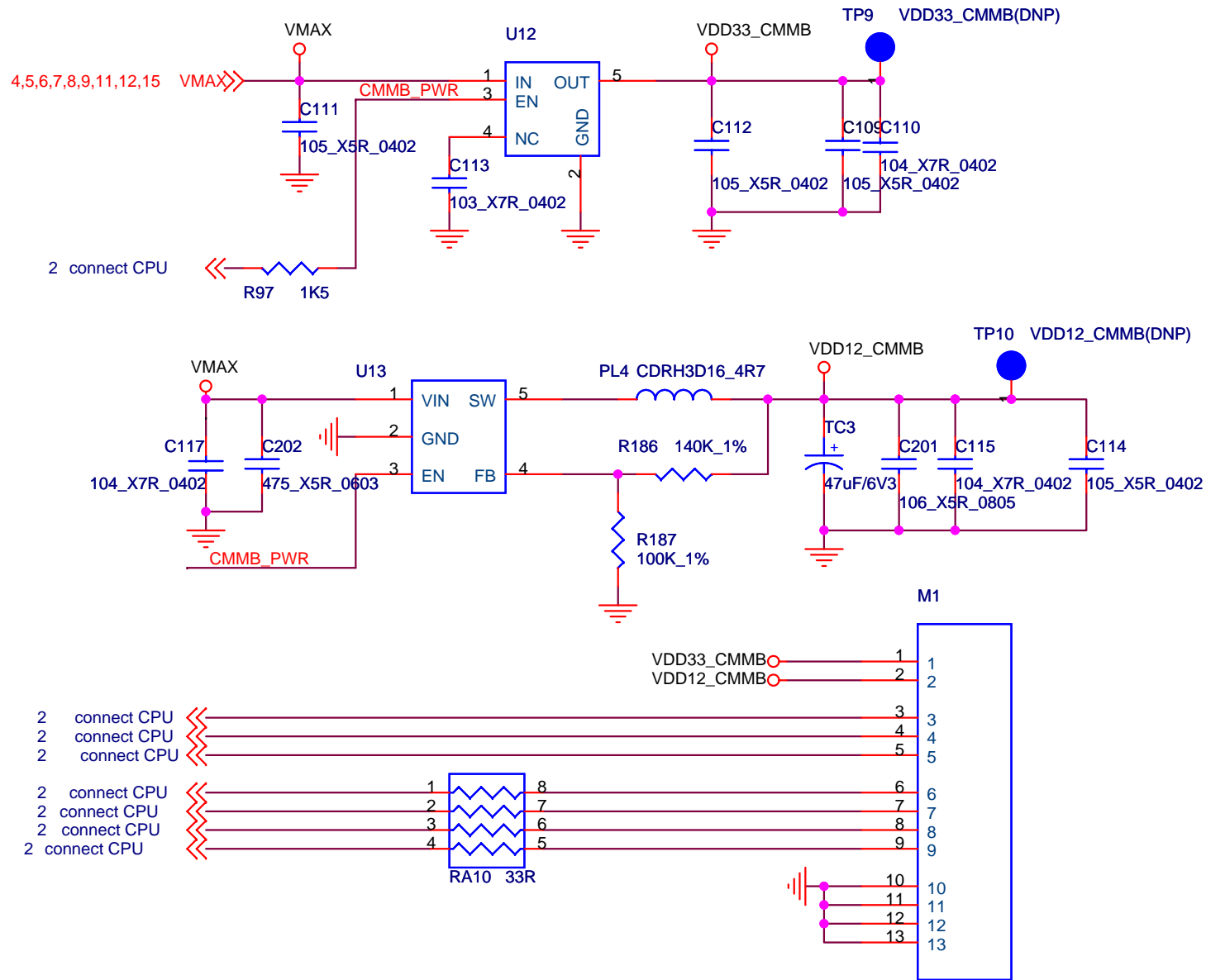


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Date:

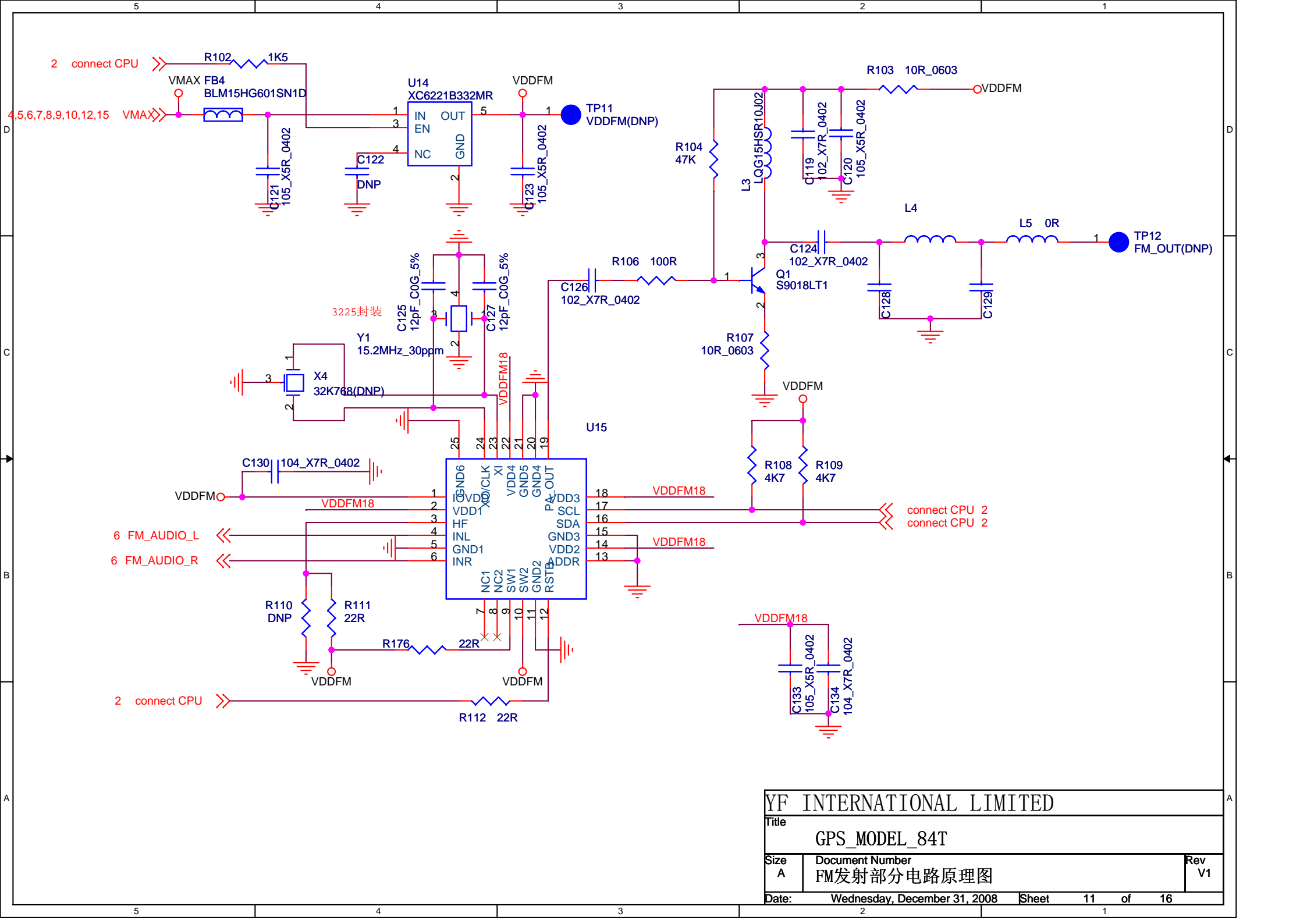
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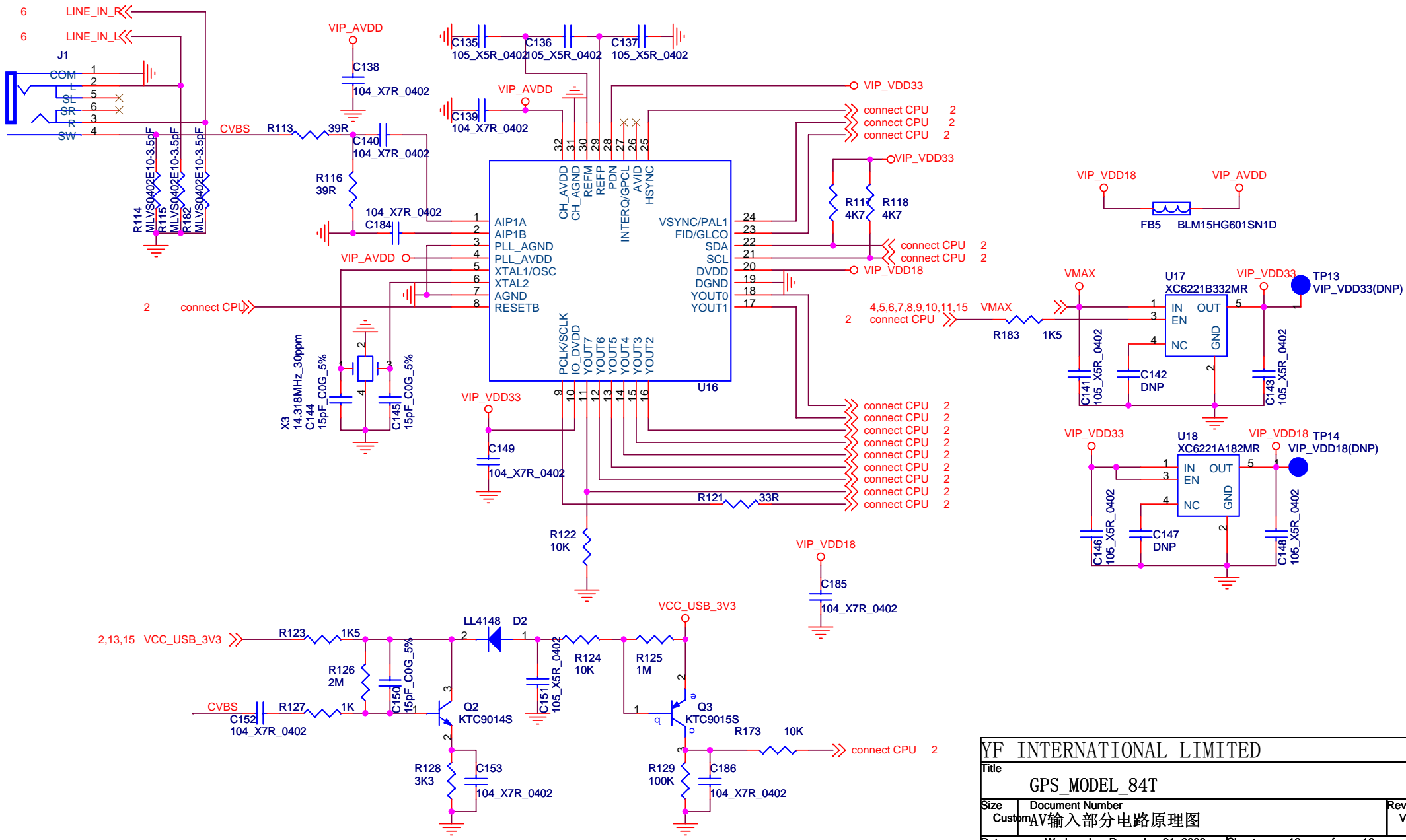
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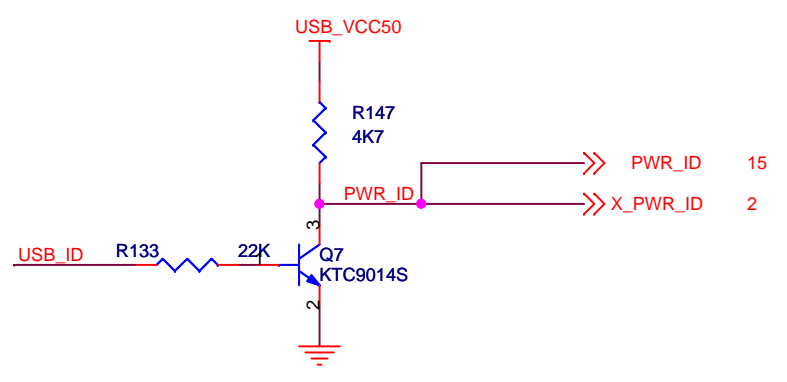
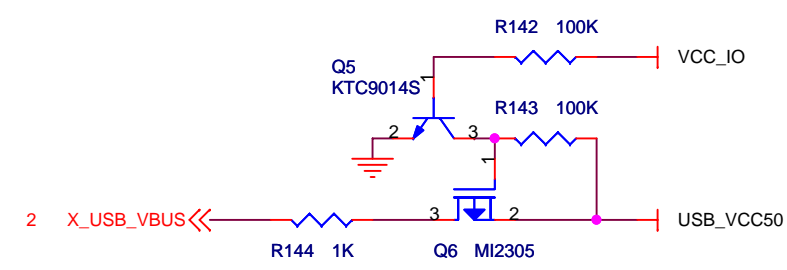
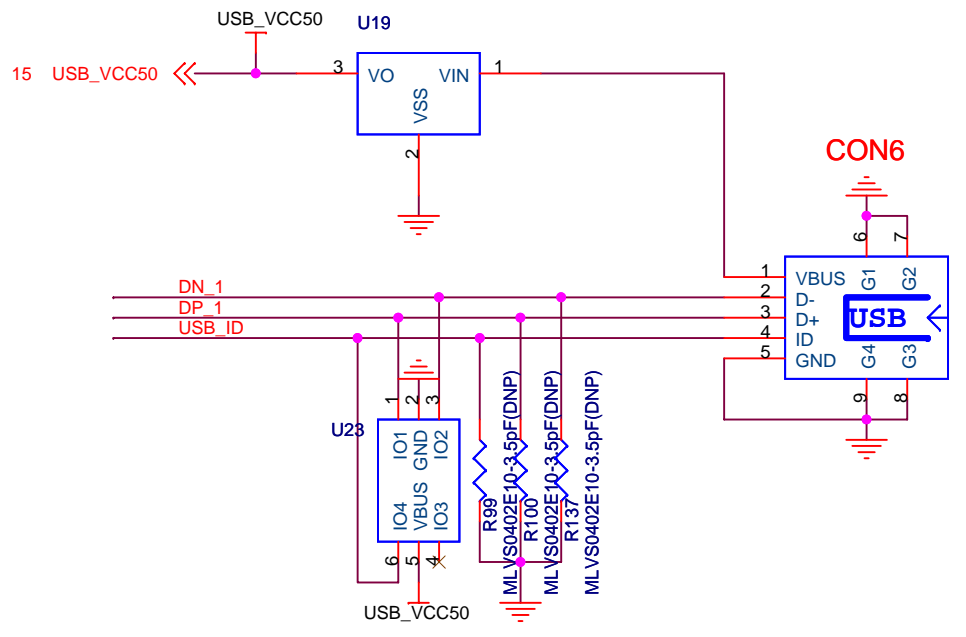
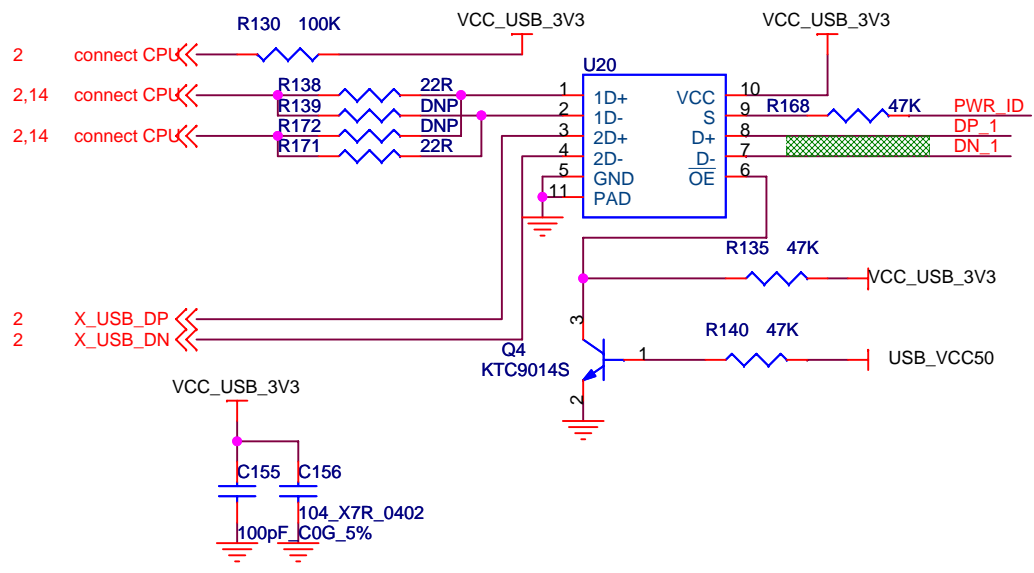
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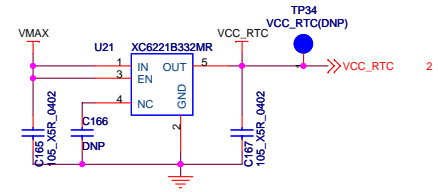
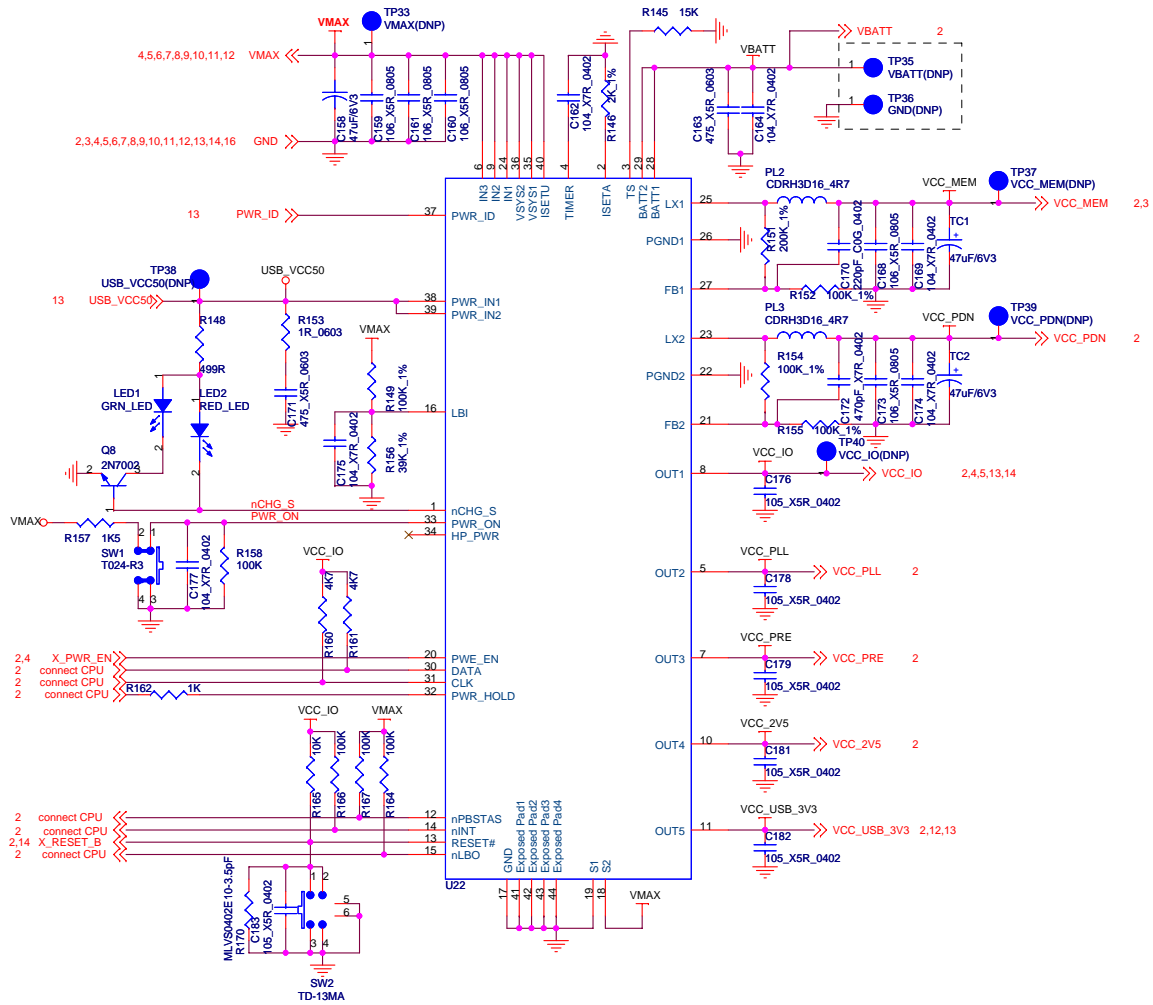
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