

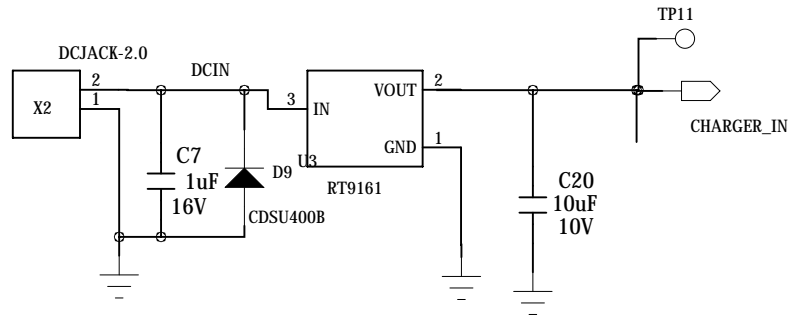
RF PATH:  
 1. Components need as close as possible  
 2. PCB is 4-layer FR4, RF path need to use 50 ohm impedance

**Test Pad descriptiom**

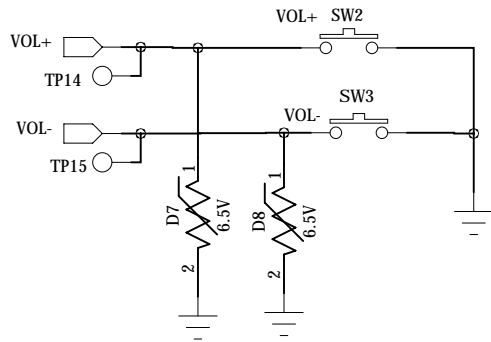
TP1	RF_TP	TP11	CHAG
TP2	MIC+		
TP3	MIC-	TP13	SAR
TP4	Reset	TP14	VOL+
		TP15	VOL-
TP6	TEST	TP16	EAN
TP9	TXD		
TP10	RXD		

<b>Litchi</b>		PROJECT DIScription: <b>LWM12(Q9)</b>		DOCUMENT NO:	
RESPONSIBLE DEPT.: <b>R&amp;D</b>		DOCUMENT TYPE: <b>CIRCUIT DIAGRAM</b>		REVISION: <b>R0.1</b>	
DRAWN BY: <b>Wendel.liu</b>		CHECKED BY:		APPROVED BY:	
SHEET SIZE: <b>A4</b>		DOC STATUS:		ISSUE DATE:	
				SCALE: <b>N/A</b>	
				PAGE NO: <b>1 / 2</b>	
DIMENSION UNIT: <b>mm</b>					
CONFIDENTIAL DOCUMENT OF Litchi					

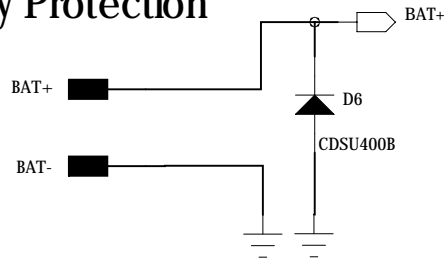
# DC-2.0



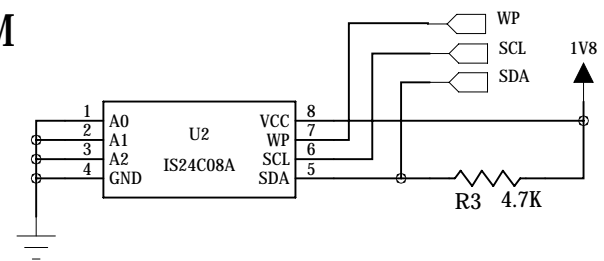
# VOL Control



# Battery Protection



# EEPROM



<b>Litchi</b>		PROJECT DISCUSSION: <b>LWM12(Q9)</b>		DOCUMENT NO:	
RESPONSIBLE DEPT.: <b>R&amp;D</b>		DOCUMENT TYPE: <b>CIRCUIT DIAGRAM</b>		REVISION: <b>R0.1</b>	PROJECTION: 
DRAWN BY: <b>Wendelliu</b>		CHECKED BY:	APPROVED BY:	REFERENCE:	
SHEET SIZE: <b>A4</b>		DOC STATUS:	ISSUE DATE:	SCALE: <b>N/A</b>	PAGE NO: <b>2 / 2</b>
				DIMENSION UNIT: <b>mm</b>	
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