

					00	PSK				
RL	ctrum Analyzer - Swep RF 50 Ω	AC		SEI	ISE:INT		ALIGN AUTO		MDec 05, 2020	Marker
larker 1	Δ 78.07250	PI	Z NO: Fast Gain:Low	Trig: Free Atten: 20		Avg Type Avg Hold:	: Log-Pwr >100/100	TYP	E 1 2 3 4 5 6 E M WWWWW T P N N N N N	Marker Select Marker 1
dB/div	Ref Offset 0.5 Ref 10.00 d	dB IBm					ΔMkr	1 78.072 -2	2 5 MHz 558 dB	
					WWW		NNMM	MMM		Norm
).0										Del
).0 										Fixed
).0		S								o
.0										Properties
	0000 GHz 100 kHz		#\/P\0	300 kHz				Stop 2.48 3.000 ms (350 GHz	Mo 1 of
G G G G G G	100 KH2		#VBVV	300 KHZ			sweep a		roor pis)	

8DPSK



14. DWELL TIME

14.1 Block Diagram Of Test Setup



14.2 Limit

Frequency hopping systems in the 2400-2483.5 MHz band shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

14.3 Test procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.

2. Set spectrum analyzer span = 0. Centred on a hopping channel;

3. Set RBW = 1MHz and VBW = 3MHz.Sweep = as necessary to capture the entire dwell time per hopping channel. Set the EUT for DH5, DH3 and DH1 packet transmitting.

4. Use the marker-delta function to determine the dwell time. If this value varies with different modes of operation (e.g., data rate, modulation format, etc.), repeat this test for each variation. The limit is specified in one of the subparagraphs of this Section. Submit this plot(s).



14.4 Test Result

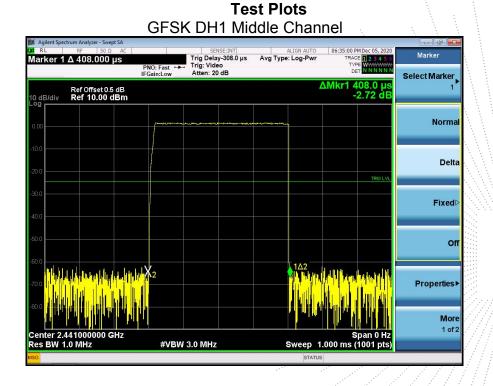
DH5 Packet permit maximum 1600 / 79 / 6 hops per second in each channel (5 time slots RX, 1 time slot TX).

DH3 Packet permit maximum 1600 / 79 / 4 hops per second in each channel (3 time slots RX, 1 time slot TX).

DH1 Packet permit maximum 1600 / 79 /2 hops per second in each channel (1 time slot RX, 1 time slot TX). So, the Dwell Time can be calculated as follows:

DH5:1600/79/6*0.4*79*(MkrDelta)/1000 DH3:1600/79/4*0.4*79*(MkrDelta)/1000 DH1:1600/79/2*0.4*79*(MkrDelta)/1000 Remark: Mkr Delta is once pulse time.

Modulation	Channel Data	Packet	pulse time(ms)	Dwell Time(s)	Limits(s)
		DH1	0.408	0.131	0.4
GFSK	Middle	DH3	1.674	0.268	0.4
		DH5	2.920	0.311	0.4
		2DH1	0.420	0.134	0.4
Pi/4DQPSK	Middle	2DH3	1.686	0.270	0.4
		2DH5	2.930	0.313	0.4
		3DH1	0.420	0.134	0.4
8DPSK	Middle	3DH3	1.686	0.270	0.4
		3DH5	2.930	0.313	0.4

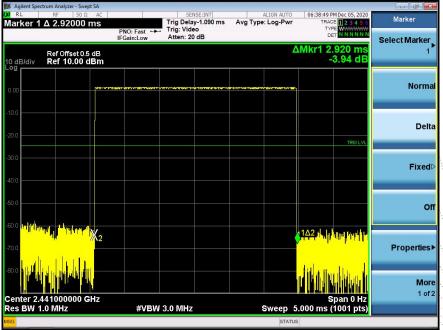




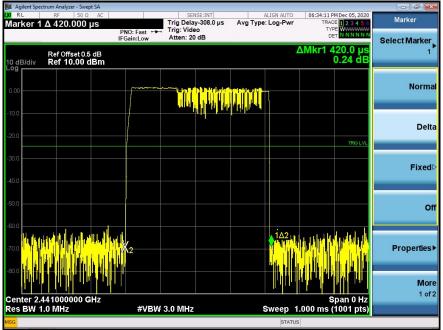
						trum Analyzer - Swept SA	
Marker	06:35:44 PM Dec 05, 2020 TRACE 2 3 4 5 6	LIGN AUTO		SENSE:INT Delay-695.0 µs		AC 1.67400 ms	Marker 1
Select Marker	DET NNNNN			: Video en: 20 dB			
1	Mkr1 1.674 ms 0.00 dB	Δ				Ref Offset 0.5 dB Ref 10.00 dBm	10 dB/div
Norma		*****	₩~~1µ -7₩₽ ₩7	-tera Biller Biller (1997) - Pilan	1 6 11 mil 10 - 707 1 - 621 6 - 71 6 - 72		0.00
Delta							-10.0
Fixed▷	TRIG LVL						-30.0
Ofi							-40.0
Properties						111 11- 111 11 × 2	-60.0
More 1 of 2							-80.0
	Span 0 Hz .000 ms (1001 pts)	Sweep 3		٧Hz	#VBW 3.01	41000000 GHz .0 MHz	Center 2. Res BW 1
		STATUS					1SG

GFSK DH3 Middle Channel

GFSK DH5 High Middle Channel

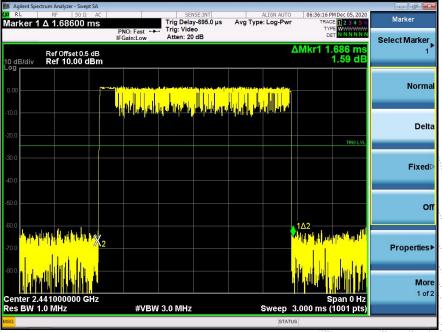




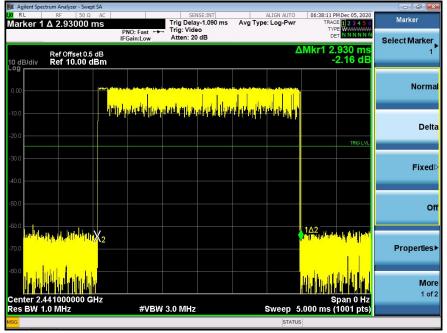


Pi/4DQPSK DH1 Middle Channel

Pi/4DQPSK DH3 Middle Channel

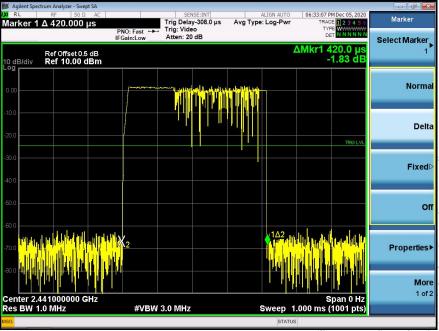




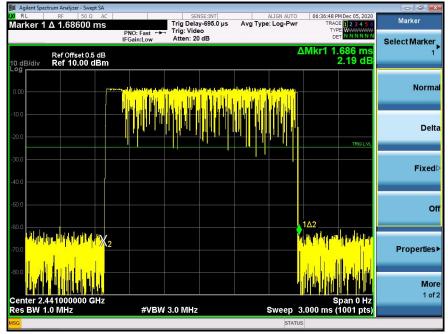


Pi/4DQPSK DH5 Middle Channel

8DPSK DH1 Middle Channel

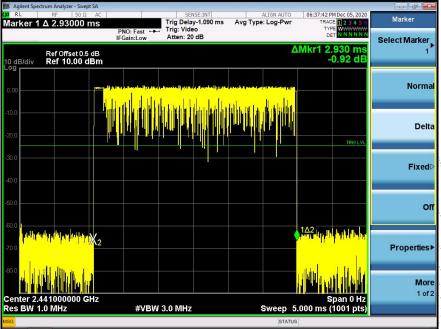






8DPSK DH3 Middle Channel

8DPSK DH5 Middle Channel



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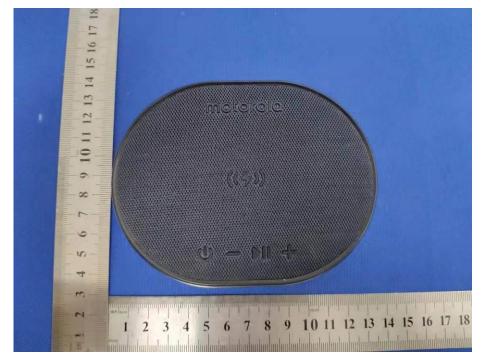
15.1 Limit

15.203 requirement: For intentional device, according to 15.203: an intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device.

15.2 Test Result

The EUT antenna is Chip antenna, Antenna Gain is 1.75dBi, fulfill the requirement of this section.

EUT Photo 1



EUT Photo 2





17. EUT TEST SETUP PHOTOGRAPHS

Conducted emissions



Radiated Measurement Photos





Report No.: BCTC2012712033-3E





STATEMENT

1. The equipment lists are traceable to the national reference standards.

2. The test report can not be partially copied unless prior written approval is issued from our lab.

3. The test report is invalid without stamp of laboratory.

4. The test report is invalid without signature of person(s) testing and authorizing.

5. The test process and test result is only related to the Unit Under Test.

6.The quality system of our laboratory is in accordance with ISO/IEC17025.

7.If there is any objection to report, the client should inform issuing laboratory within 15 days from the date of receiving test report.

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***** END *****