

NAVIGON BU MOBILITY
DEVICE BOARD P6A

PAGE1	TITLE	PAGE
PAGE2	HISTORY	
PAGE3	BLOCK	
PAGE4	PXA270_MEM_GPSUART	
PAGE5	PXA270_LCD	
PAGE6	PXA270_IO_USB	
PAGE7	PXA270_PWR_GND	
PAGE8	INTEL_STRATA_FLASH	
PAGE9	SYSTEM_SDRAM	
PAGE10	LCD_BL_TP_CONN	
PAGE11	SD_SOCKET_KP_HOLD_CONN	
PAGE12	CPU_P5_RS232_JTAG	
PAGE13	FM_TUNER	
PAGE14	GPS_MODULE	
PAGE15	REMOTE_CONTROL	
PAGE16	P5_PWR_MEM	
PAGE17	P5_IO_DUAL_MK	
PAGE18	P5_SDRAM_FLASH_PWR_REG	
PAGE19	BLUE_TOOTH	
PAGE20	SPEAKER_LINE_OUT_JACK	
PAGE21	USB_CLIENT_CHARGER	
PAGE22	DC_JACK_CHARGER_MCU_NTC	
PAGE23	PCF50606_IO	
PAGE24	PCF50606_REGULATOR	
PAGE25	CORE_REGULATOR	
PAGE26	ESD_FOAM_HOLE_SHIELDING_CAN	

VERSION 6.3.3

BY
SOLECTRON SHANGHAI CO.,LTD.

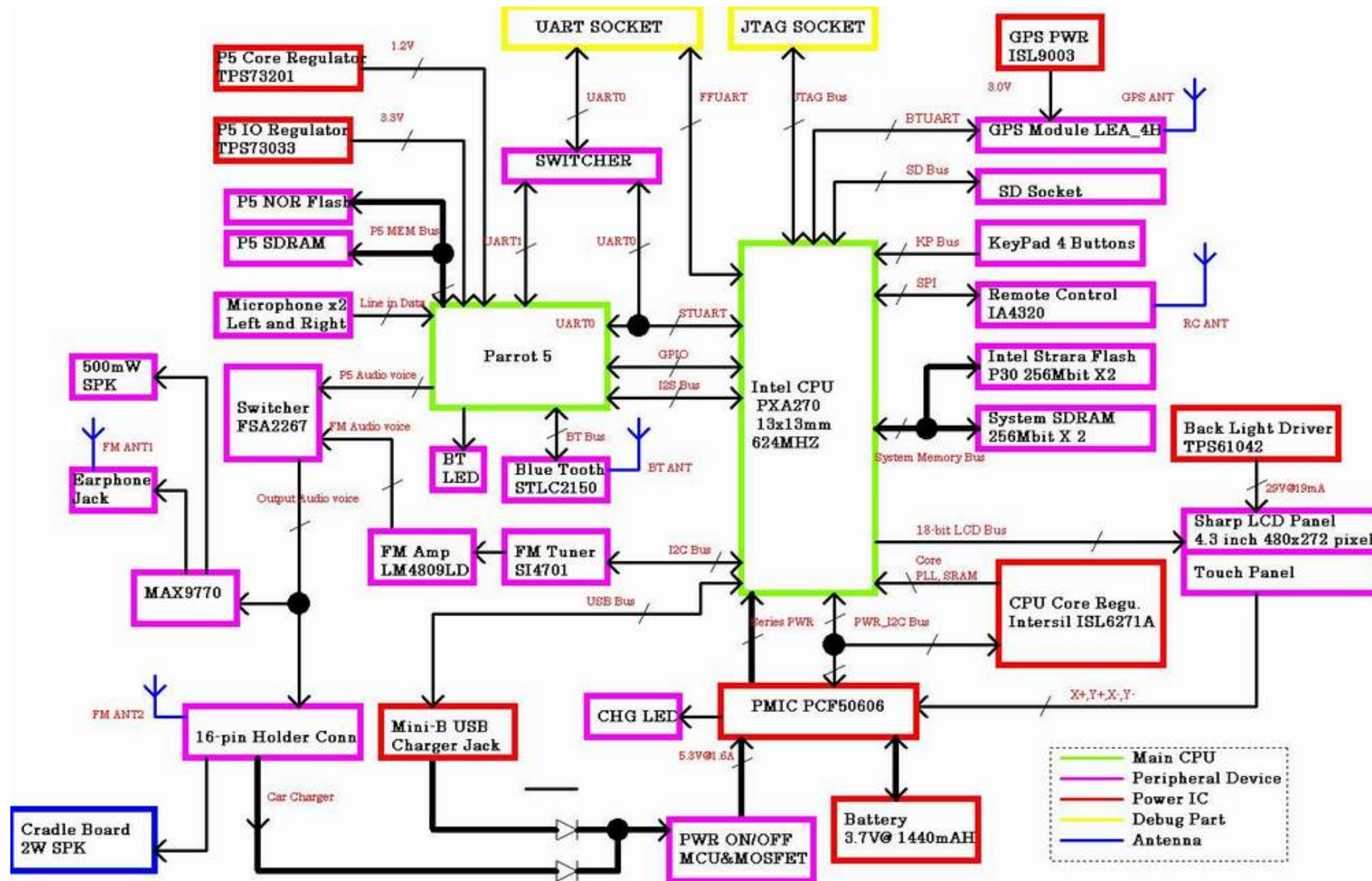


TITLE: TITLE PAGE		DATE: 2007-06-19
ENGINEER: BUDDY HUANG		PAGE: PAGE 1 OF 26

HISTORY

- 1.VERSION 4.9.2 IS P4 SCHEMATIC, BUDDY HUANG, 2006-11-06
- 2.NAVIGON DID DELETE 2700G AND SDRAM, RELEASE VERSION 5.0.0,BUDDY HUANG, 2006-11-06
- 3.FOR FLASHING P5 VIA PXA270, MODIFY SCH VERSION 5.0.0,BUDDY HUANG, 2006-11-06
- 4.SWAP RESISTOR AND REPLACE SERIES-R TO SINGAL R, MODIFY SCH VERSION 5.0.1,BUDDY HUANG, 2006-11-07
- 5.MODIFY DEBUG SEGMENT,BUDDY HUANG, 2006-11-08
- 6.MODIFY DEBUG SEGMENT,BUDDY HUANG,RELEASE VESRION 5.2.0 2006-11-29
- 7.SPLIT GPS GND,BUDDY HUANG,RELEASE VESRION 5.3.0 2006-12-05
- 8.PAGE 7, CHG 1.8V TO 2.5V, BUDDY HUANG,RELEASE VESRION 5.3.1 2006-12-06
- 9.PAGE 14, MODIFY RF SCH, BUDDY HUANG,RELEASE VESRION 5.4.0 2006-12-08
- 10.MODIFY SCH, BUDDY HUANG,RELEASE VESRION 5.5.0 2006-12-08
- 11.DEL 3AXIS, BUDDY HUANG,RELEASE VESRION 5.6.0 2006-12-12
- 12.ADD HOLE AND SHIELDING CAN, BUDDY HUANG,RELEASE VESRION 5.6.1 2006-12-13
- 13.ADD 4 FB FOR BAT CONN, BUDDY HUANG,RELEASE VESRION 5.7.0 2006-12-14
- 14.DEL DEBUG AREA, ADD 2 8-PIN SOCKET FOR DEBUG,BUDDY HUANG,RELEASE VESRION 5.7.2 2006-12-18
- 15.REVISE SCH,BUDDY HUANG,RELEASE VESRION 5.7.3 2006-12-20
- 16.REVISE SCH,BUDDY HUANG,RELEASE VESRION 5.7.8 2007-1-04
- 17.REVISE P5A FINAL SCH,BUDDY HUANG,RELEASE VESRION 5.7.9 2007-1-10
- 18.ACCORING TO DEBUG RESULT,MODIFY SCH,RELEASE VESION 6.0.0,BUDDY HUANG,2007-3-23
- 19.ACCORING TO DEBUG RESULT,MODIFY SCH,RELEASE VESION 6.0.1,BUDDY HUANG,2007-3-28
- 20.ADD PROGRAM MCU VIA ON-LINE MODE,MODIFY SCH,RELEASE VESION 6.0.2,BUDDY HUANG,2007-3-30
- 21.ADD PMOSFET TO SHUT BATTERY IN OFF MODE,RELEASE VESION 6.0.3,BUDDY HUANG,2007-4-3
- 22.CHANAGE LED DRIVER IC,RELEASE VESION 6.0.5,BUDDY HUANG,2007-4-4
- 23.KEEP 2 OPTION FOR CHARNGE LED DIRVER,RELEASE VESION 6.1.0,BUDDY HUANG,2007-4-5
- 24.ADD 2 FB FOR PMIC,RELEASE VESION 6.1.0,BUDDY HUANG,2007-4-6
- 25.MODIFY MICR CIRCUIT,RELEASE VESION 6.1.2,BUDDY HUANG,2007-4-9
- 26.P6A FOR CAT4238,RELEASE VESION 6.1.2,BUDDY HUANG,2007-4-9
- 27.UPDATE MICR AND BT,RELEASE VESION 6.1.3,BUDDY HUANG,2007-4-12
- 28.ACCORDING TO DEBUG RESULT,MOIDFY SCH,RELEASE VESION 6.1.4,BUDDY HUANG,2007-4-27
- 29.ACCORDING TO DEBUG RESULT,MOIDFY SCH,RELEASE VESION 6.1.5,BUDDY HUANG,2007-4-28
- 30.ACCORDING TO DEBUG RESULT,MOIDFY SCH,RELEASE VESION 6.1.6,BUDDY HUANG,2007-4-29
- 31.ACCORDING TO DEBUG RESULT,MOIDFY SCH,RELEASE VESION 6.1.8,BUDDY HUANG,2007-5-14
- 32.ACCORDING TO DEBUG RESULT,MOIDFY SCH,RELEASE VESION 6.2.0,BUDDY HUANG,2007-5-16
- 33.RE-CHANGE AUDIO COMPONENT PARAMETER,MOIDFY SCH,RELEASE VESION 6.3.0,BUDDY HUANG,2007-5-22
- 34.RE-CHANGE LCD COMPONENT PARAMETER,MOIDFY SCH,RELEASE VESION 6.3.1,BUDDY HUANG,2007-5-29
- 35.RE-CHANGE R272 TO 6.2K,MOIDFY SCH,RELEASE VESION 6.3.2,BUDDY HUANG,2007-6-4
- 36.ACCORDING TO P6 BUILDING RESUTL,MOIDFY SCH,RELEASE VESION 6.3.3,BUDDY HUANG,2007-6-19

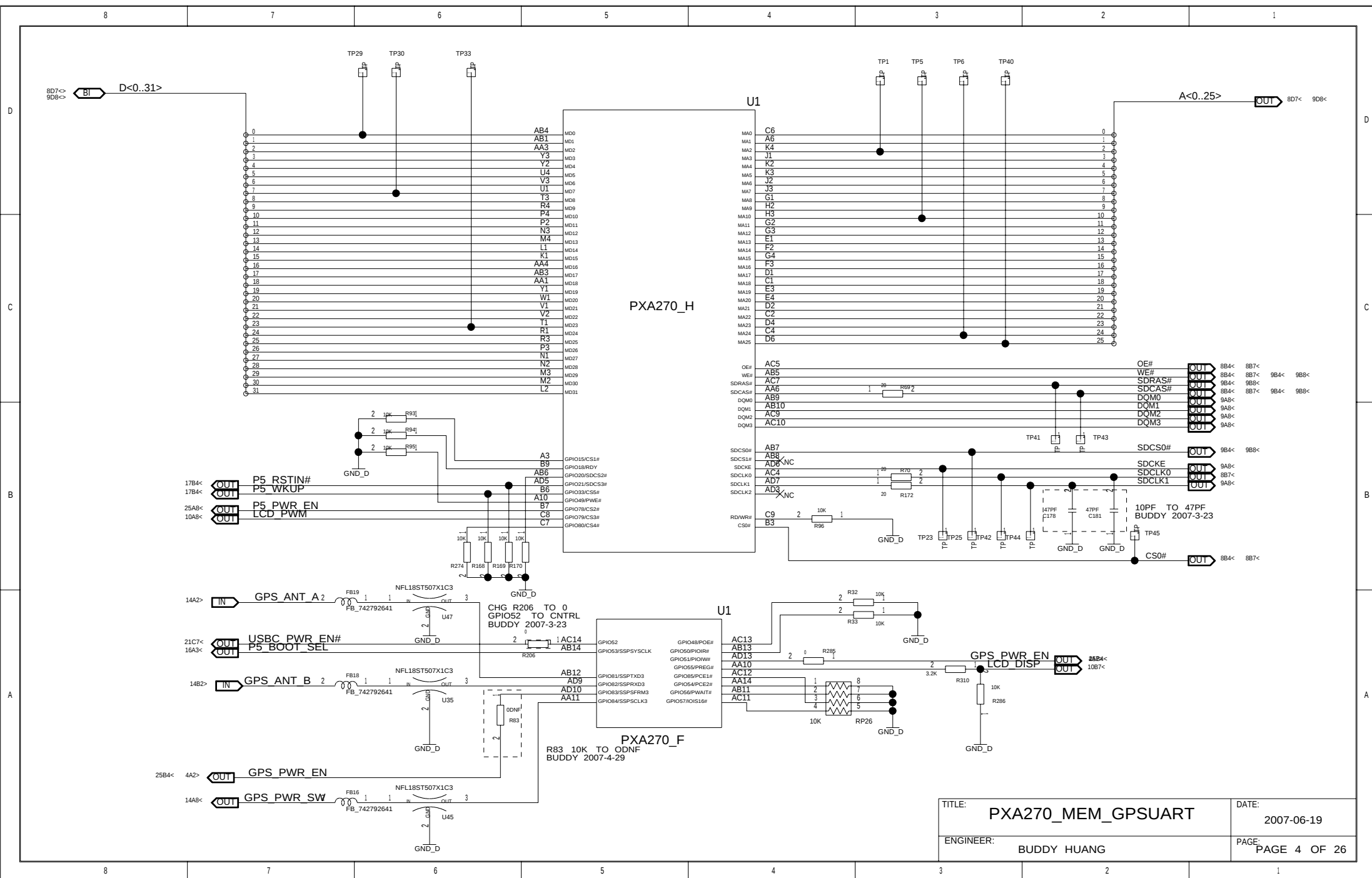
TITLE:	HISTORY	DATE:	2007-06-19
ENGINEER:	BUDDY HUANG	PAGE:	PAGE 2 OF 26

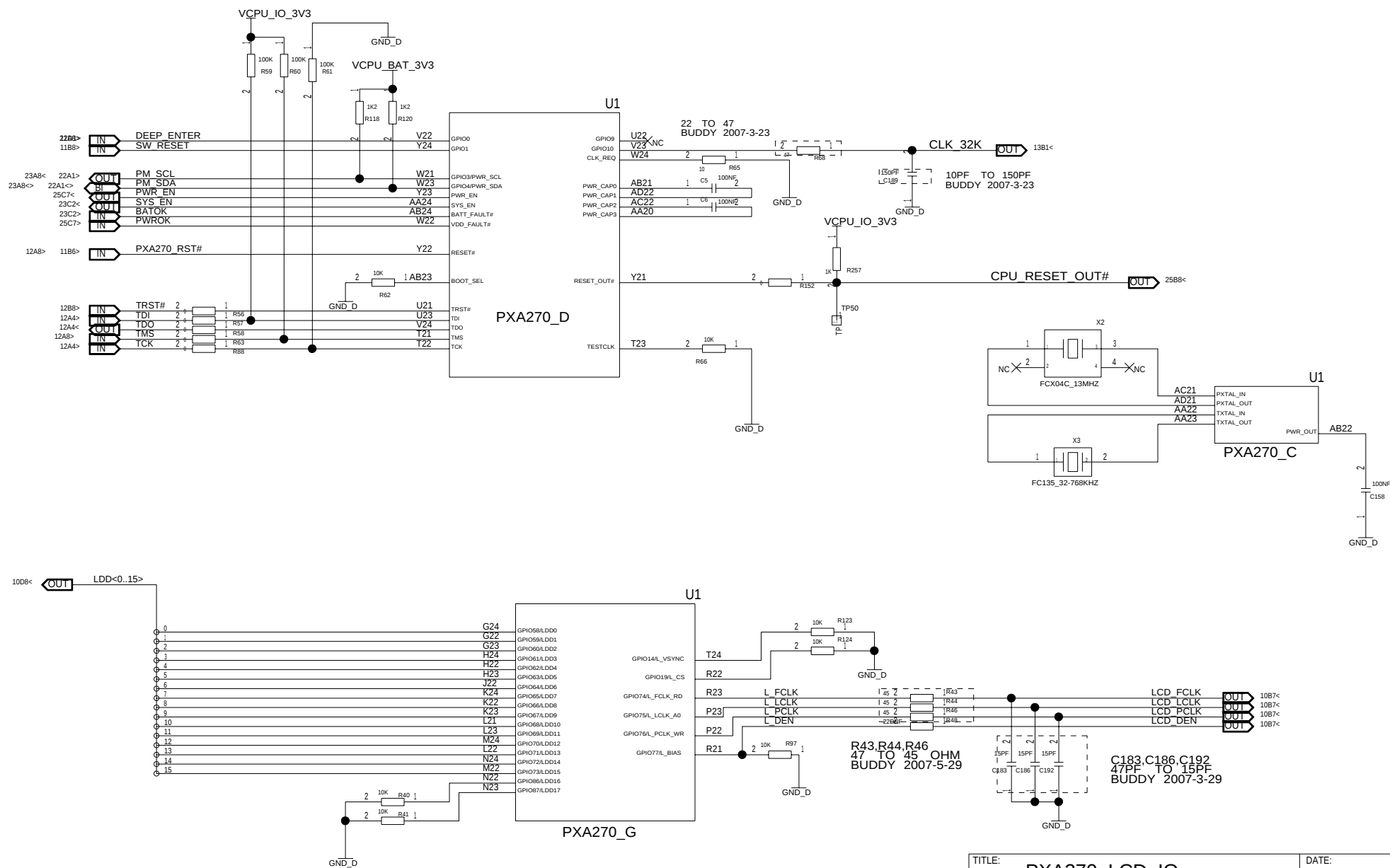


NOTE:
INDEX SEE 1 OF 26

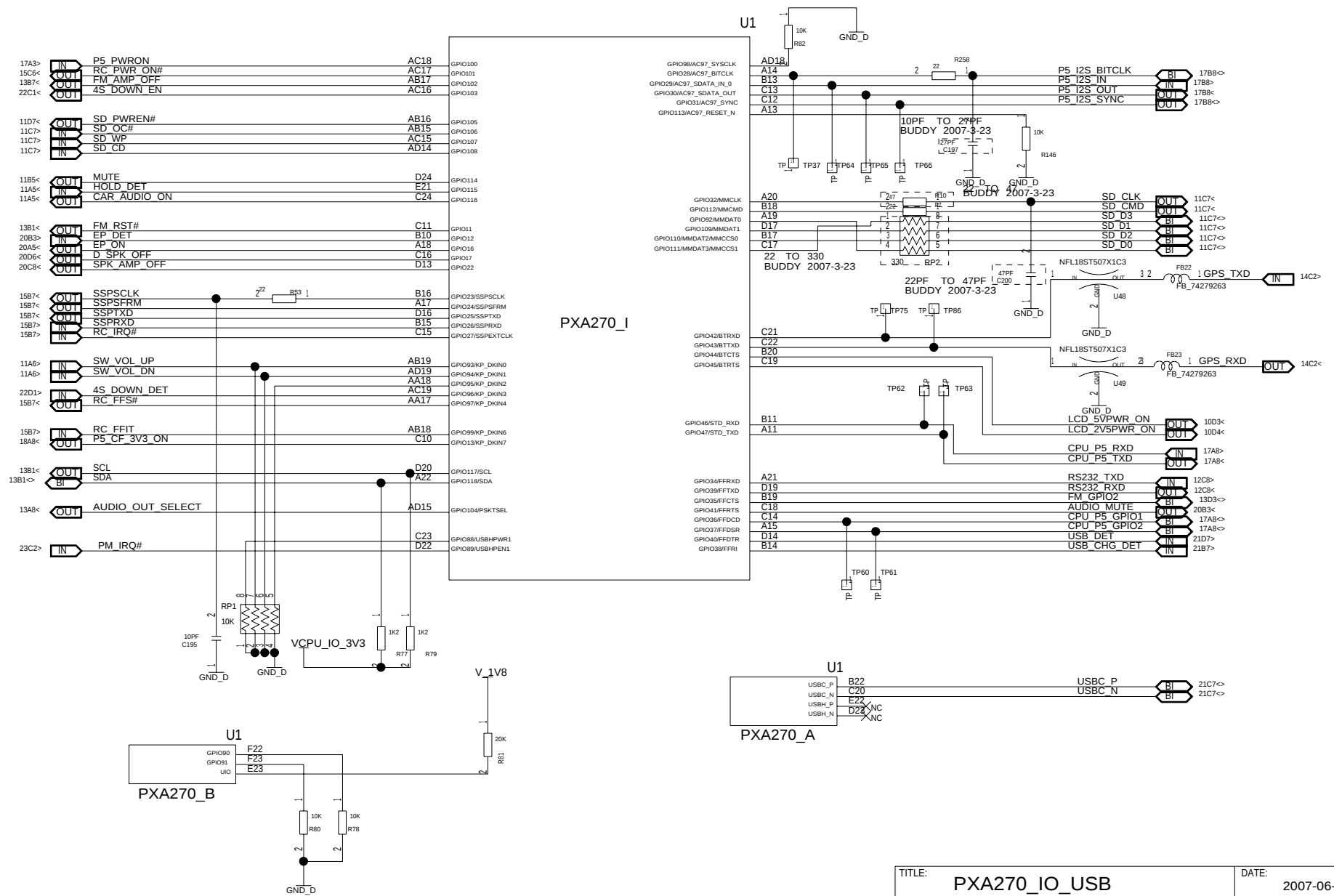
BLOCK

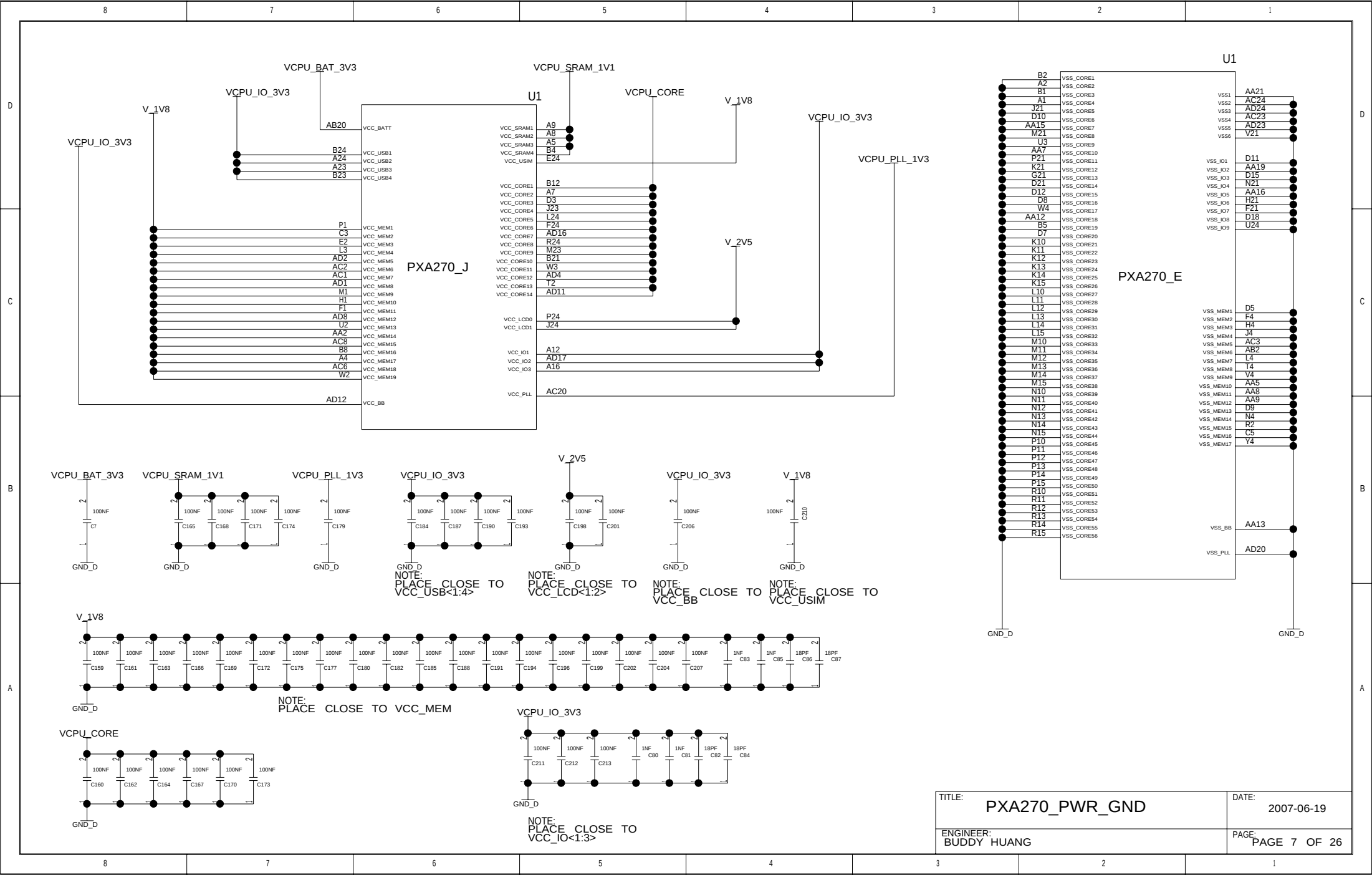
TITLE:	BLOCK	DATE:	2007-06-19
ENGINEER:	BUDDY HUANG	PAGE:	PAGE 3 OF 26





TITLE: PXA270_LCD_IO	DATE: 2007-06-19
ENGINEER: BUDDY HUANG	PAGE: PAGE 5 OF 26

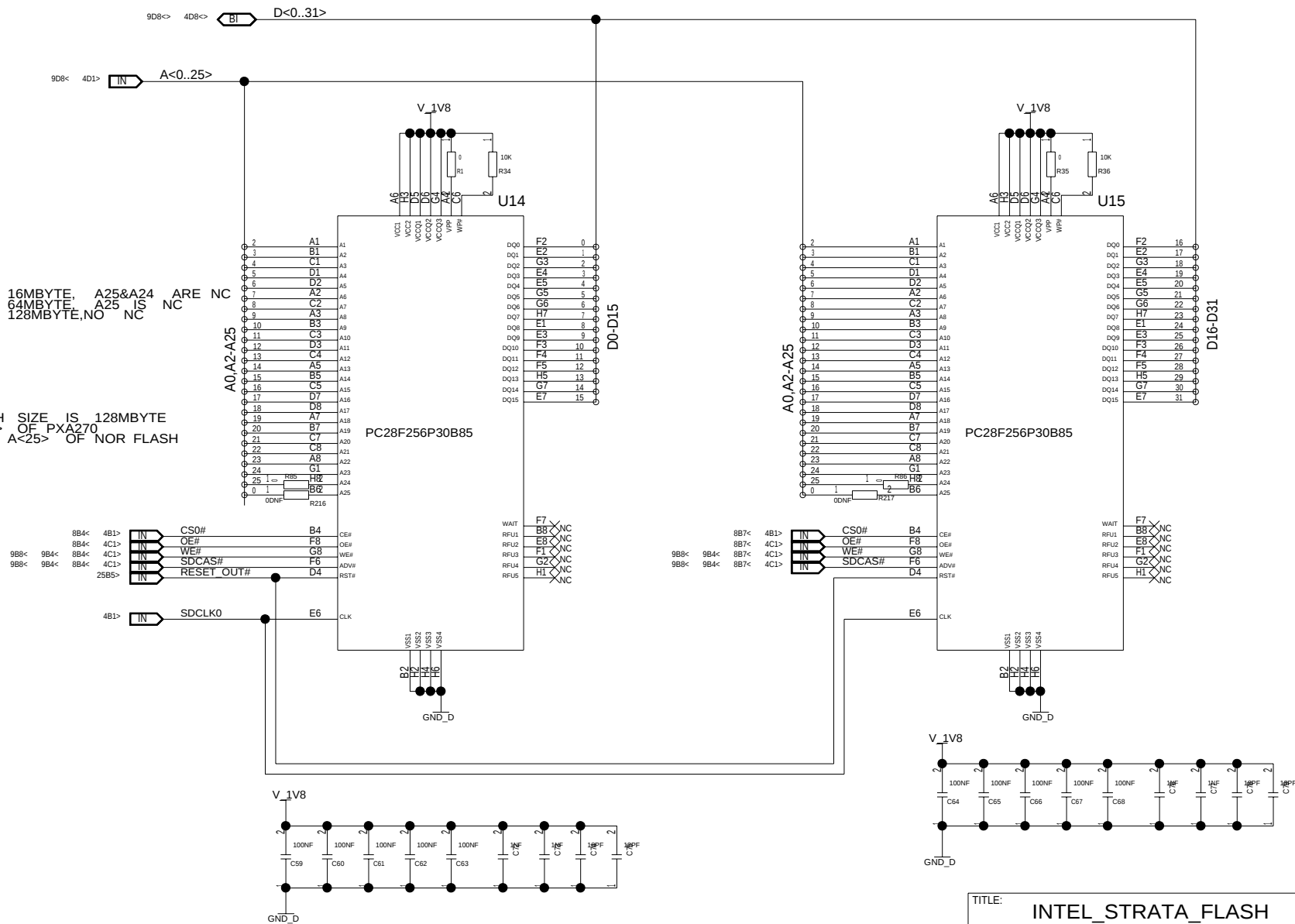


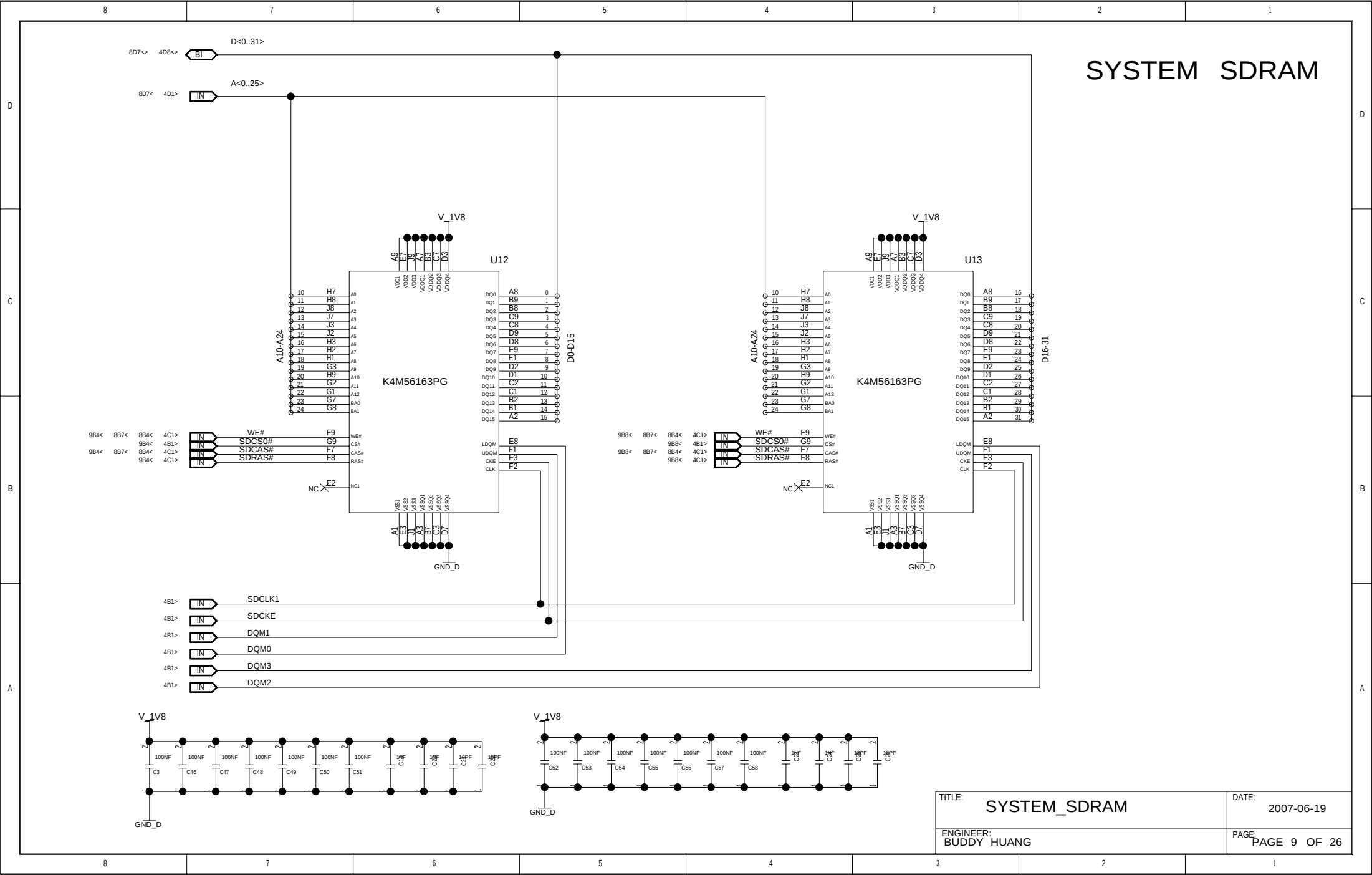


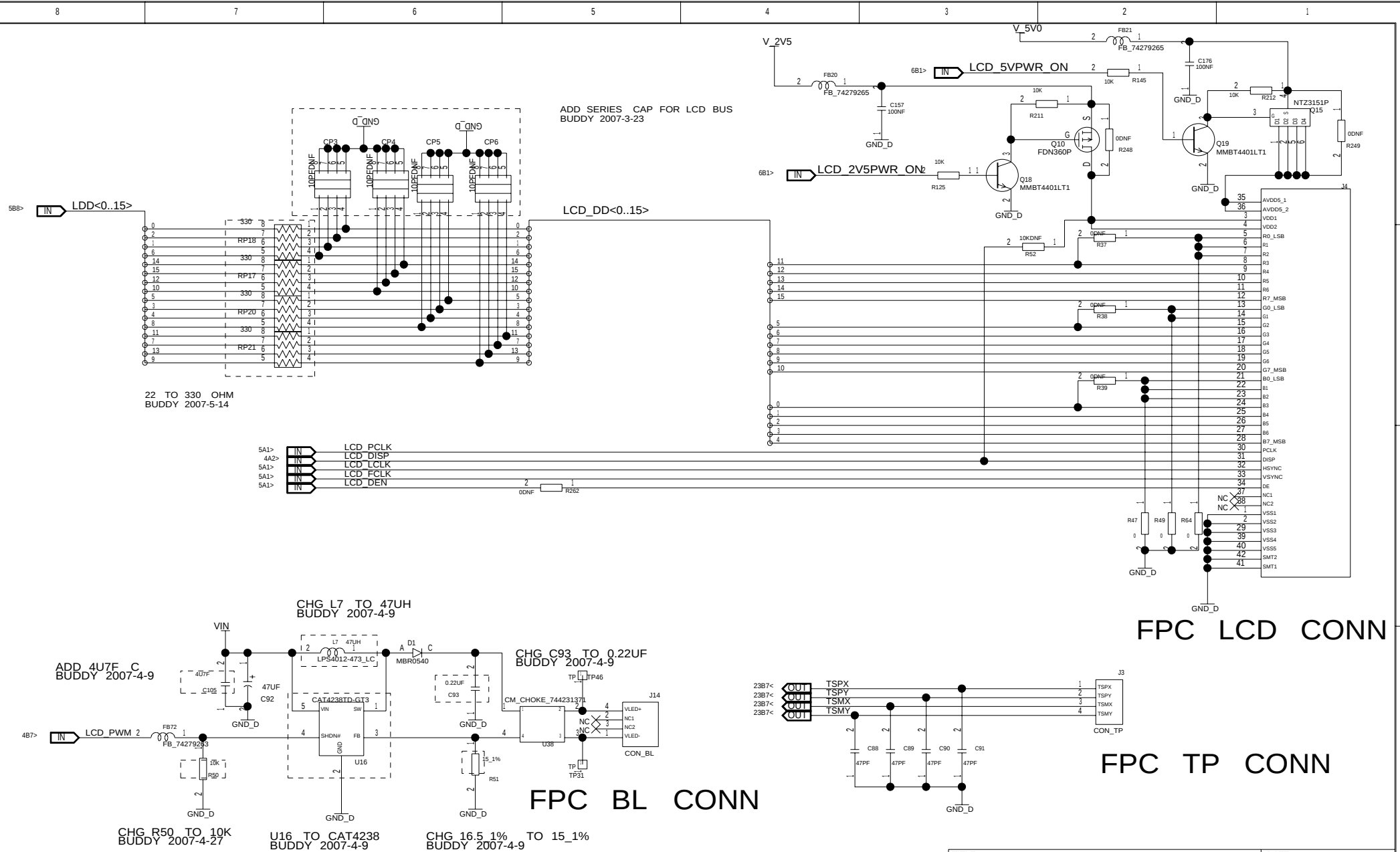
INTEL STRATA FLASH

FLASH SIZE IS 16MBYTE, A25&A24 ARE NC
 FLASH SIZE IS 64MBYTE, A25 IS NC
 FLASH SIZE IS 128MBYTE, NO NC

WHEN NOR FLASH SIZE IS 128MBYTE
 NOR FLASH A<0> OF PXA270
 CONNECTION TO A<25> OF NOR FLASH





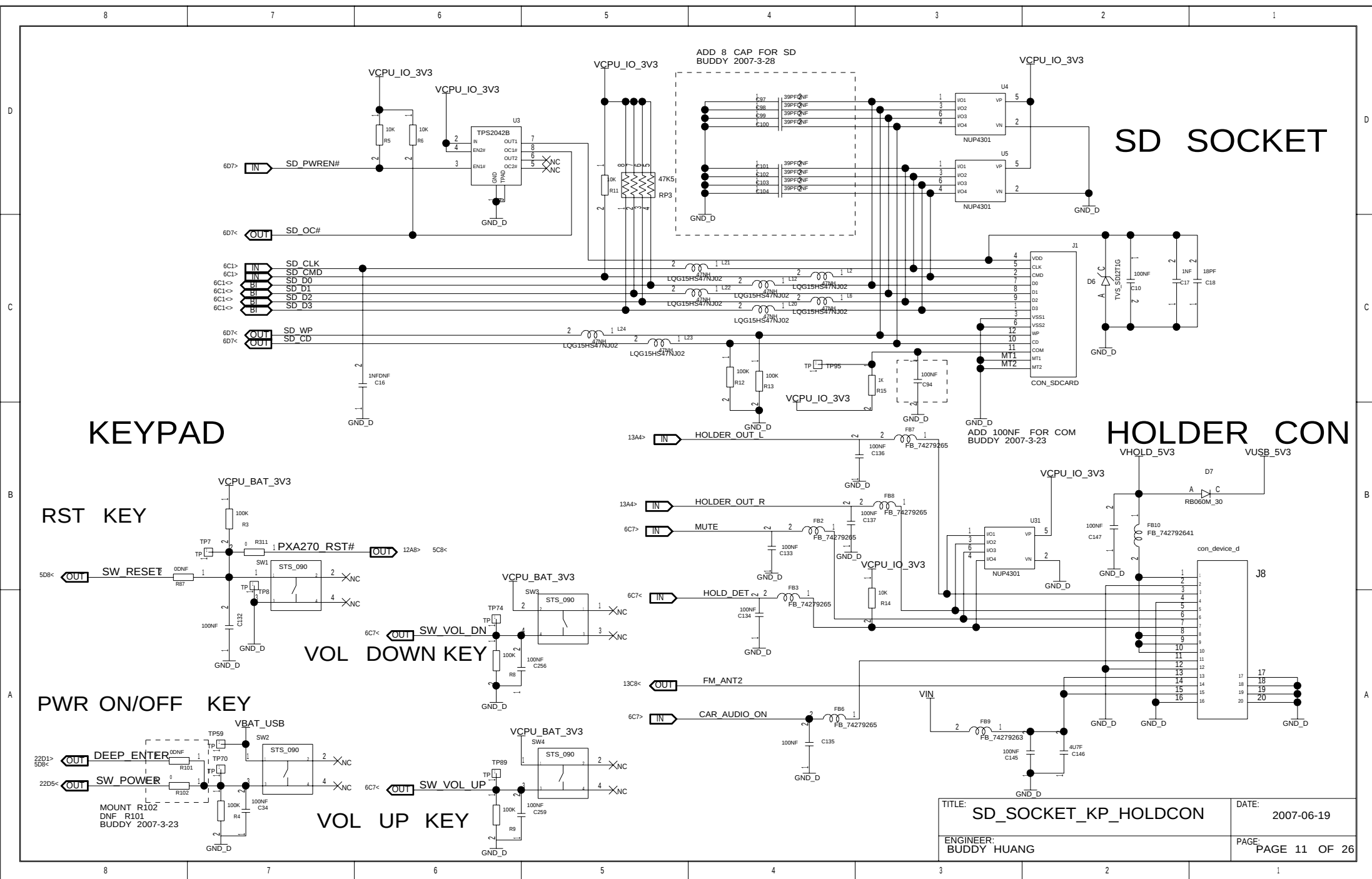


FPC LCD CONN

FPC TP CONN

FPC BL CONN

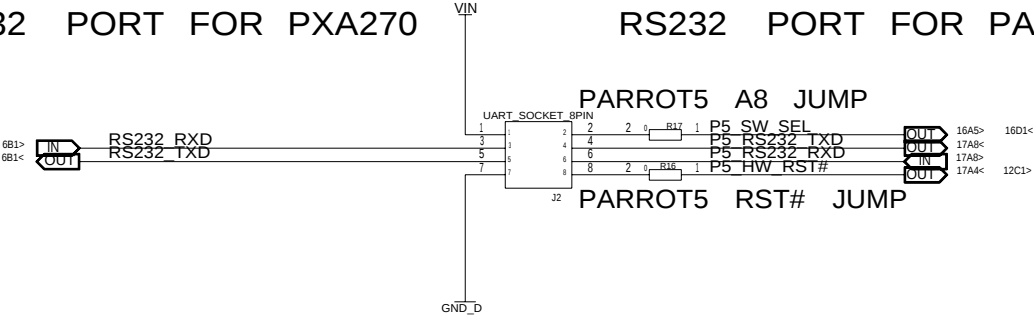
TITLE: LCD_BL_TP_CONN	DATE: 2007-06-19
ENGINEER: BUDDY HUANG	PAGE: PAGE 10 OF 26



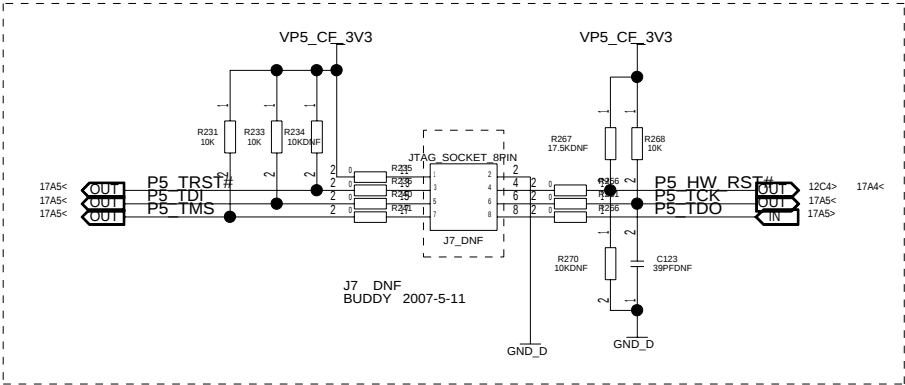
UART SOCKET

RS232 PORT FOR PXA270

RS232 PORT FOR PARROT5

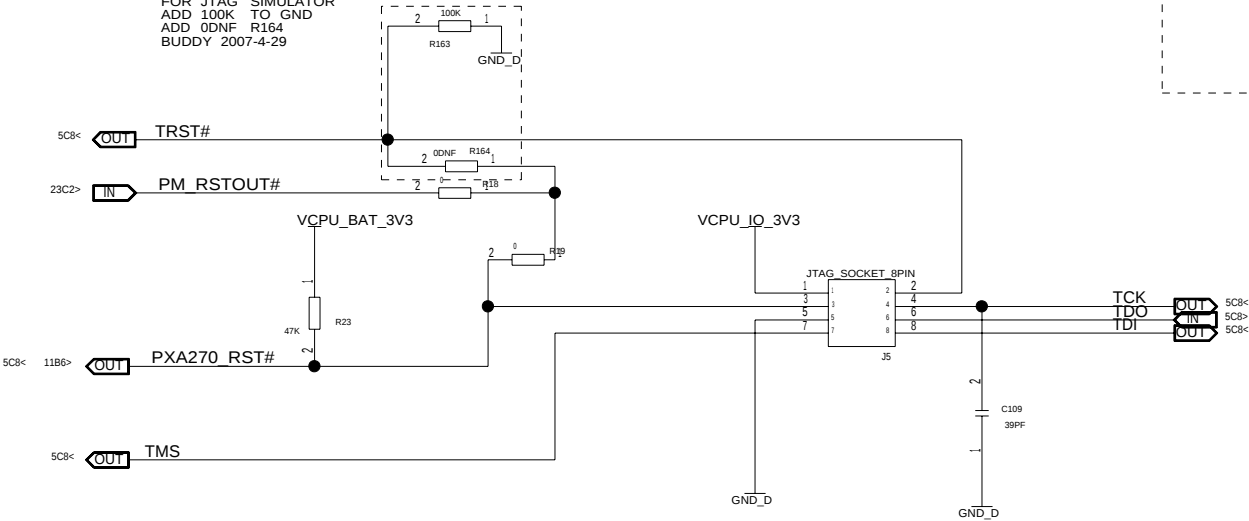


P5 JTAG SOCKET



PXA270 JTAG SOCKET

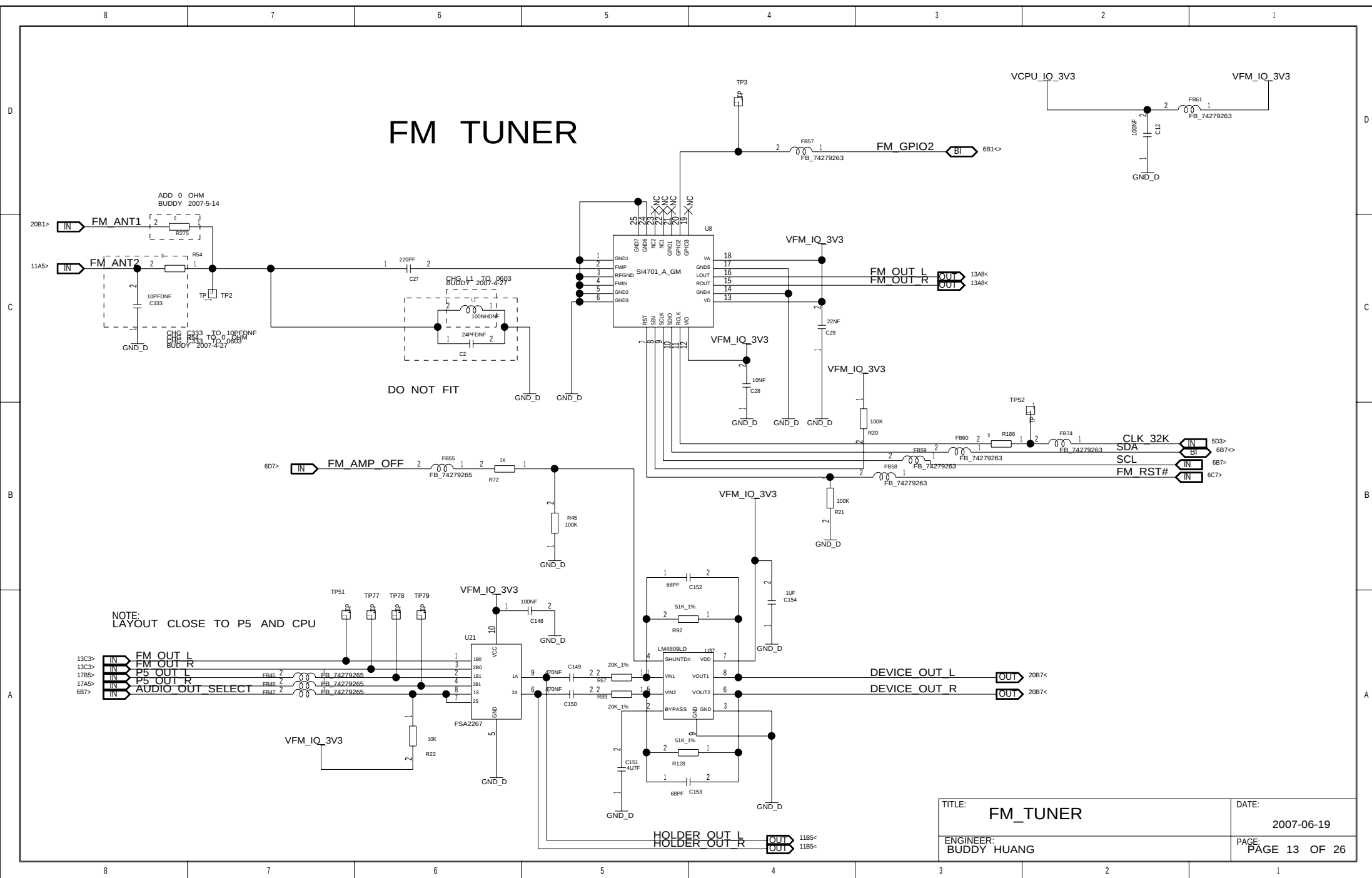
FOR JTAG SIMULATOR
ADD 100K TO GND
ADD 0DNF R164
BUDDY 2007-4-29

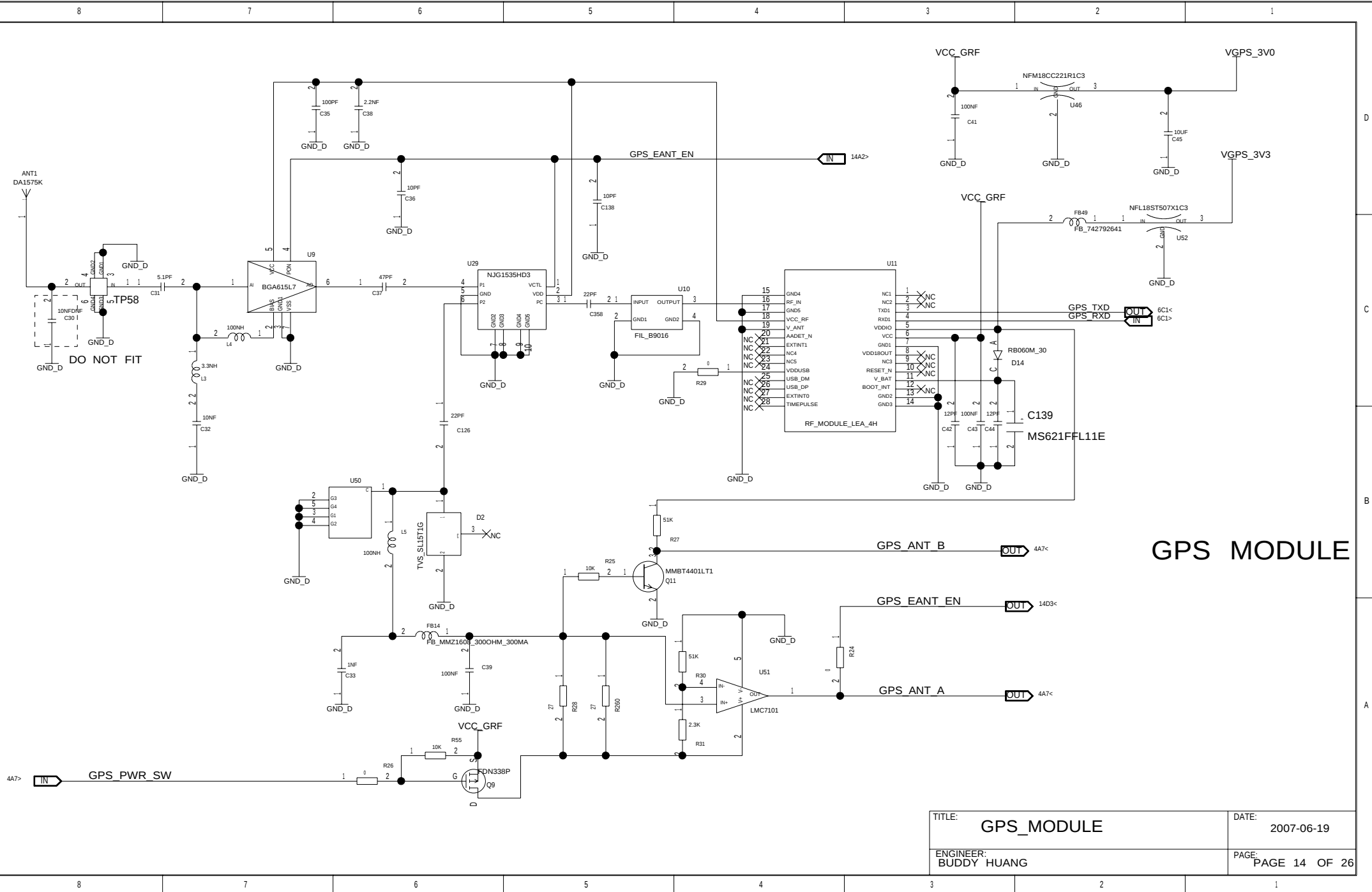


NOTE:RESET#
DEBUG MODE, TWO RESISTOR MOUNTED
ACTUAL PRODUCT, PM_RSTOUT# RESISTOR NO MOUNT

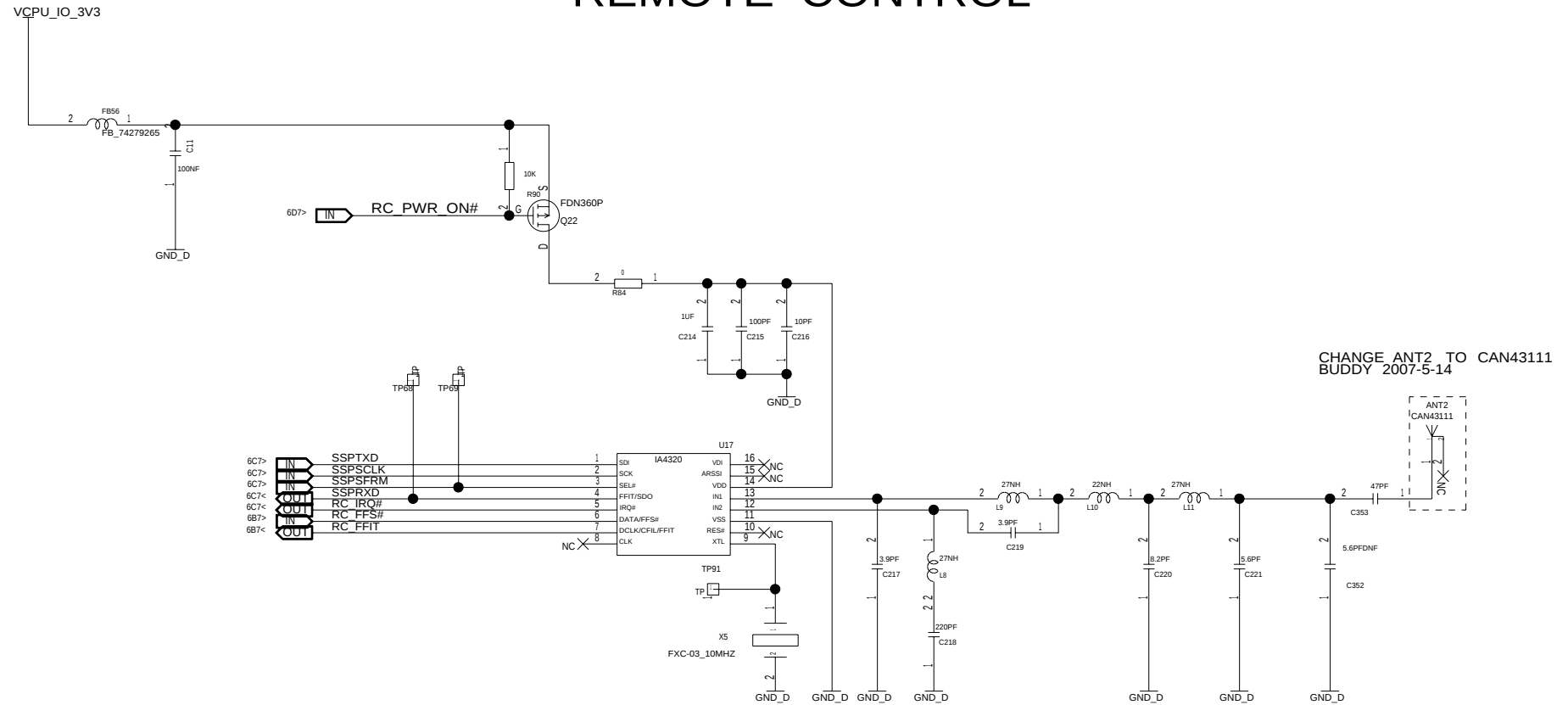
ADD PARROT5 JTAG SOCKET CIRCUIT
BUDDY 2007-4-29

TITLE:	CPU_P5_RS232_JTAG	DATE:	2007-06-19
ENGINEER:	BUDDY HUANG	PAGE:	PAGE 12 OF 26

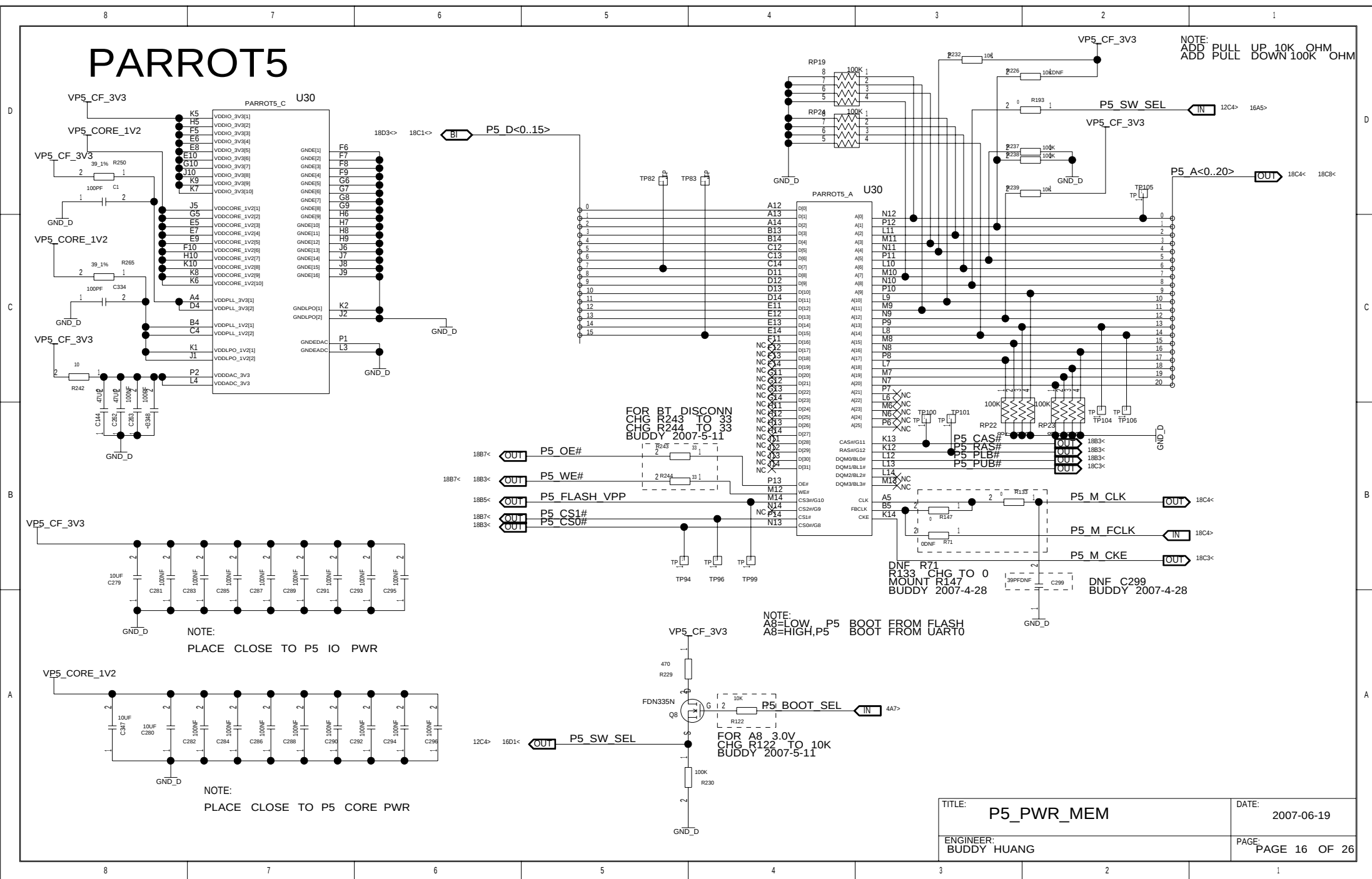


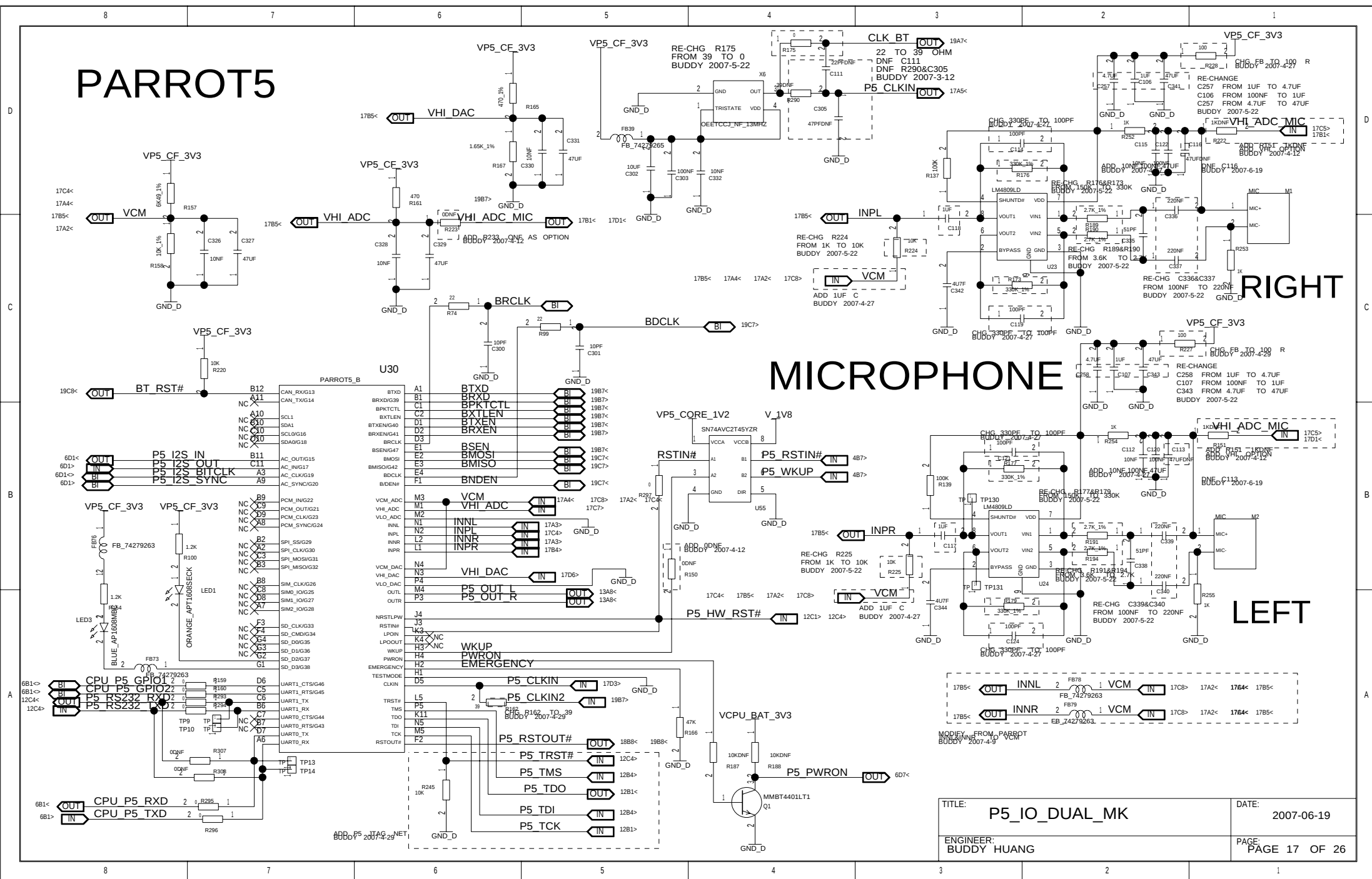


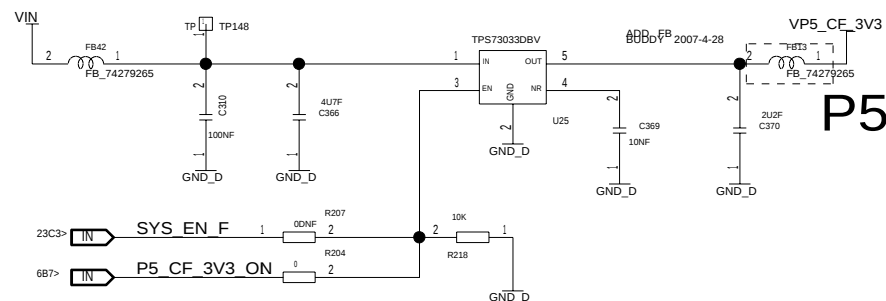
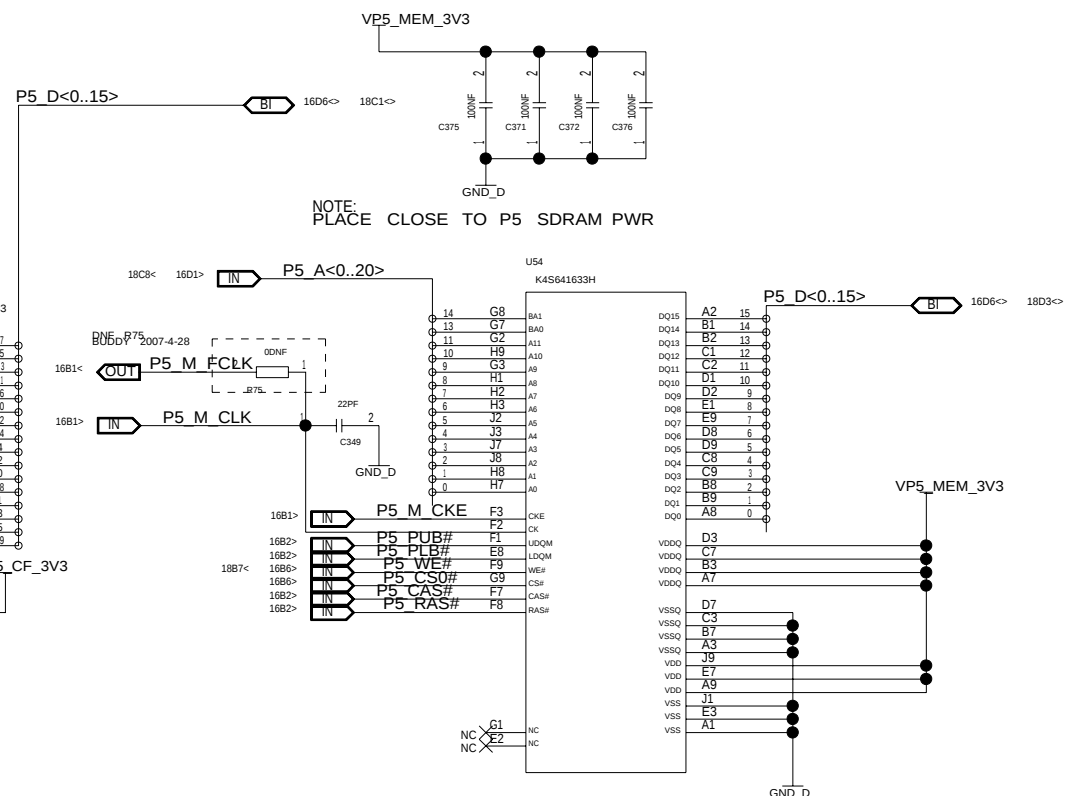
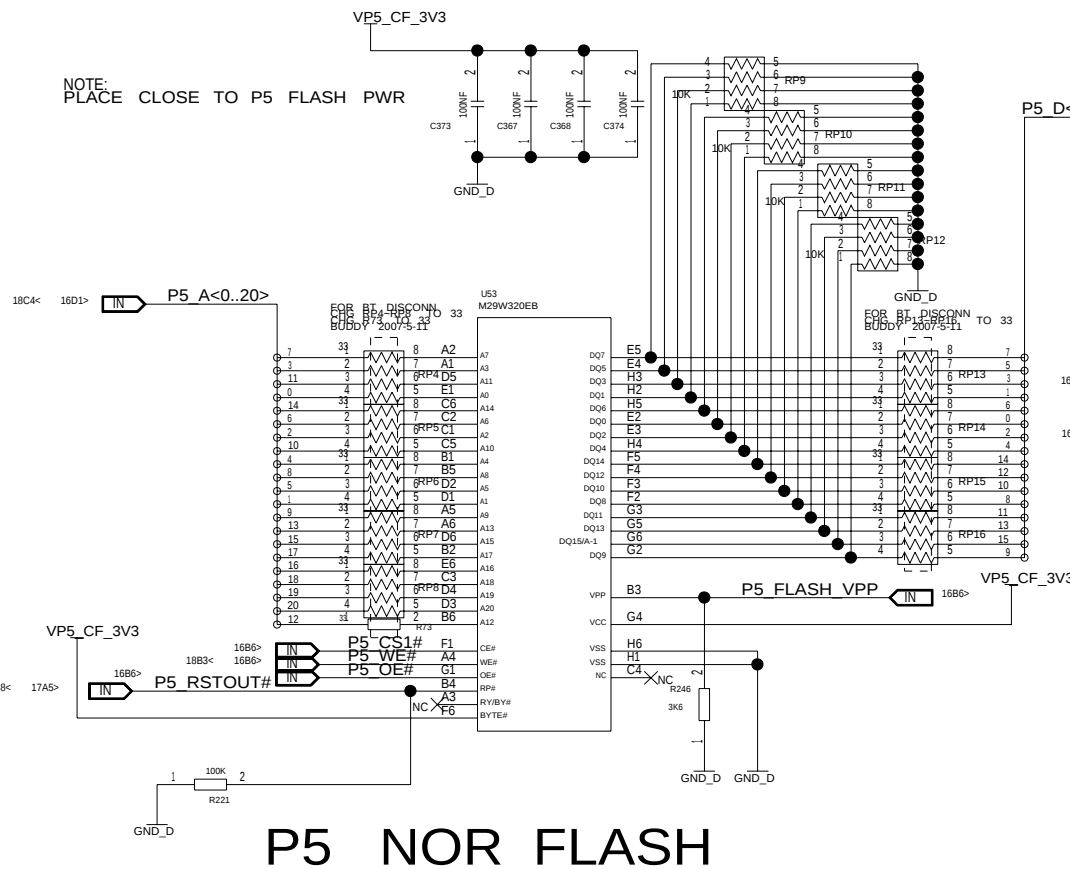
REMOTE CONTROL



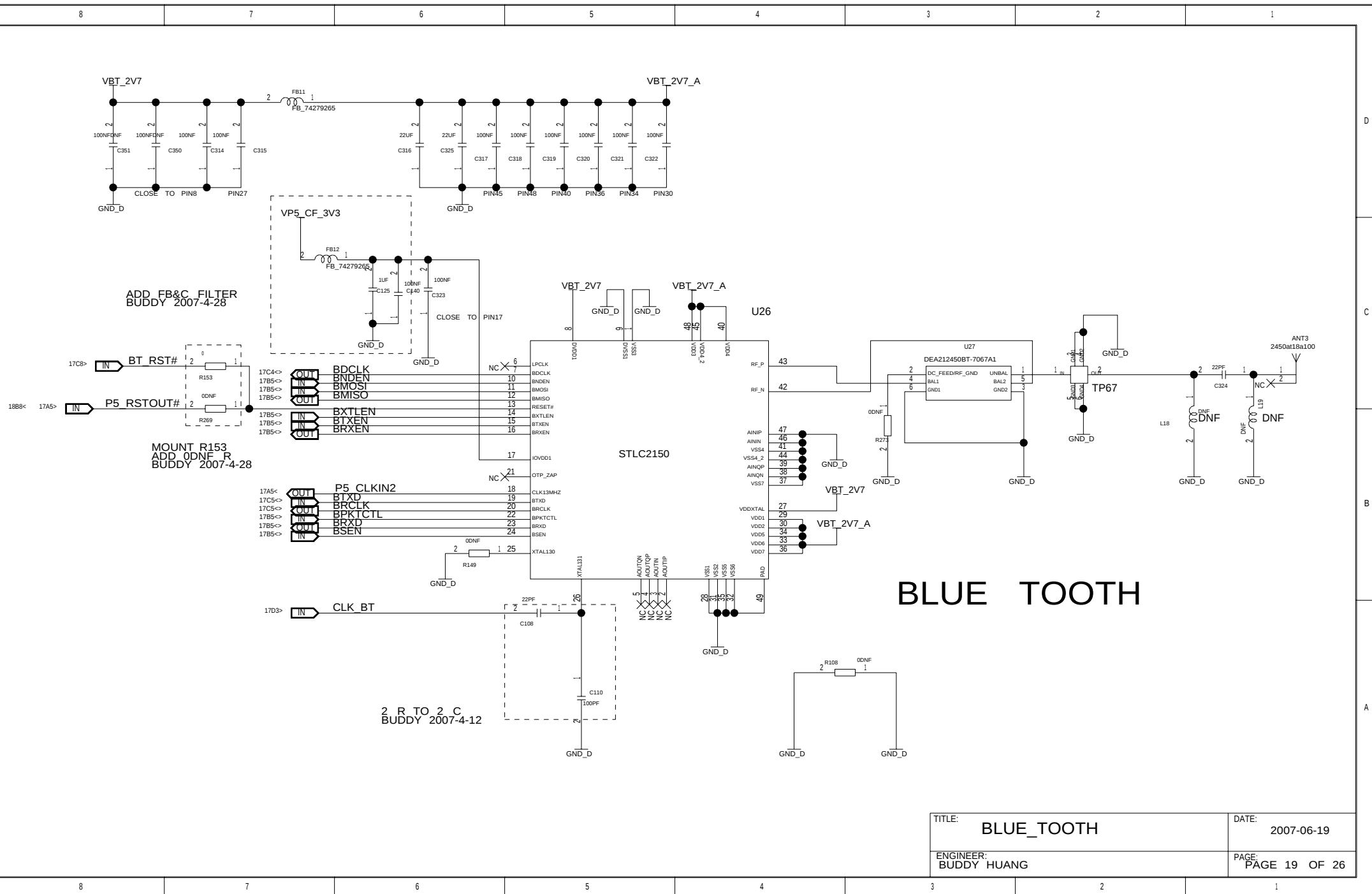
TITLE: REMOTE_CONTROL	DATE: 2007-06-19
ENGINEER: BUDDY HUANG	PAGE: PAGE 15 OF 26

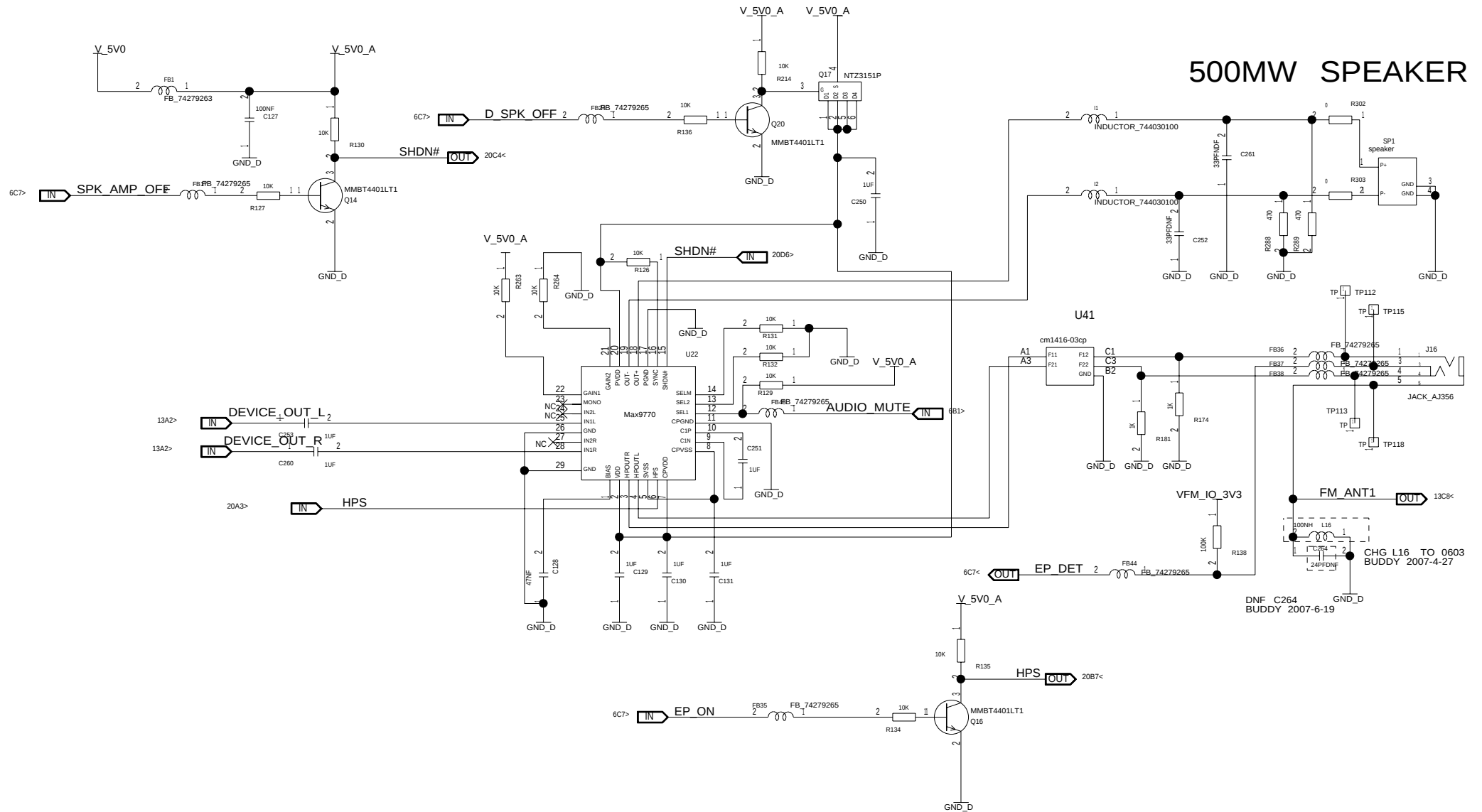




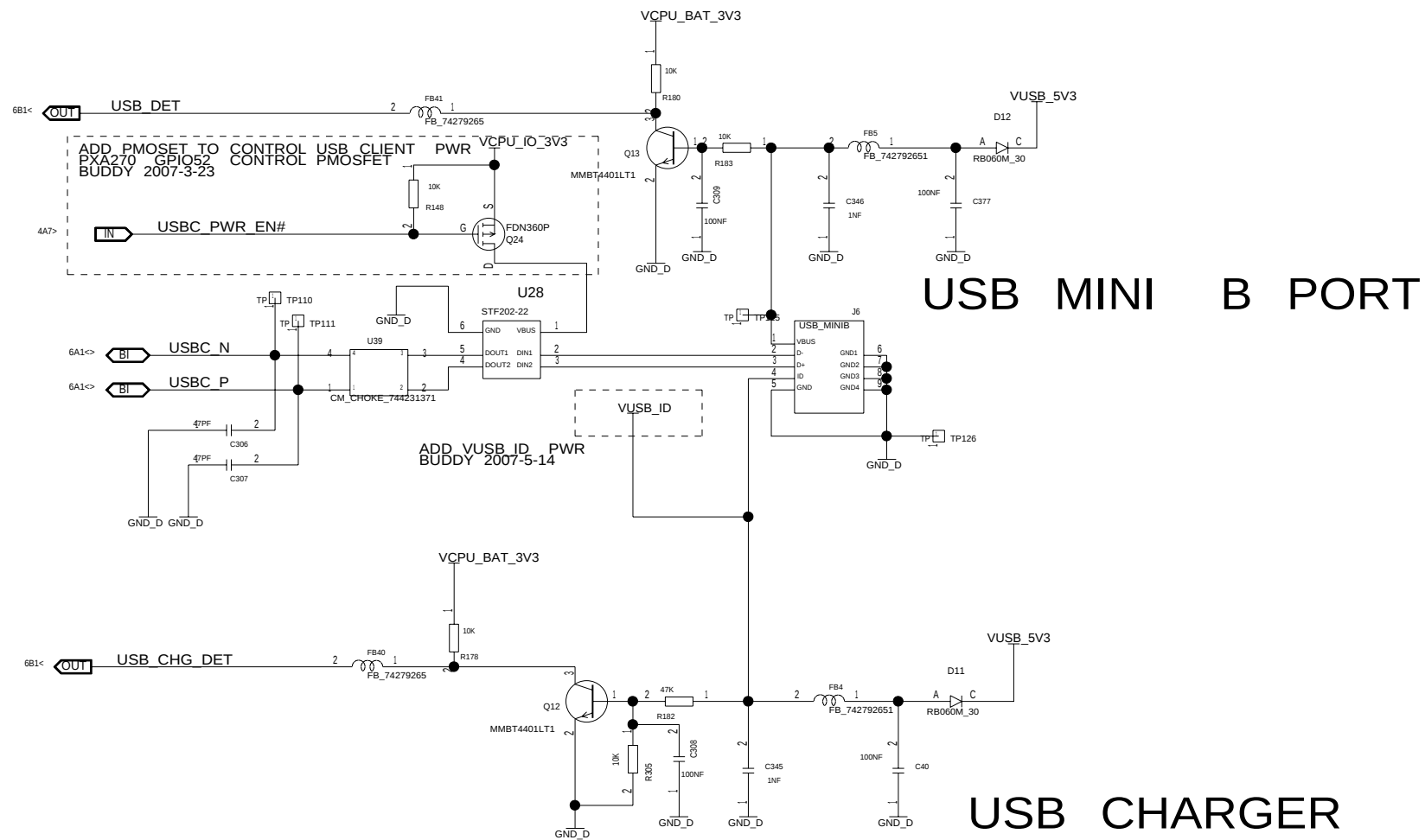


TITLE: P5_MEM_PWR_REG	DATE: 2007-06-19
ENGINEER: BUDDY HUANG	PAGE: PAGE 18 OF 26

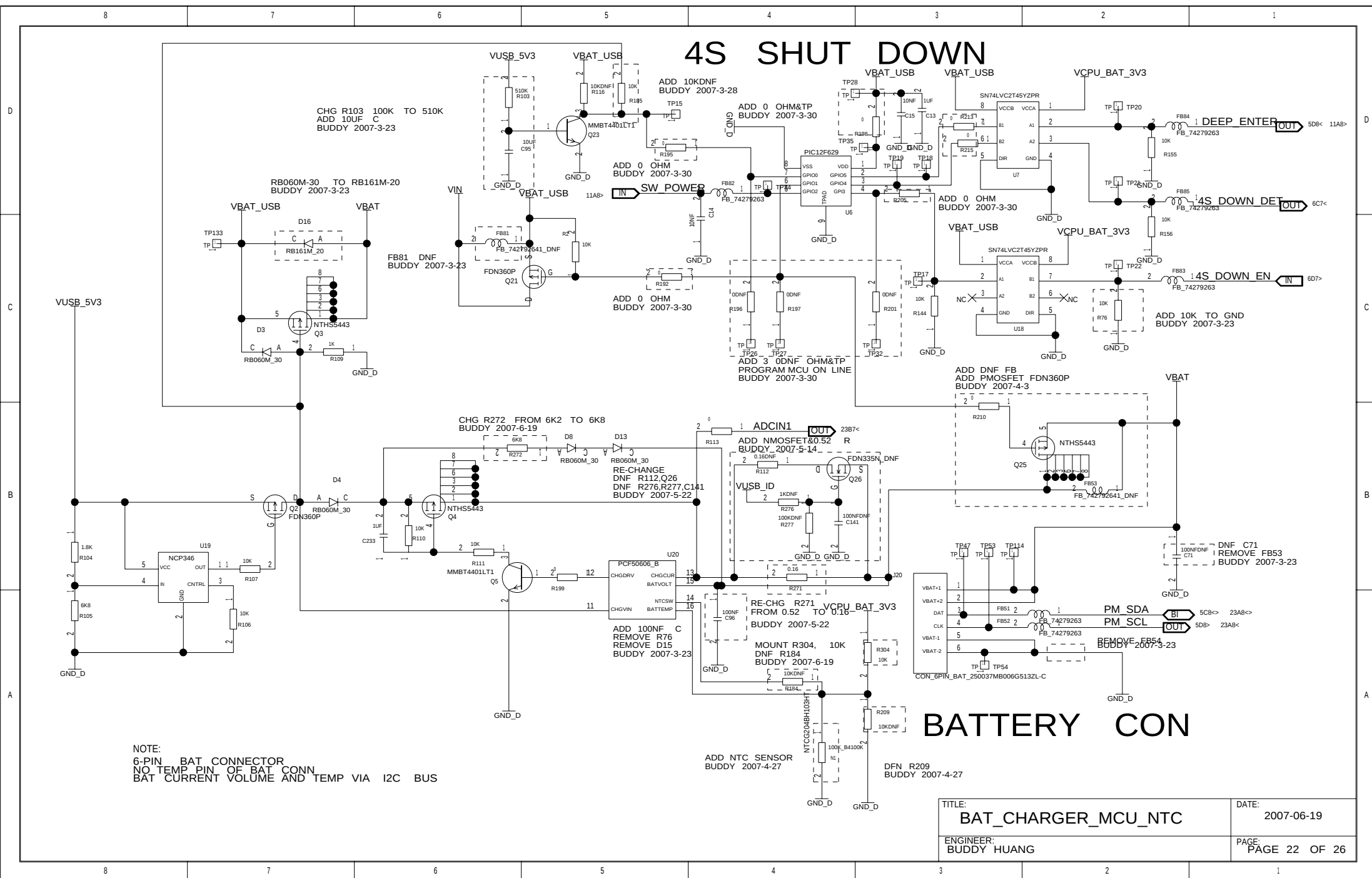


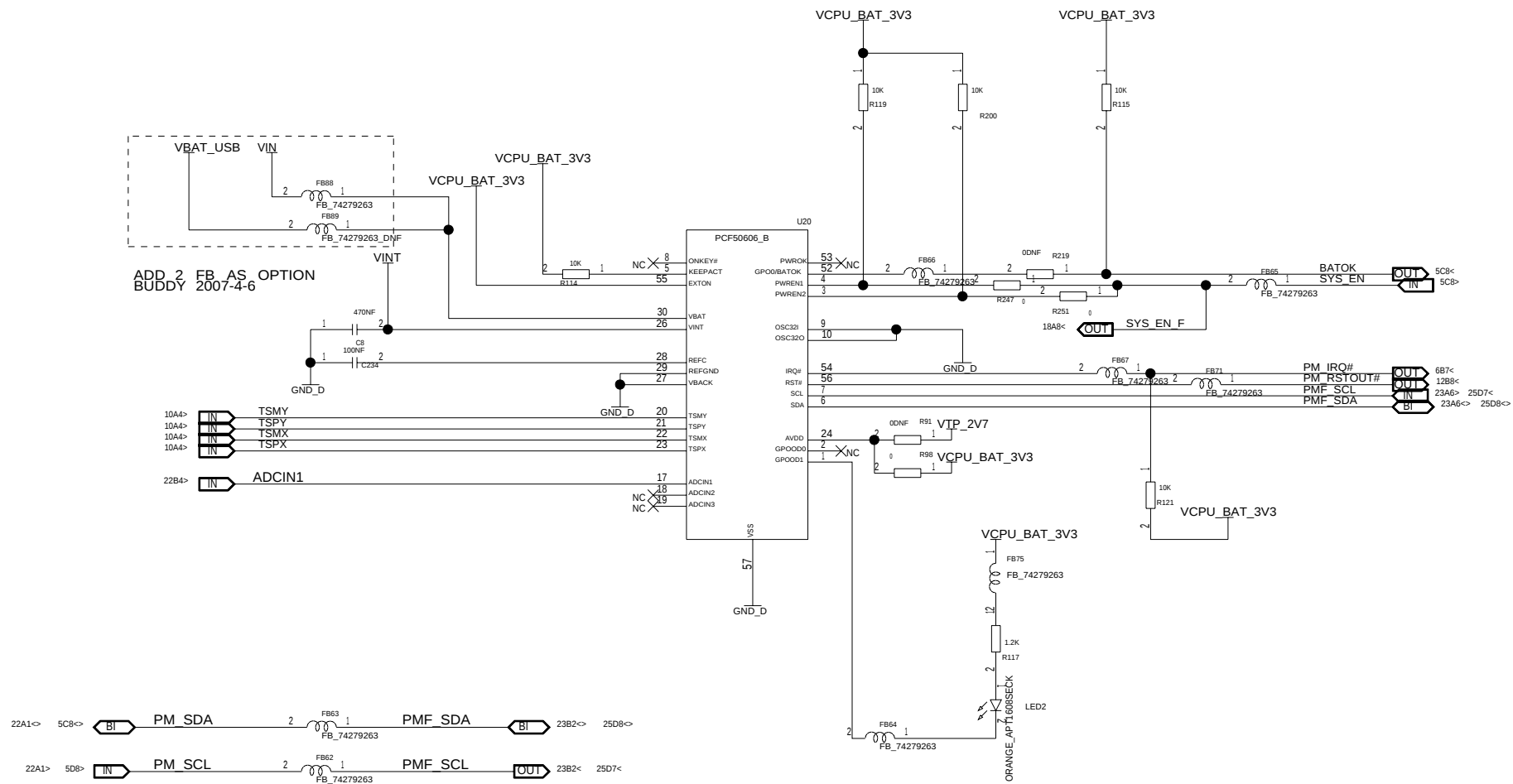


TITLE: SPEAKER_LINE_OUT_JACK	DATE: 2007-06-19
ENGINEER: BUDDY HUANG	PAGE: PAGE 20 OF 26

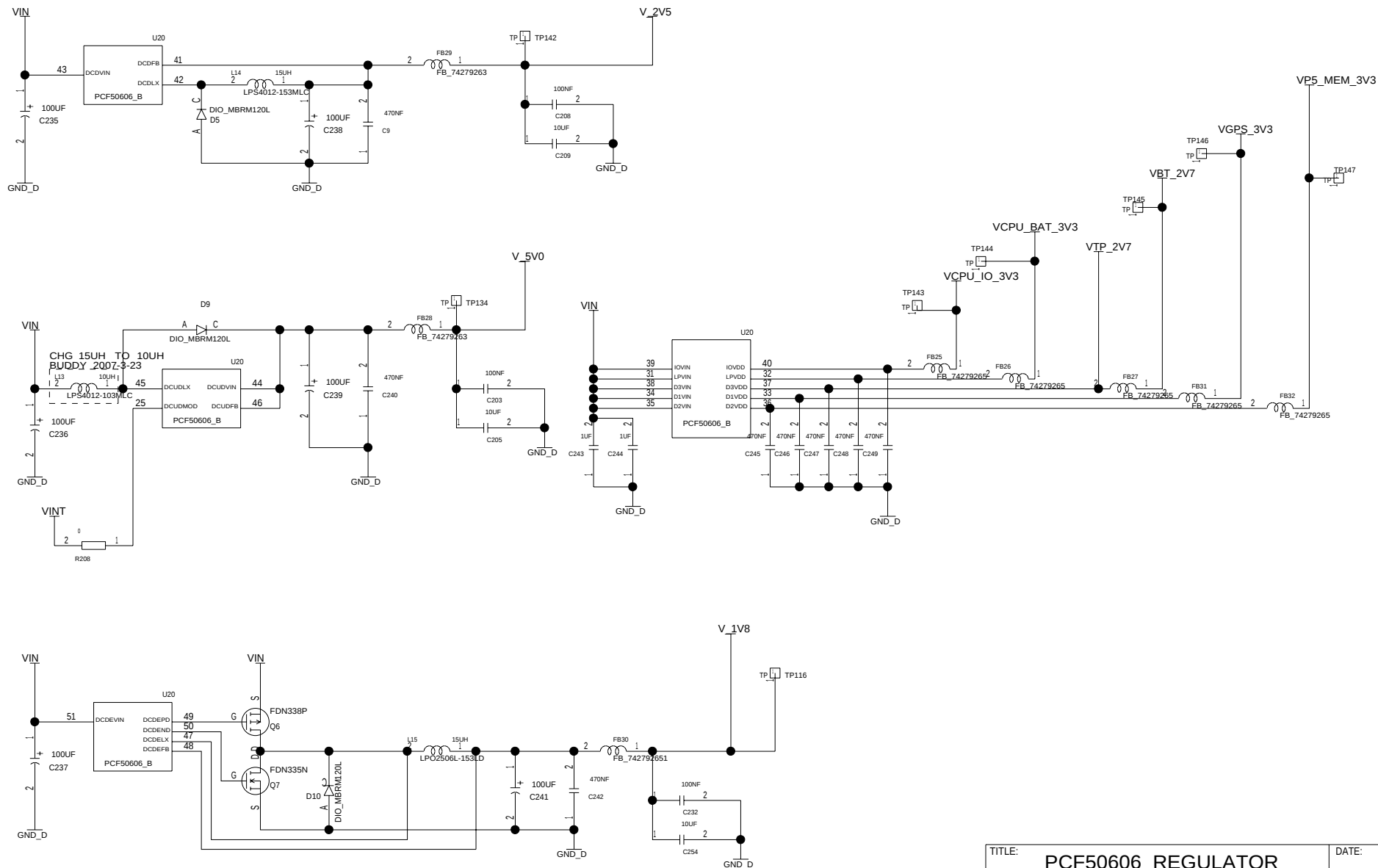


TITLE:	USB_CLIENT_CHG	DATE:	2007-06-19
ENGINEER:	BUDDY HUANG	PAGE:	PAGE 21 OF 26

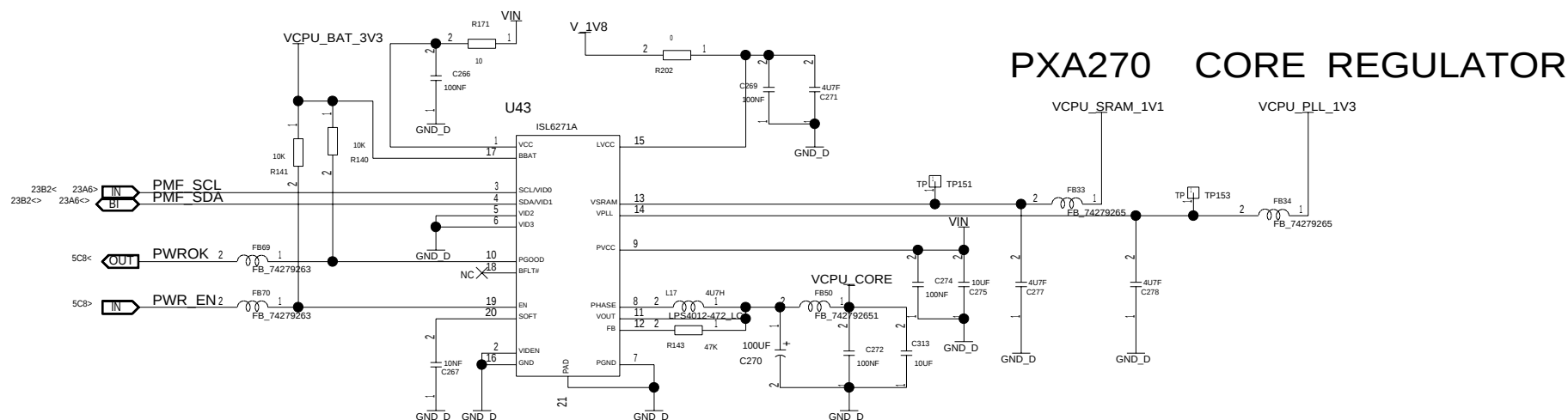




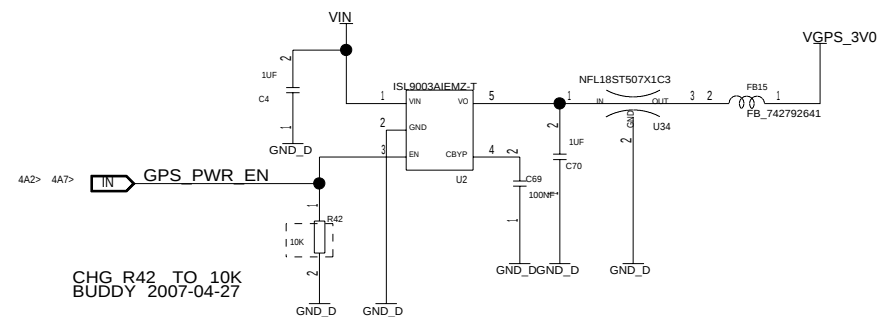
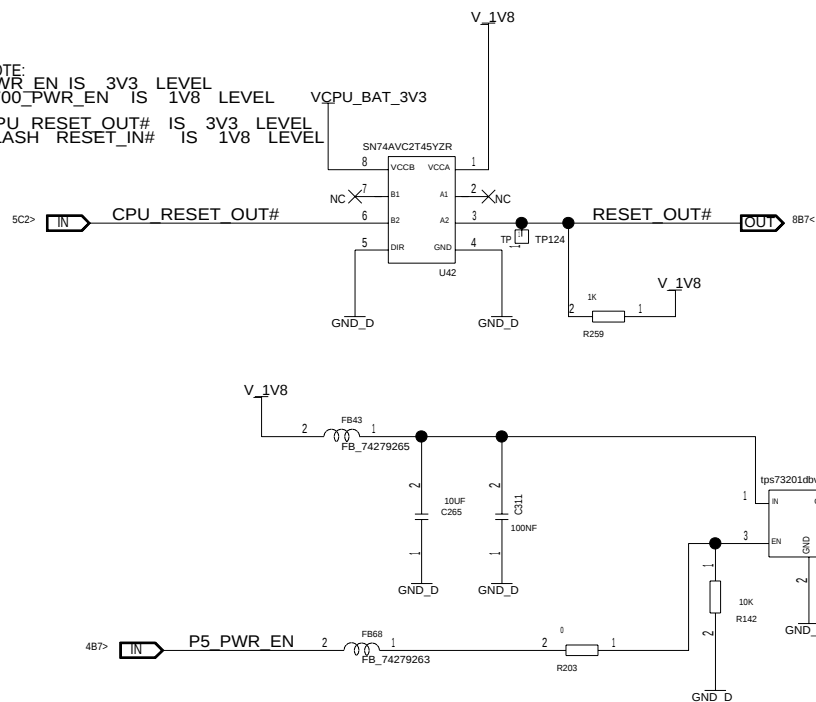
TITLE: PCF50606_IO		DATE: 2007-06-19	
ENGINEER: BUDDY HUANG		PAGE: PAGE 23 OF 26	



TITLE: PCF50606_REGULATOR	DATE: 2007-06-19
ENGINEER: BUDDY HUANG	PAGE: PAGE 24 OF 26



NOTE:
 PWR_EN IS 3V3 LEVEL
 2700_PWR_EN IS 1V8 LEVEL
 CPU RESET_OUT# IS 3V3 LEVEL
 FLASH RESET_IN# IS 1V8 LEVEL



TITLE: CORE_REGULATOR	DATE: 2007-06-19
ENGINEER: BUDDY HUANG	PAGE: PAGE 25 OF 26

