

Model: 5F60C2A (Receiver portion)
Technical Description of Receiver

The receiver operates at 49.860 MHz, and controls by the corresponding transmitter to move forward, backward, left and right.

The circuit of the receiver comprises of,

1. Super-regenerative circuit
Consisting: Q1
2. Decoder:
Consisting: U1 (TR10S SOP-16) , X1 (455kHz) as sytem clock
3. Motor driver
Consisting: U2 (SOP-16 MX1508)
4. A Power Supply
Consisting: 3.6 V DC source (1 X 3.6V internal rechargeable battery)