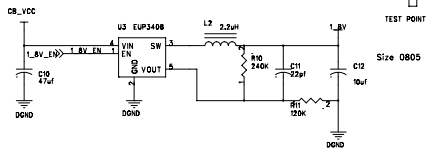
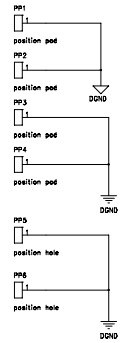
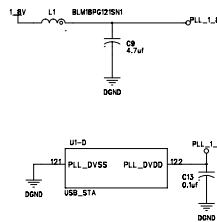


Thick trace to handle 1A @ 3.3V



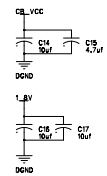
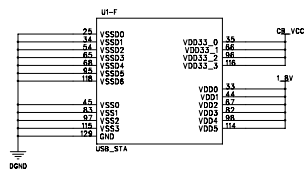
PUT A LOT OF COPPER AROUND TO ABSORB HEAT

Thick trace to handle 10mA @ 2.0V  
Run as a trace not plane

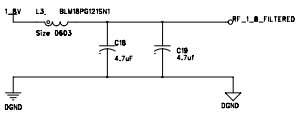


These need to be placed close to power pins  
These ones should be on top

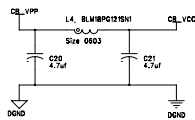
Chip may draw up to 1.5A @ 2.0V



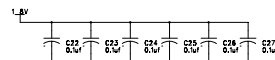
The traces should run through the Caps  
Thick trace to handle .3A @ 2.0V



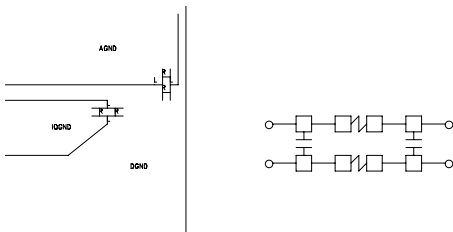
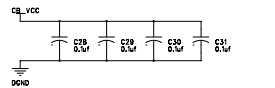
Place between Digital and RF power sections on the board  
Thick traces to handle 1A at 3.3V  
The traces should run through the Caps



These need to be placed close to power pins



These need to be placed close to power pins



# 3DSP-WLAN + AIROHA RF(AL2230) USB

Version B

Page list:

01	Cover	Cover page
02	BUS	USB interface
03	Power	The power supply part
04	RF Interface	The Airoha Transceiver & PA & Antenna
05	GPIO	The digital interface between DSP & RF & other parts

Version Information:

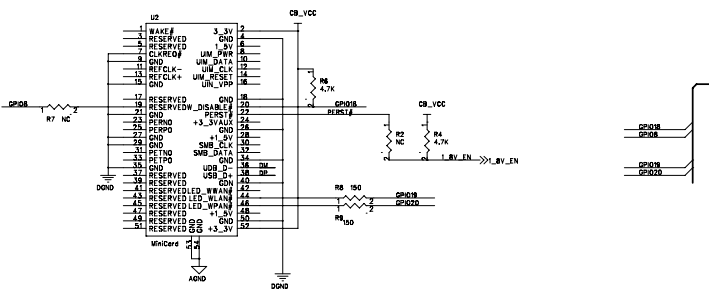
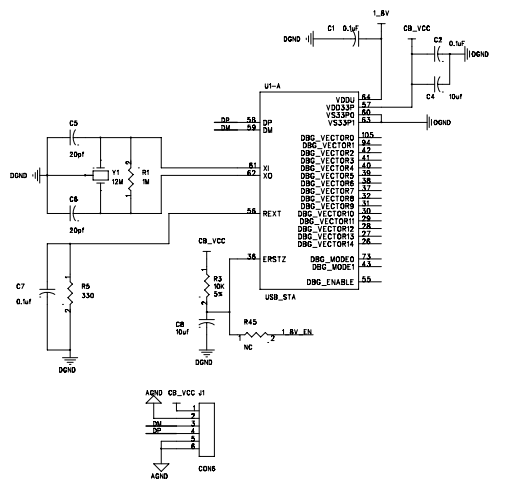
Version	Date	Comment	Designer
Ver 1.0	Feb-27-2008	Initial design, usb interface of MinCard	Aaron HW_Team/Xi'an
Ver 2.0	July-27-2008	Change to two antenna in page4	Aaron HW_Team/Xi'an
Ver 2.4	Jan-15-2009	No change	Isaac HW_Team/Xi'an
Ver 2.5	Jan-21-2009	Add PRST# signal to control the 1.8V power enable	Isaac HW_Team/Xi'an
Ver 2.6	Mar-5-2009	1.Added two capacitors on the antenna circuit 2.Corrected the connection relationship of the components <u>R13,C46 and R15,C59</u>	Isaac HW_Team/Xi'an
Ver 2.7	Mar-19-2009	Added two position holes	Isaac HW_Team/Xi'an
Ver 2.8	Apr-8-2009	Changed the W_disable contoll signal from GPIO7 to GPIO18	Isaac HW_Team/Xi'an

3DSP Corporation

\*\*\*\*\* NOTICE \*\*\*\*\*  
 These are schematics of a design example which is under development.  
 3DSP Corp. assumes no responsibility for the content and makes no commitment to  
 update any of the information herein.  
 \*\*\*\*\*

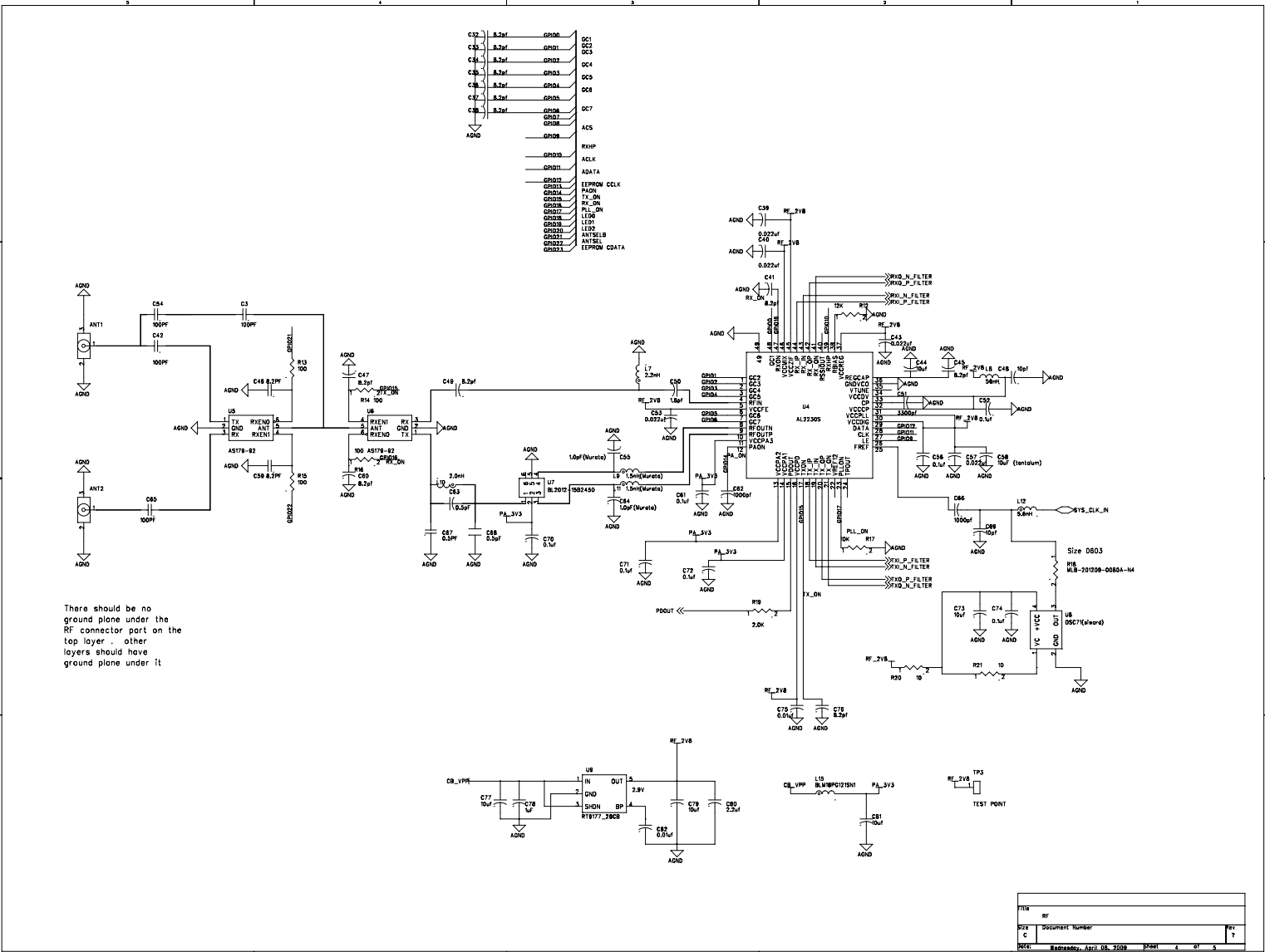
AL2230s	
Cover Page	
REV	DATE
AZ	2009-04-08
DATE	2009-04-08
1	5

Refer to Layout Reference for placement info



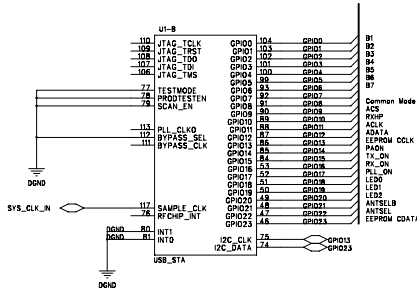
Title	BUS
Sheet	Document Number
C	?
DATE	Wednesday, April 08, 2009 10:08 AM 2 of 5

C12	8.2nf	GRND	OC1
C13	8.2nf	GRND	OC2
C14	8.2nf	GRND	OC3
C15	8.2nf	GRND	OC4
C16	8.2nf	GRND	OC5
C17	8.2nf	GRND	OC6
C18	8.2nf	GRND	OC7
C19	8.2nf	GRND	OC8
C20	8.2nf	GRND	OC9
C21	8.2nf	GRND	OC10
C22	8.2nf	GRND	OC11
C23	8.2nf	GRND	OC12
C24	8.2nf	GRND	OC13
C25	8.2nf	GRND	OC14
C26	8.2nf	GRND	OC15
C27	8.2nf	GRND	OC16
C28	8.2nf	GRND	OC17
C29	8.2nf	GRND	OC18
C30	8.2nf	GRND	OC19
C31	8.2nf	GRND	OC20
C32	8.2nf	GRND	OC21
C33	8.2nf	GRND	OC22
C34	8.2nf	GRND	OC23
C35	8.2nf	GRND	OC24
C36	8.2nf	GRND	OC25
C37	8.2nf	GRND	OC26
C38	8.2nf	GRND	OC27
C39	8.2nf	GRND	OC28
C40	8.2nf	GRND	OC29
C41	8.2nf	GRND	OC30
C42	8.2nf	GRND	OC31
C43	8.2nf	GRND	OC32
C44	8.2nf	GRND	OC33
C45	8.2nf	GRND	OC34
C46	8.2nf	GRND	OC35
C47	8.2nf	GRND	OC36
C48	8.2nf	GRND	OC37
C49	8.2nf	GRND	OC38
C50	8.2nf	GRND	OC39
C51	8.2nf	GRND	OC40
C52	8.2nf	GRND	OC41
C53	8.2nf	GRND	OC42
C54	8.2nf	GRND	OC43
C55	8.2nf	GRND	OC44
C56	8.2nf	GRND	OC45
C57	8.2nf	GRND	OC46
C58	8.2nf	GRND	OC47
C59	8.2nf	GRND	OC48
C60	8.2nf	GRND	OC49
C61	8.2nf	GRND	OC50
C62	8.2nf	GRND	OC51
C63	8.2nf	GRND	OC52
C64	8.2nf	GRND	OC53
C65	8.2nf	GRND	OC54
C66	8.2nf	GRND	OC55
C67	8.2nf	GRND	OC56
C68	8.2nf	GRND	OC57
C69	8.2nf	GRND	OC58
C70	8.2nf	GRND	OC59
C71	8.2nf	GRND	OC60
C72	8.2nf	GRND	OC61
C73	8.2nf	GRND	OC62
C74	8.2nf	GRND	OC63
C75	8.2nf	GRND	OC64
C76	8.2nf	GRND	OC65
C77	8.2nf	GRND	OC66
C78	8.2nf	GRND	OC67
C79	8.2nf	GRND	OC68
C80	8.2nf	GRND	OC69
C81	8.2nf	GRND	OC70
C82	8.2nf	GRND	OC71
C83	8.2nf	GRND	OC72
C84	8.2nf	GRND	OC73
C85	8.2nf	GRND	OC74
C86	8.2nf	GRND	OC75
C87	8.2nf	GRND	OC76
C88	8.2nf	GRND	OC77
C89	8.2nf	GRND	OC78
C90	8.2nf	GRND	OC79
C91	8.2nf	GRND	OC80
C92	8.2nf	GRND	OC81
C93	8.2nf	GRND	OC82
C94	8.2nf	GRND	OC83
C95	8.2nf	GRND	OC84
C96	8.2nf	GRND	OC85
C97	8.2nf	GRND	OC86
C98	8.2nf	GRND	OC87
C99	8.2nf	GRND	OC88
C100	8.2nf	GRND	OC89
C101	8.2nf	GRND	OC90
C102	8.2nf	GRND	OC91
C103	8.2nf	GRND	OC92
C104	8.2nf	GRND	OC93
C105	8.2nf	GRND	OC94
C106	8.2nf	GRND	OC95
C107	8.2nf	GRND	OC96
C108	8.2nf	GRND	OC97
C109	8.2nf	GRND	OC98
C110	8.2nf	GRND	OC99
C111	8.2nf	GRND	OC100

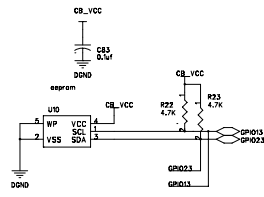


There should be no ground plane under the RF connector part on the top layer. other layers should have ground plane under it

File	RF
Sheet	Document Number
C	
Rev	
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Page	4 of 5

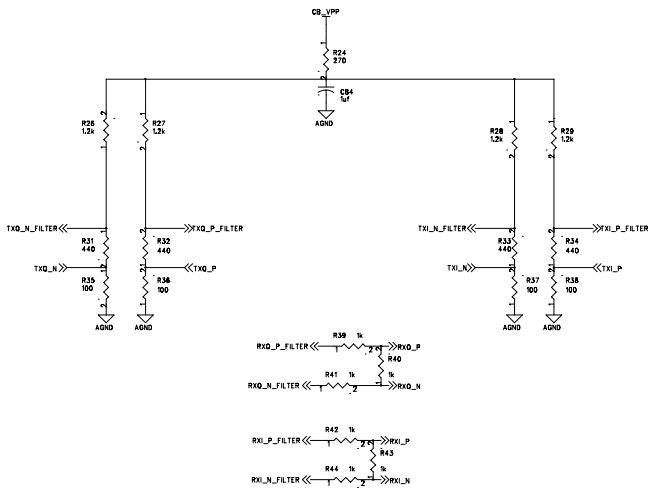


DSP Config and GPIOs

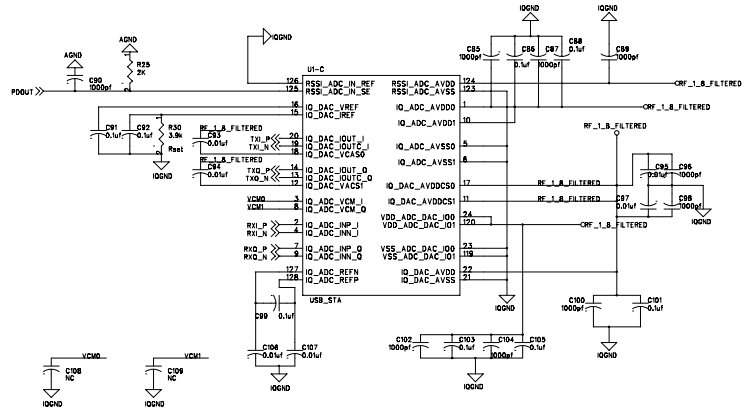


EEPROM

TX/RX RFI



IQ DAC, IQ ADC, RSSI ADC



116	GPIO	
117	USCCPRT Number	117
C		?