



产品差异说明:

W268R: 1T1R, RT3050, 2M FLASH, 16M SDRAM, 内置2根天线
 W368R: 2T2R, RT3052, 2M FLASH, 16M SDRAM, 内置2根天线
 W311R: 1T1R, RT3050, 2M FLASH, 16M SDRAM, 外置2根5dbi不可拆卸天线
 W311R+: 1T1R, RT3050, 2M FLASH, 16M SDRAM, 外置2根3dbi可拆卸天线
 W268R、W368R、W311R、W311R+共用W268R的PCB设计
 W306R、W307R: 2T2R, RT3052, 4M FLASH, 32M SDRAM
 外置2根5dbi不可拆卸天线, 共用W306R的PCB设计

W306R与W268R共用W268R的原理设计, 除PCB定位结构而造成的差异, 在电气主体部分, W306R的PCB设计基本与W268R一致

RT3050与RT3052 PIN脚PIN数, 只是增加了TX/RX1 RF通道 (原理图上均有虚线框说明), 其它基本一致

原理说明:

Operational Principle
 1. CPU: U1, The RT3052 SOC combines Malink's 802.11n draft compliant 2T2R(RT3050:1T1R) MAC/BPF/2.4G ISM band Transceiver, a high performance 384MHz MIPS24KEC CPU core, 5-port integrated I/O
 Ethernet switch/SRV: An 40MHz crystal(U2) for CPU's reference frequency.
 2. SDRAM: U8, U7 Hynix HY57V641620FTP-7 CMOS Synchronous DRAM, 16M Byte (4M X 16bit X 2FCs); OR U8 Hynix HY57V281620FTP-H CMOS Synchronous DRAM, 16M Byte (8M X 16bit X 1PCs)
 3. FLASH: U6 MXIC KH29LV320(160)CBTC-700/MX29LV320(160)CBTC-700 4(2)M Byte FLASH, used for store firmware and user's settings;
 4. PA: U10, U12 Richtek R62691, Freq.Range:2.4GHz-2.5GHz, PA For transmitter, Gain:13.5db (Max);
 5. LNA: Q1,Q2, Sirenza SGA83432, Freq.Range:2.4GHz-2.5GHz,LNA For RF Receiver, Gain:17db (Max);
 6. BPF: U9,U11, BFD012-E2R4DAA, Freq.Range:2.4GHz-2.5GHz,IL@BW:2.5-3db;
 7. RF Switch: U13,U15, Skyworks AS179-92, transmit /receive SPDT Switch;
 8. Power part: U16 and U14, CP2894 Step-Down PWM Converter, used for transfer DC8-14V to DC3.3V and DC1.5V; ; U25 SB8117ALF LDO used for transfer DC3.3V to DC2.0V;

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