



- Operational Description

When the U4(DWM3200) operates in audio mode,

DWM3200 of compressed sampled stereo audio data transformed into a serial data stream with its internal hardwired 4:1 near lossless compressor.

Redundancy bits for forward error correction(read Solomon), synchronization flags, and control signals for time division duplexing(TDD) are appended to data frames for error-free transmission.

IIC(2wire serial interface) Interface can down program to U3(AT24C32)

DWM3200 start to load operation parameters from U3(EEPROM AT24C32)using an IIC power on

And Radio Interface can control SiW1711 with SPI

SPI is in charge of several function such as channel setup, frequency hopping control for interface and the control signal for TDD and SiW1711

The data frames are modulated to RF with U1 (SiW1711)

The amplified RF signal by PA1 go into BPF and Antenna