



X\_CAP\_CLKOUT >> X\_CAP\_CLKOUT

X\_CAP0\_HS  
X\_CAP0\_VS  
X\_CAP0\_FIELD  
X\_CAP0\_CLK  
X\_CAP0\_D[0..7]

X\_CAP1\_CLK  
X\_CAP1\_D[0..7]

CAP0\_HS  
CAP0\_VS  
CAP0\_FIELD  
CAP0\_CLK  
CAP0\_D[0..7]

CAP1\_CLK  
CAP1\_D[0..7]

CAP\_CLKOUT >> CAP\_CLKOUT

X\_I2S\_FS  
X\_I2S\_SCLK  
X\_I2S\_TXD  
X\_I2S\_RXD

I2S\_FS  
I2S\_SCLK  
I2S\_TXD  
I2S\_RXD

I2C\_SCL  
I2C\_SDA

PWM0  
PWM1  
PWM2  
PWM3

X\_SPI\_FS0  
X\_SPI\_FS1  
X\_SPI\_SCLK  
X\_SPI\_TXD  
X\_SPI\_RXD

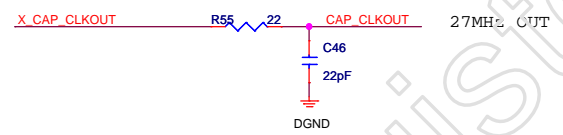
SPI\_SCLK  
SPI\_FS0  
SPI\_FS1  
SPI\_TXD  
SPI\_RXD

X\_CAP0\_HS R147 22 CAP0\_HS  
X\_CAP0\_VS R148 22 CAP0\_VS  
X\_CAP0\_FIELD R149 22 CAP0\_FIELD  
X\_CAP0\_CLK R150 22 CAP0\_CLK

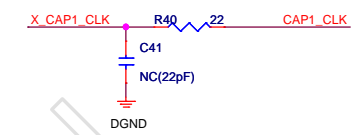


X\_CAP0\_D7 R153 22 CAP0\_D7  
X\_CAP0\_D6 R154 22 CAP0\_D6  
X\_CAP0\_D5 R155 22 CAP0\_D5  
X\_CAP0\_D4 R156 22 CAP0\_D4

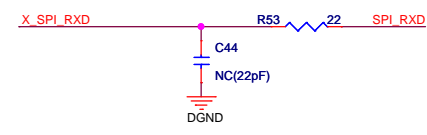
X\_CAP0\_D3 R157 22 CAP0\_D3  
X\_CAP0\_D2 R158 22 CAP0\_D2  
X\_CAP0\_D1 R159 22 CAP0\_D1  
X\_CAP0\_D0 R160 22 CAP0\_D0



X\_I2S\_FS R148 22 I2S\_FS GPIO0\_19  
X\_I2S\_RXD R150 22 I2S\_RXD GPIO0\_20  
X\_I2S\_SCLK R149 22 I2S\_SCLK GPIO0\_21  
X\_I2S\_TXD R152 22 I2S\_TXD GPIO0\_22



X\_SPI\_FS0 R51 22 SPI\_FS0  
X\_SPI\_FS1 R56 22 SPI\_FS1  
X\_SPI\_SCLK R52 22 SPI\_SCLK  
X\_SPI\_TXD R54 22 SPI\_TXD



X\_CAP1\_D7 R161 22 CAP1\_D7  
X\_CAP1\_D6 R162 22 CAP1\_D6  
X\_CAP1\_D5 R163 22 CAP1\_D5  
X\_CAP1\_D4 R164 22 CAP1\_D4

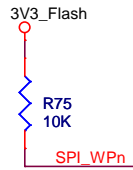
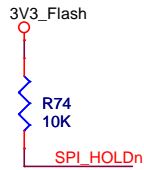
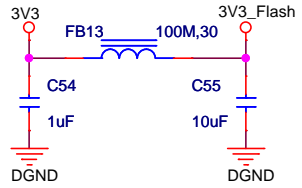
X\_CAP1\_D3 R165 22 CAP1\_D3  
X\_CAP1\_D2 R166 22 CAP1\_D2  
X\_CAP1\_D1 R167 22 CAP1\_D1  
X\_CAP1\_D0 R168 22 CAP1\_D0

Title		
PIN MUX		
Size	Document Number	Rev
B	OTIPCAM8126	1VA
Date:	Tuesday, June 05, 2012	Sheet 1 of 1

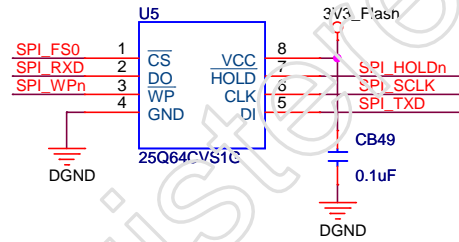


SPI\_SCLK  
SPI\_FS0  
SPI\_FS1  
SPI\_TXD  
SPI\_RXD

SPI\_SCLK  
SPI\_FS0  
SPI\_FS1  
SPI\_TXD  
SPI\_RXD



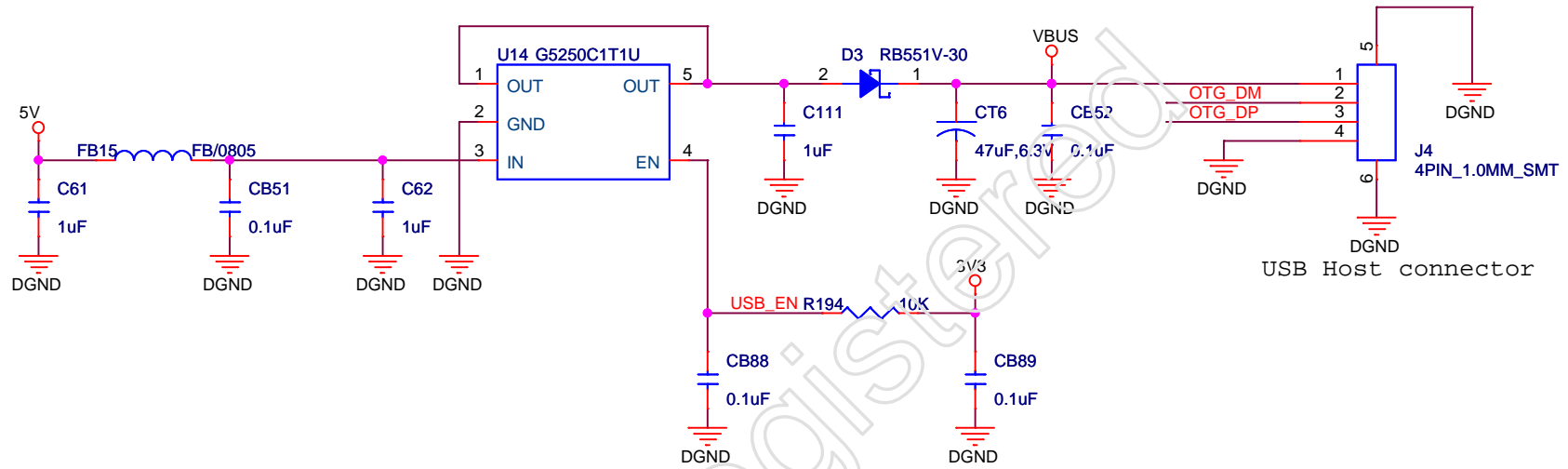
Overlap



UnRegistered

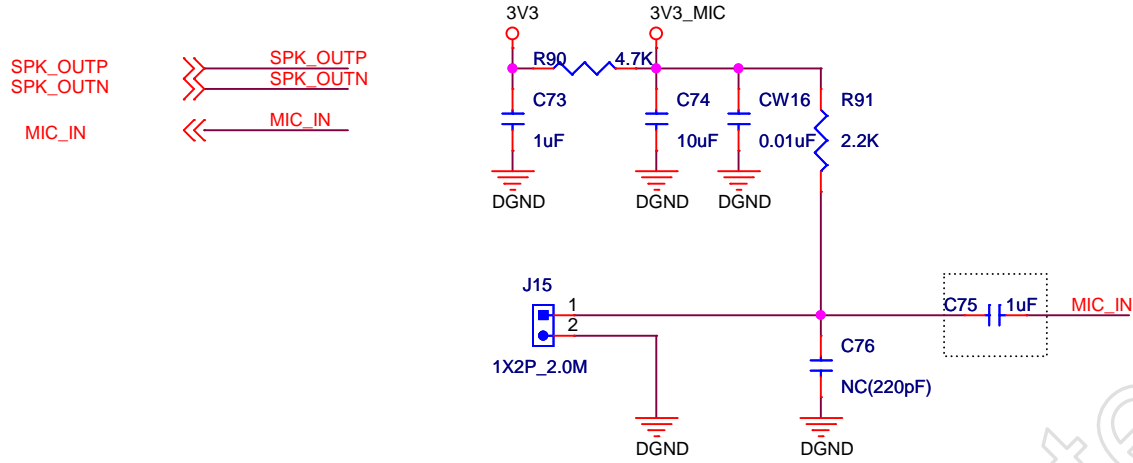
OTG\_DP  
OTG\_DM

OTG\_DP  
OTG\_DM

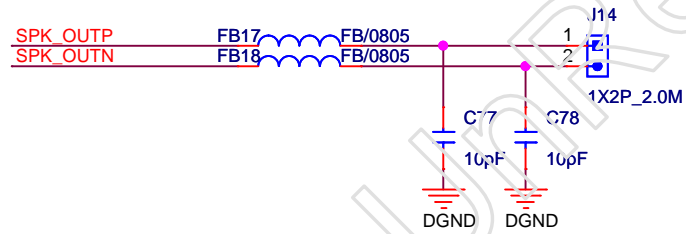


Title		
USB		
Size	Document Number	Rev
A	OTIPCAM8126	1VA
Date:	Tuesday, June 05, 2012	Sheet 1 of 1

MIC IN



Speaker OUT





I2C\_SCL  
I2C\_SDA

ICE\_TDO  
ICE\_TRSTn  
ICE\_TDI  
ICE\_TMS  
ICE\_TCK

UART0\_RX  
UART0\_TX  
UART1\_TX  
UART1\_RX  
UART2\_TX

5V\_IN  
ALIVE\_IN

CAP0\_HS  
CAP0\_VS  
CAP0\_FIELD  
CAP0\_CLK  
CAP0\_D[0..7]

CAP1\_CLK  
CAP1\_D[0..7]

CAP\_CLKOUT

WAKEUP\_IN

DAC\_IOUTA

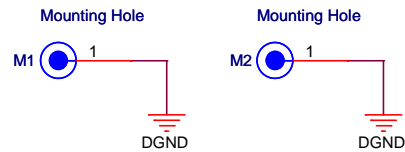
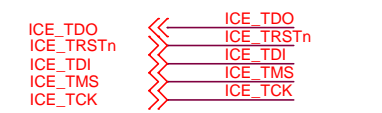
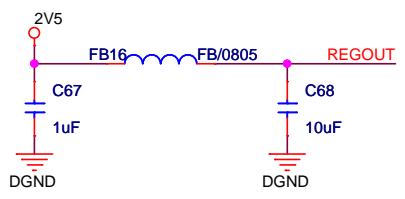
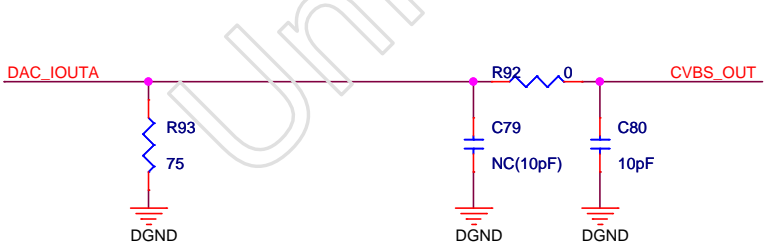
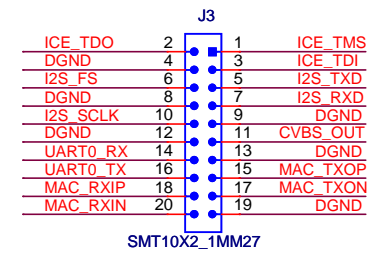
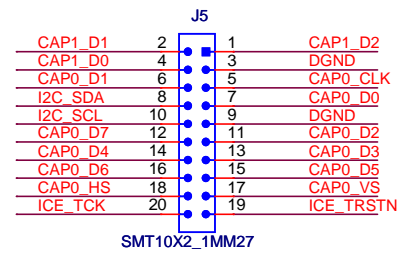
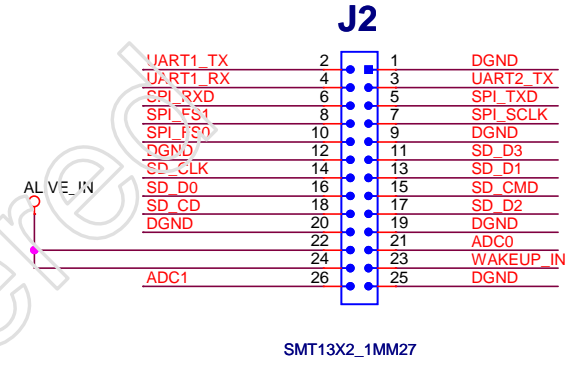
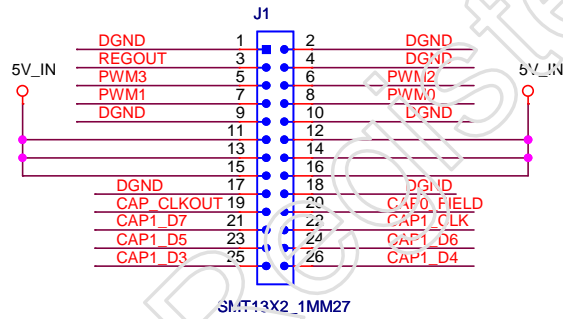
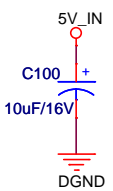
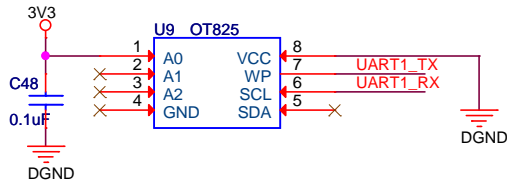
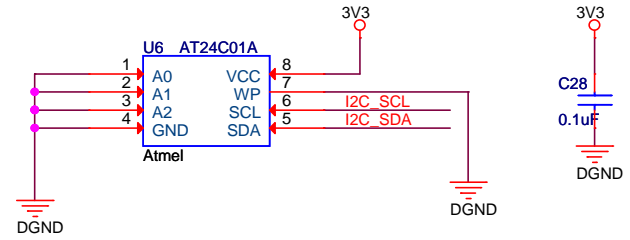
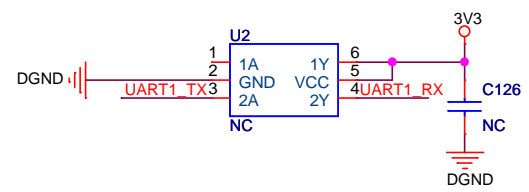
PWM0  
PWM1  
PWM2  
PWM3

MAC\_TXOP  
MAC\_TXON  
MAC\_RXIP  
MAC\_RXIN

ADC0  
ADC1

SD\_D[0..3]  
SD\_CMD  
SD\_CLK  
SD\_CD

UART0\_RX  
UART1\_RX  
UART0\_TX  
UART1\_TX  
UART2\_TX

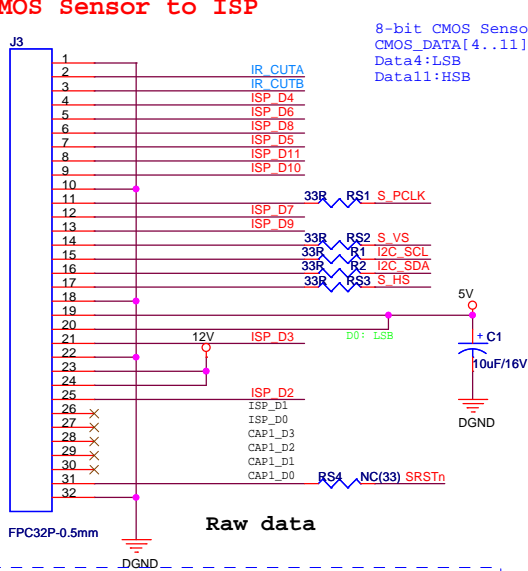


Title		
Interface		
Size	Document Number	Rev
A4	OTIPCAM8126	1VA
Date:	Tuesday, June 05, 2012	Sheet 1 of 1

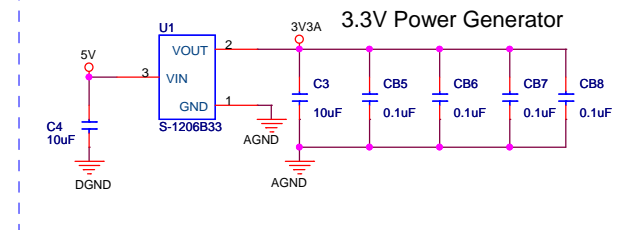
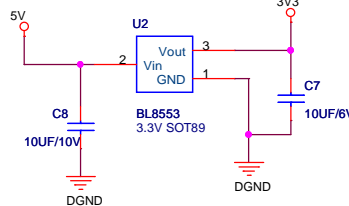
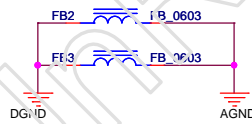
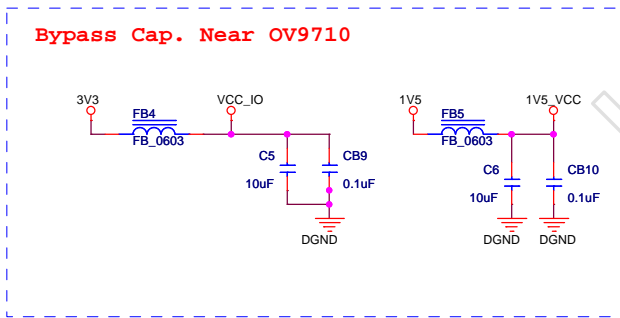
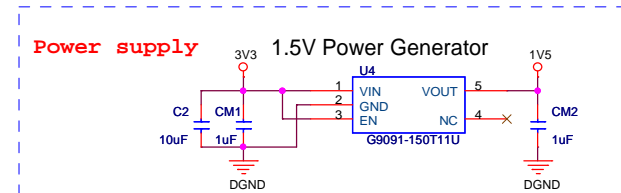
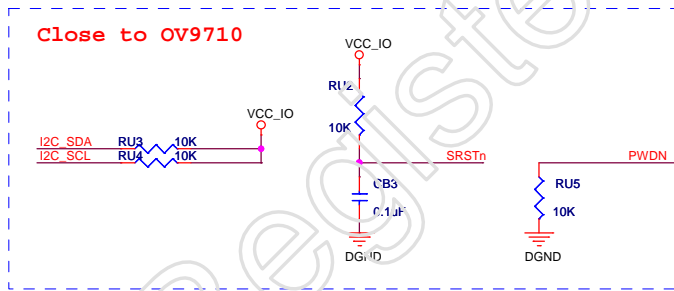
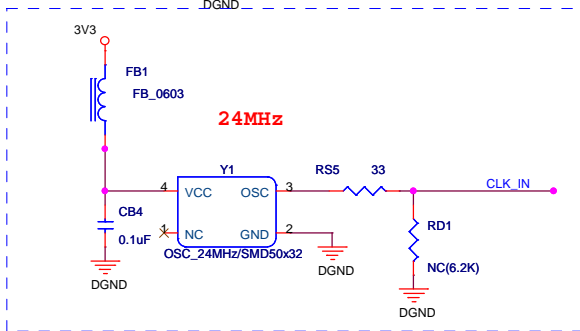
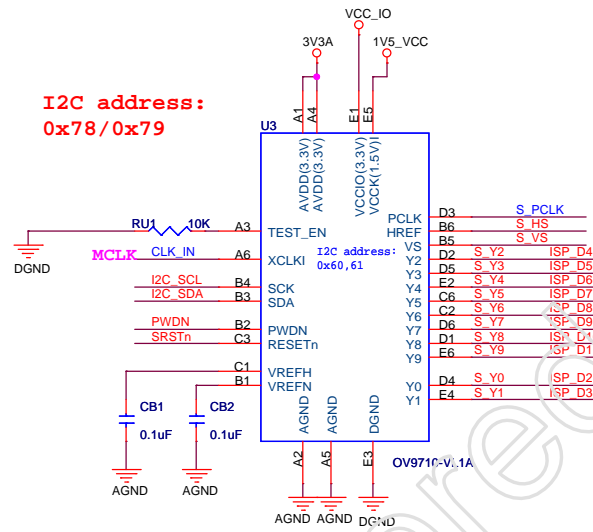


# CMOS Sensor to ISP

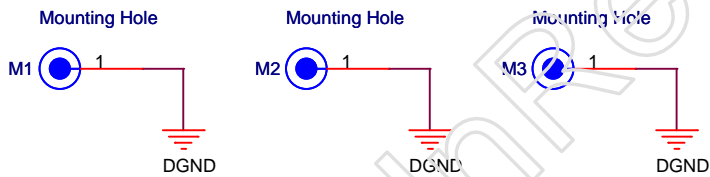
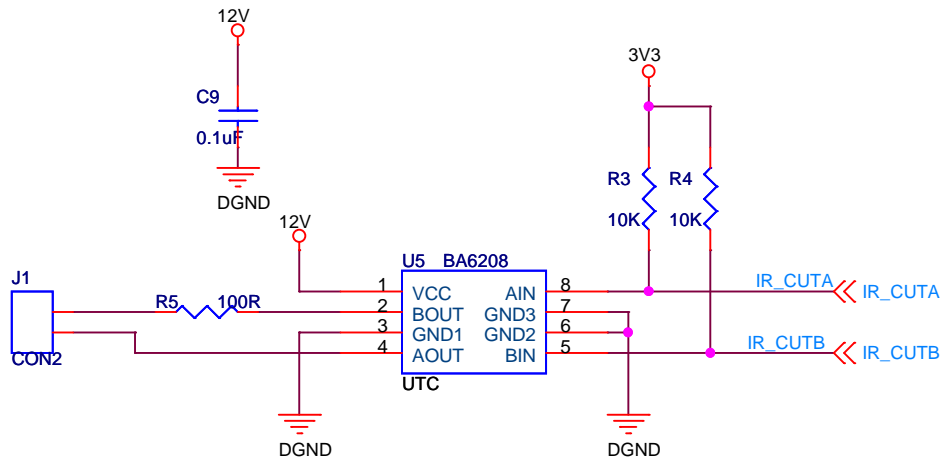
8-bit CMOS Sensor  
 CMOS\_DATA[4..11]  
 Data4:LSB  
 Data1:HSB



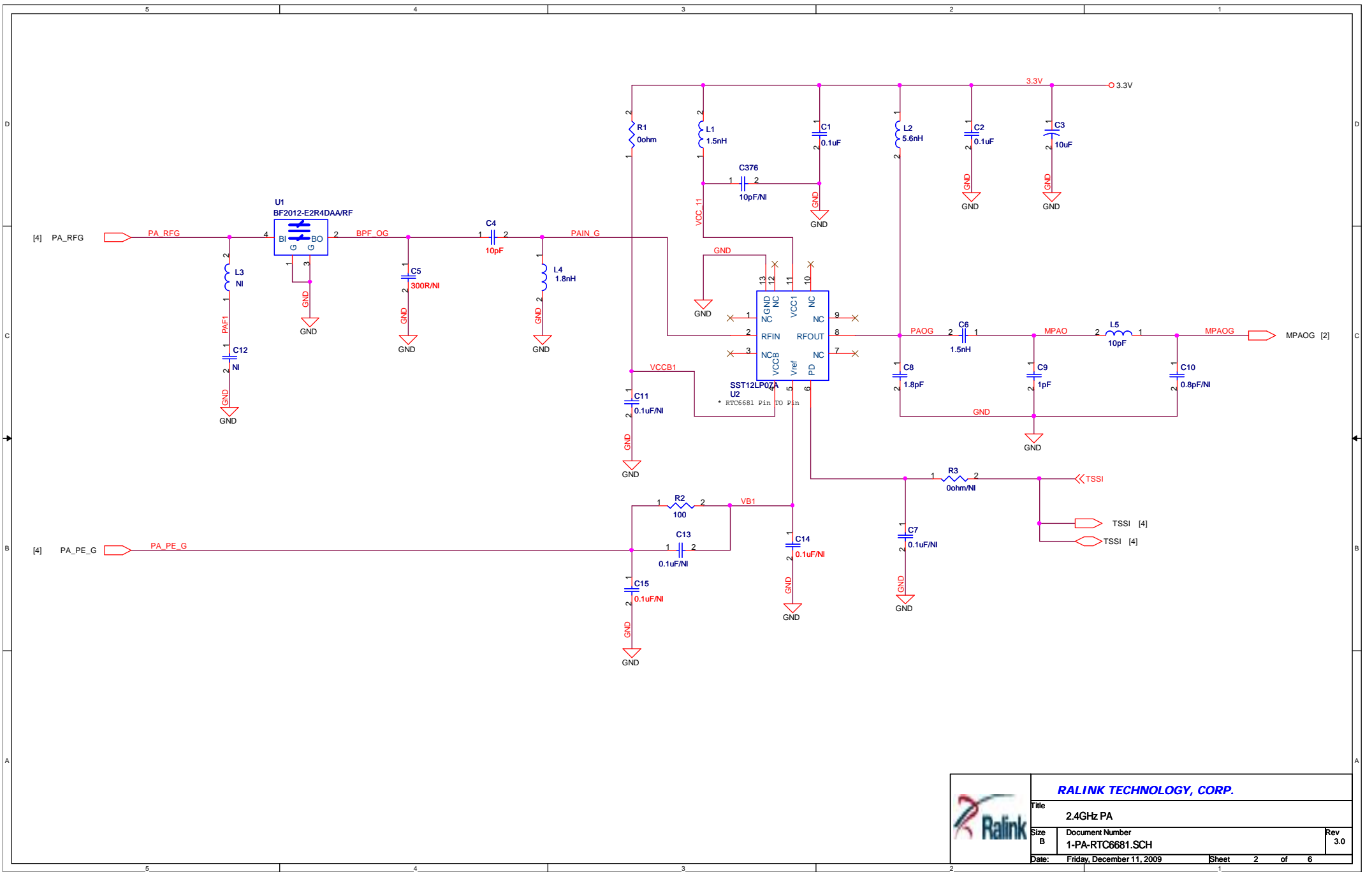
I2C address:  
 0x78/0x79




Title		
OV9712		
Size	Document Number	Rev
	OTIP316BOV97121VA	
Date:	Friday, January 13, 2012	Sheet 2 of 3

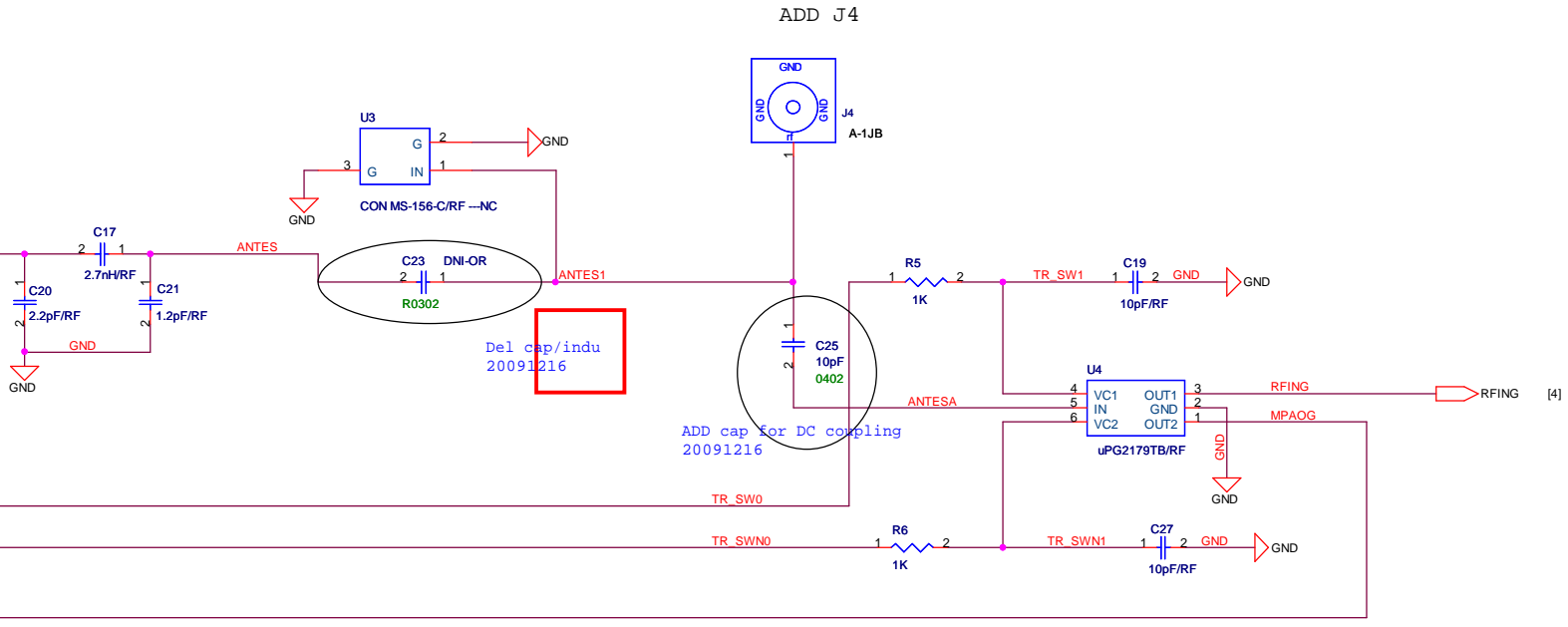
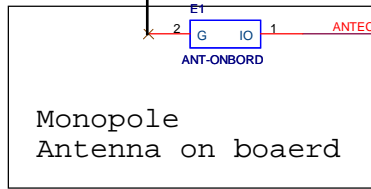


Title		
IR CUT		
Size	Document Number	Rev
	OTIP316BOV97121VA	
Date:	Friday, January 13, 2012	Sheet 3 of 3



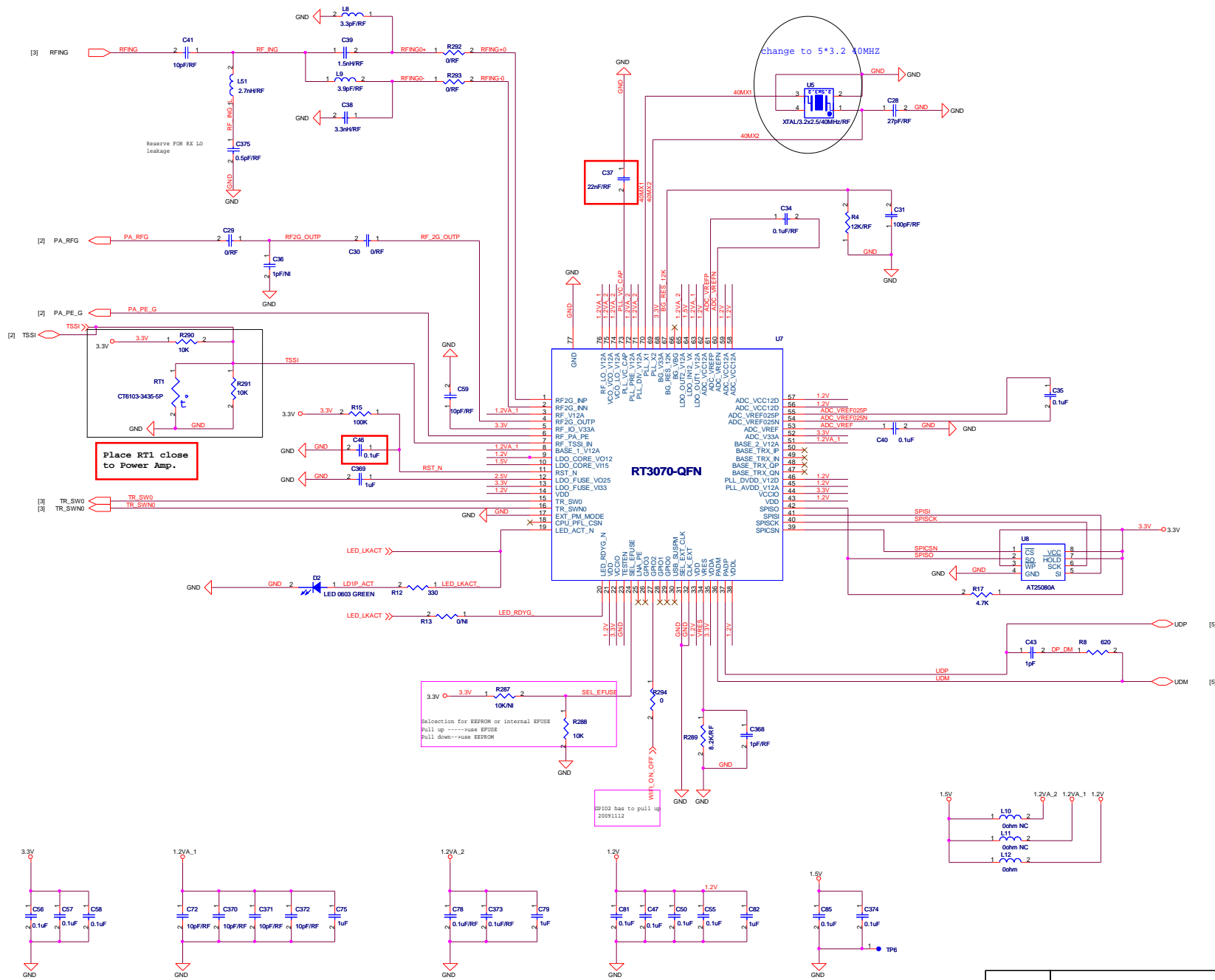
		<b>RALINK TECHNOLOGY, CORP.</b>	
		Title: 2.4GHz PA	
Size: B	Document Number: 1-PA-RTC6681.SCH		Rev: 3.0
Date: Friday, December 11, 2009	Sheet: 2	of 6	

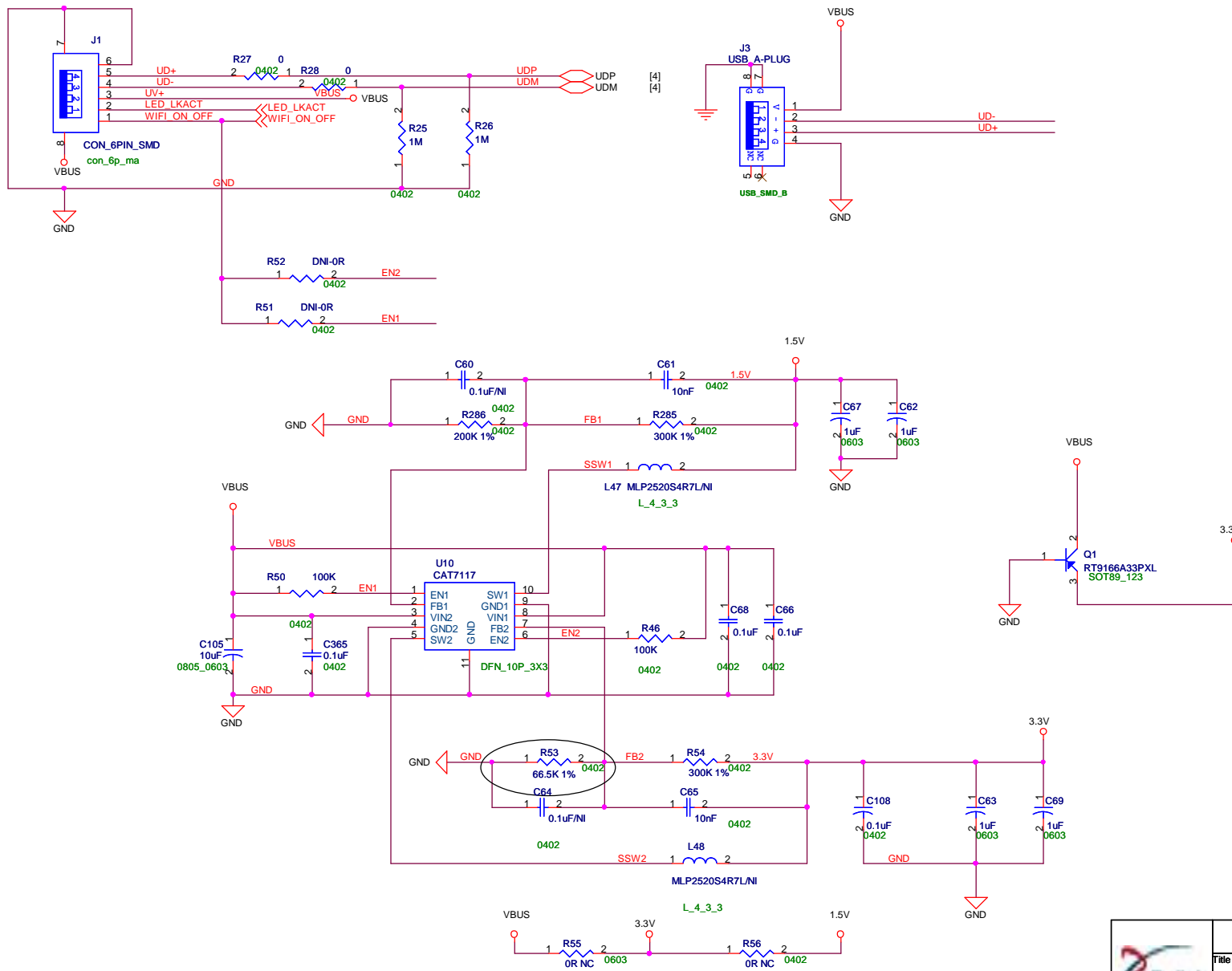
(ANTENNA0)  
(TX0/RX0)




- [4] TR\_SW0
- [4] TR\_SWN0
- [3] MPAOG

	<b>RALINK TECHNOLOGY, CORP.</b>		
	Title: Front-End		
	Size B	Document Number: 2-FrontEnd.SCH	Rev 3.0
	Date: Wednesday, December 16, 2009	Sheet 3 of 6	





如果使用3.3v输入，则R55、R56上。

		<b>RALINK TECHNOLOGY, CORP.</b>	
		Title PEN USB Interface and Power	
Size B	Document Number 4-USB-A.SCH		Rev 3.0
	Date: Thursday, September 22, 2011		
		Sheet	5 of 6