

FCC ID: UV5EIYR09231RR

Technical Description :

The brief circuit description is listed as follows :

- Q1 and associated circuit act as 49.860 MHz Super-regenerative receiver.
- IC1 SM6135B acts as Decoder and Signal Amplifier.
- DZ1 and associated circuit act as Voltage Regulator.
- Q8(D882), Q8(9014), Q9(D882), Q9(9014), Q12(B772), Q13(B772) and associated circuit act as Motor 1 Motor Driver.
- Q2(9014), Q3(9014), Q6(D882), Q7(D882), Q10(B772), Q11(B772) and associated circuit act as Motor 2 Motor Driver.
- Q14(8050), Q15 (8050) and associated circuit act as LED Driver.

Antenna Used :

An integral antenna (non-extendable) has been used.