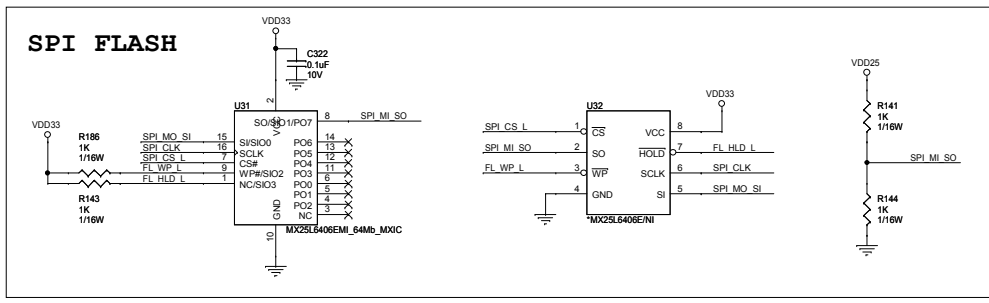
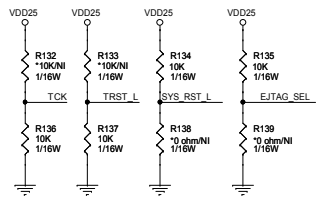
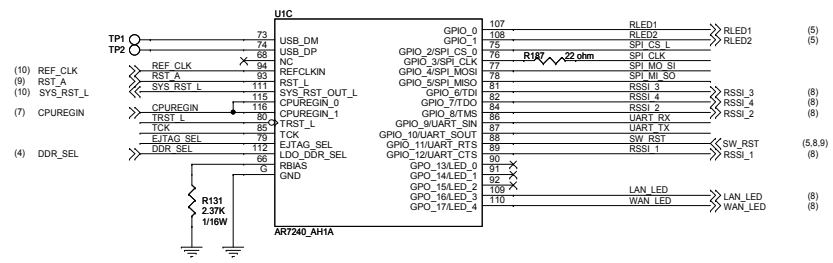
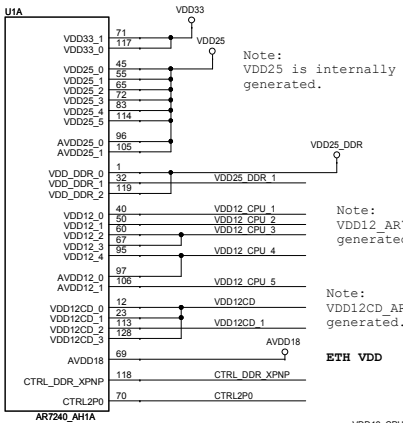


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VDD33



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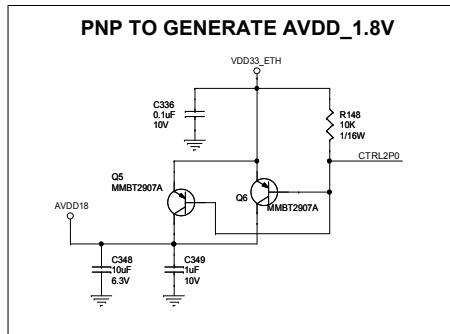
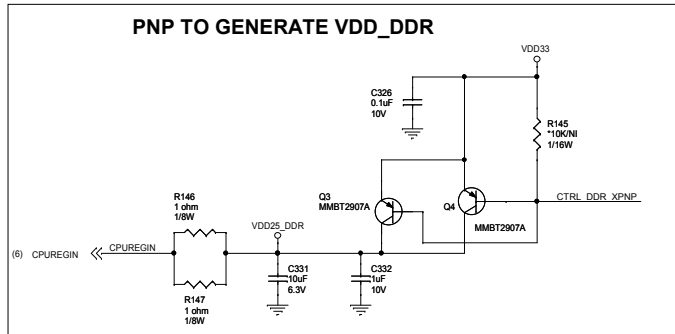
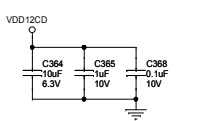
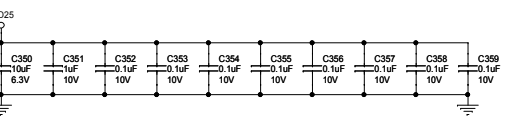
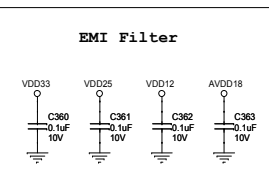
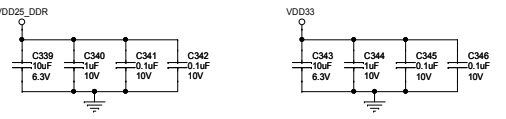
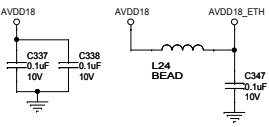
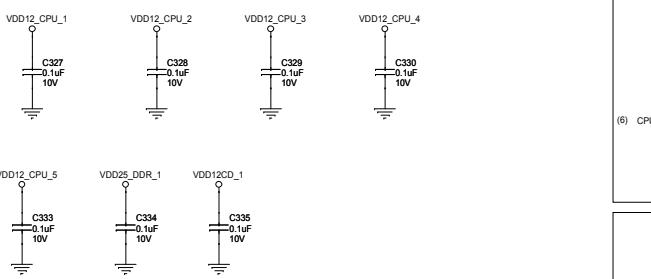


Note:
VDD25 is internally generated.

Note:
VDD12_AR7240 is internally generated.

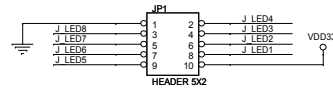
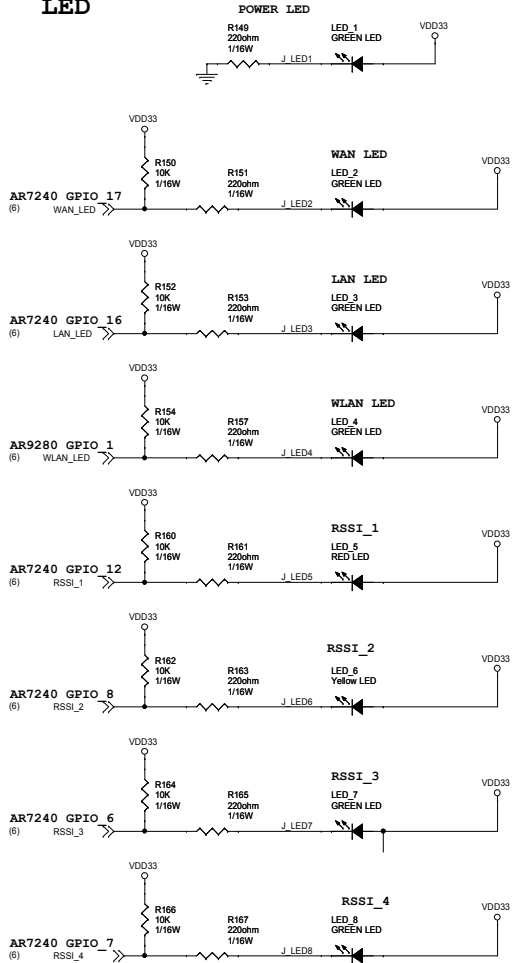
Note:
VDD12CD_AR7240 is internally generated.

ETH VDD

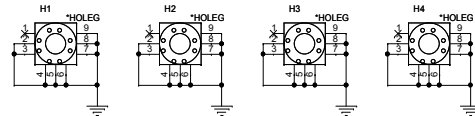
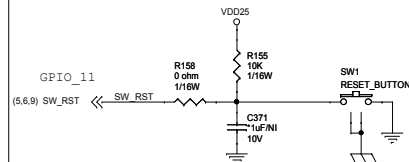


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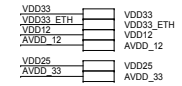
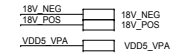
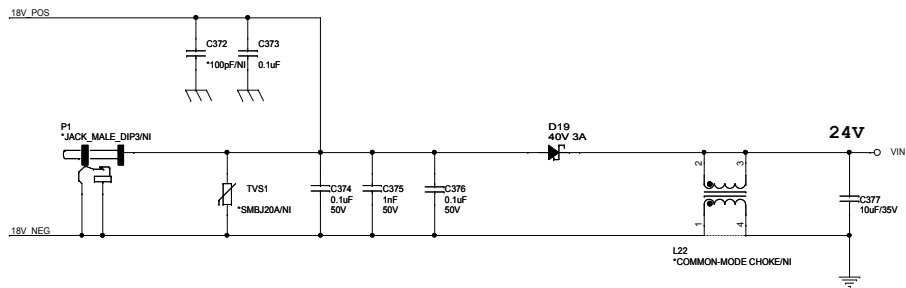
LED



FACTORY DEFAULT/ SOFTWARE RESET



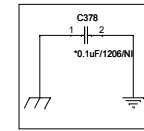
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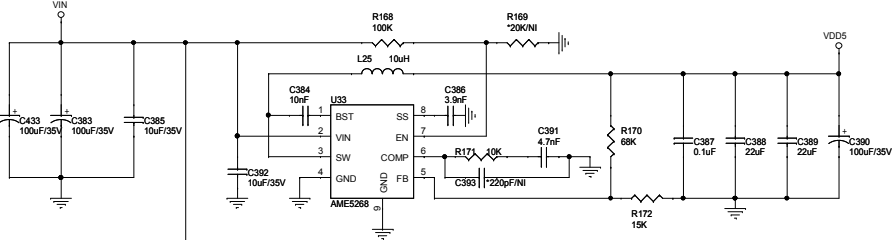
Layout Aspect References

VDD5_VPA = 0.6A (PA)
 3.3V = 0.345A
 2.5V = 0.23A
 1.2V = 0.41A

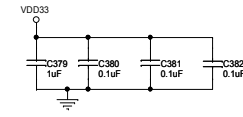
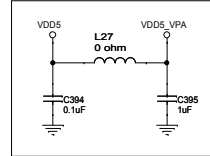
GND Isolated



8~24V -> 5V, I_{max}=3A V_{out} = 0.925*(68K+15K/15K) = 5.118V

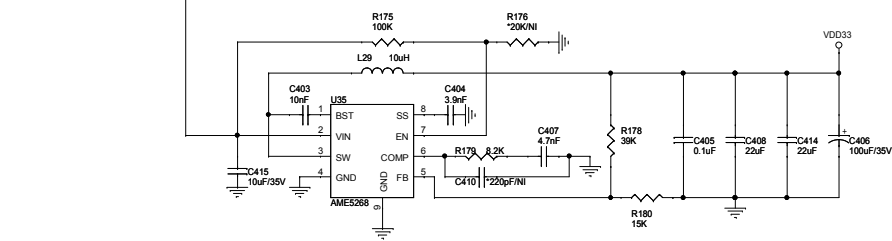


Note:
 5V_VPA is used for PA VDD.

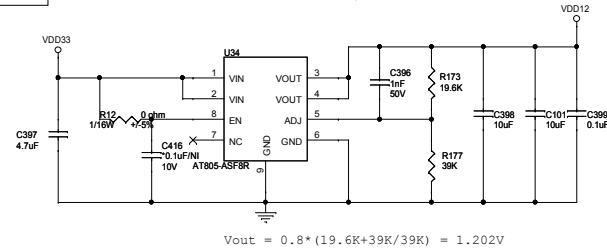
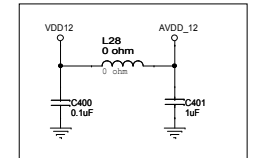


3.3V -> 1.2V, I_{max}=600mA

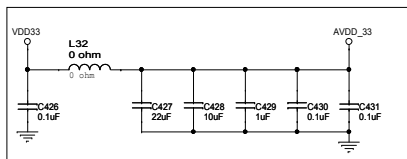
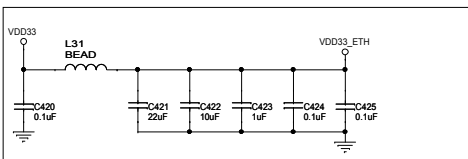
8~24V -> 3.3V, I_{max}=3A V_{out} = 0.925*(39K+15K/15K) = 3.33V



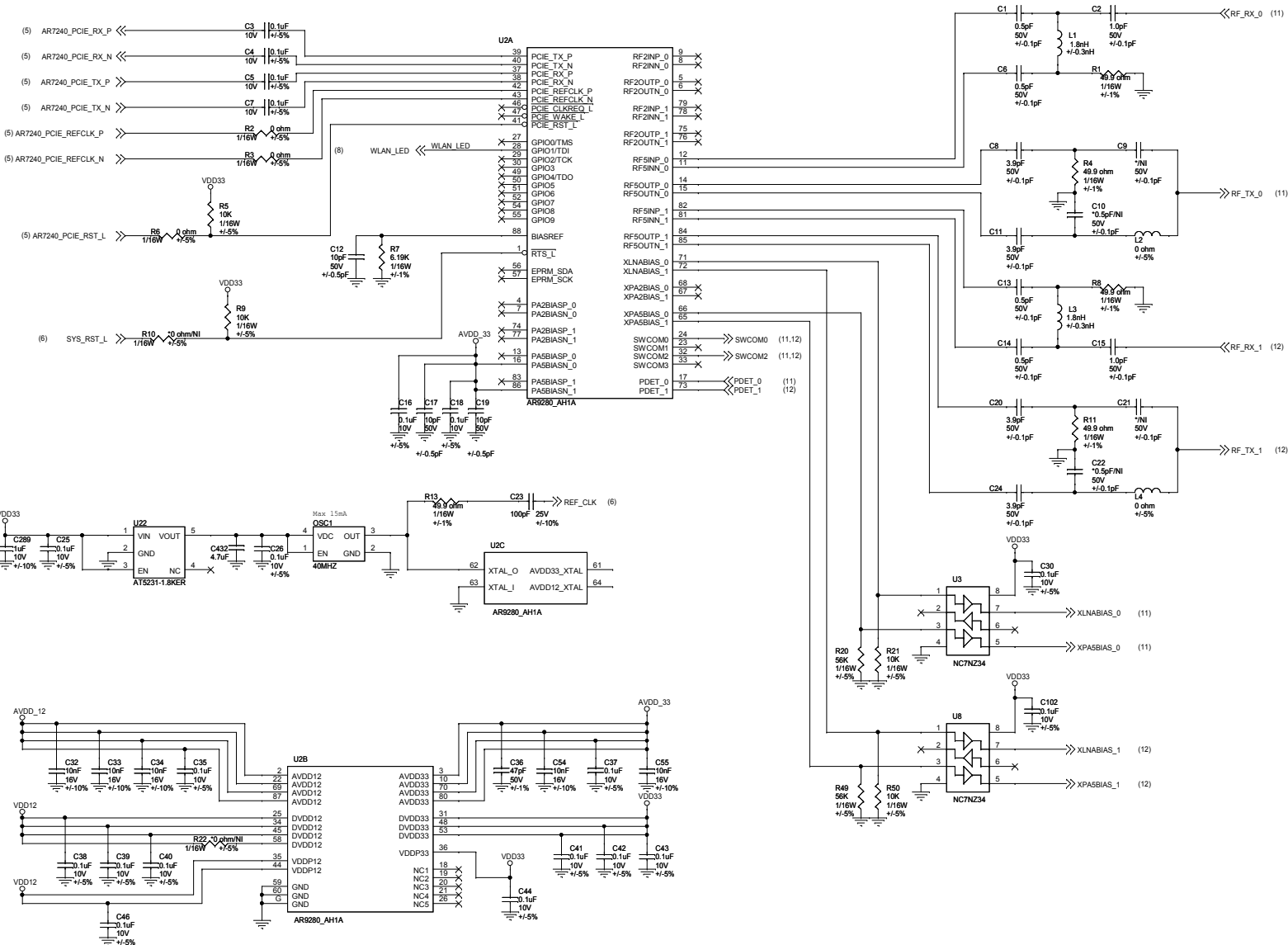
Note:
 VDD12_RF is used for AR9283.



V_{out} = 0.8*(19.6K+39K/39K) = 1.202V



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