Operation Description
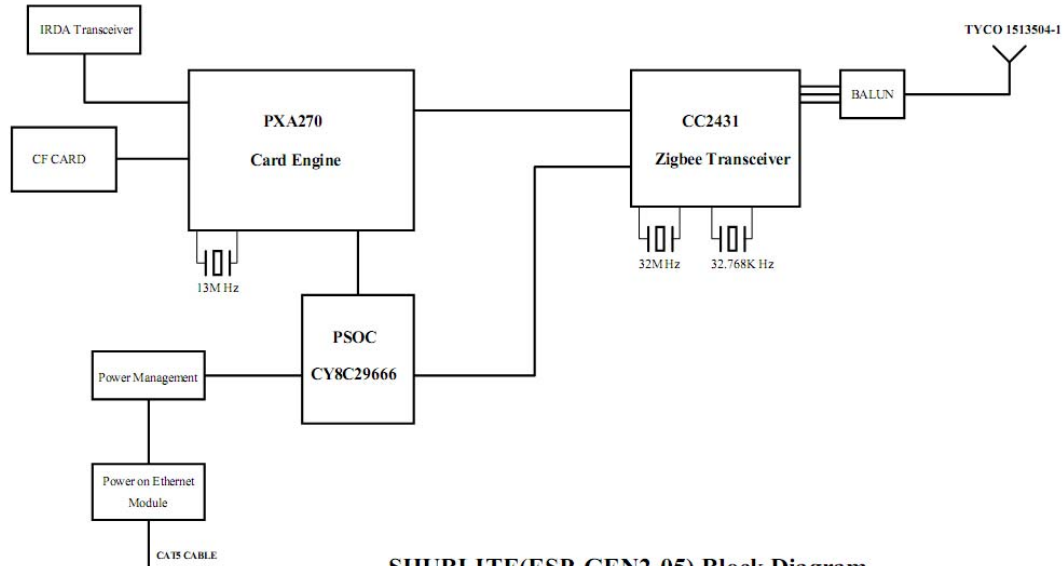

SHUBLITE(ESP-GEN2-05) Block Diagram

1. CPU：According the oscillation time base, receive the RF modulator single and command and sent the command and single to RF modulator. Make sure the data and single is right to receive and sent!
2. Engine: The card engine has a resistor between PWR_SENSE1 and PWR_SENSE2 that can serve to adjust the voltage in the feedback of the voltage regulator for the core voltage.
3. Oscillation: supply the base time to CPU(13Mhz) and Zigbee Chip(32Mhz), 32.768khz is for Zigbee chip internal power management.
4．Power: supply the power to whole system of shublite.
8. RF: sent the data to receiver or receive the data from receiver.