

Model HL-1.2KFX

Explanation on Band Inhibit Circuit

(To prohibit 26.0 ~ 28.0 MHz)

Drive signal from the transceiver is, at first, received by the (RF) DET UNIT PC1398 and then transferred to PCS1733, FREQ. DETECTOR CIRCUIT.

The signal is divided by sixteen (16) through the prescaler IC1, TD7101F. The resulting signal is sent to the IC2, PIC micro processor, where the signal frequency is counted

If the drive signal is judged to fall in the band width of 26.0 to 28.0 MHz, IC2 issues the command to open the relay RL1, of which contacts are in series with the STAND-BY (STBY/OPER.) control circuit, as is shown around T/R pin of PC1698, CONT'L UNIT and STBY, RCA-Jack located at the rear panel of the amp.

Once the STAND-BY control circuit becomes open, the amp keeps stand-by mode and amplifier gain is nil, even if it is driven by such signals between 26.0 and 28.0 MHz.