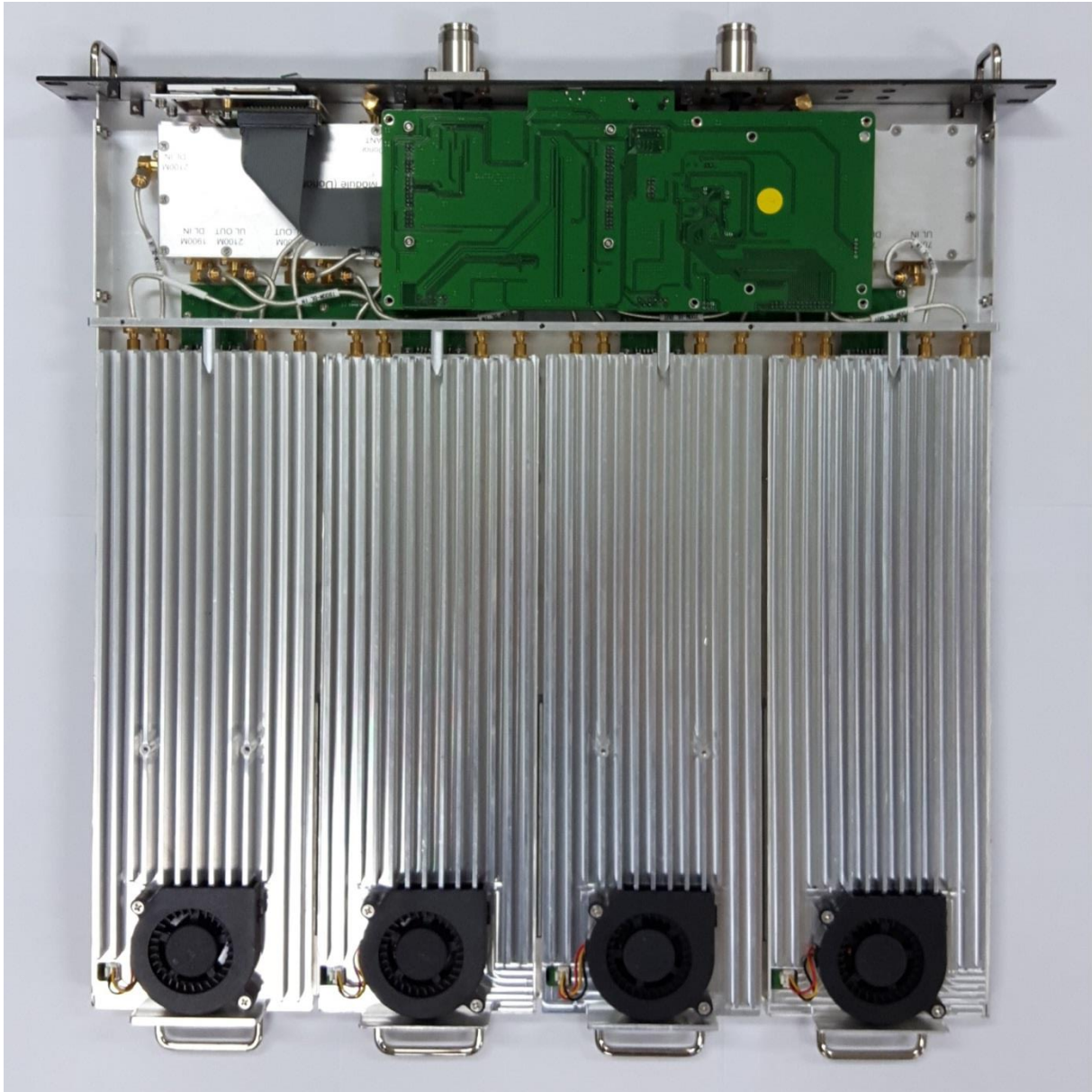
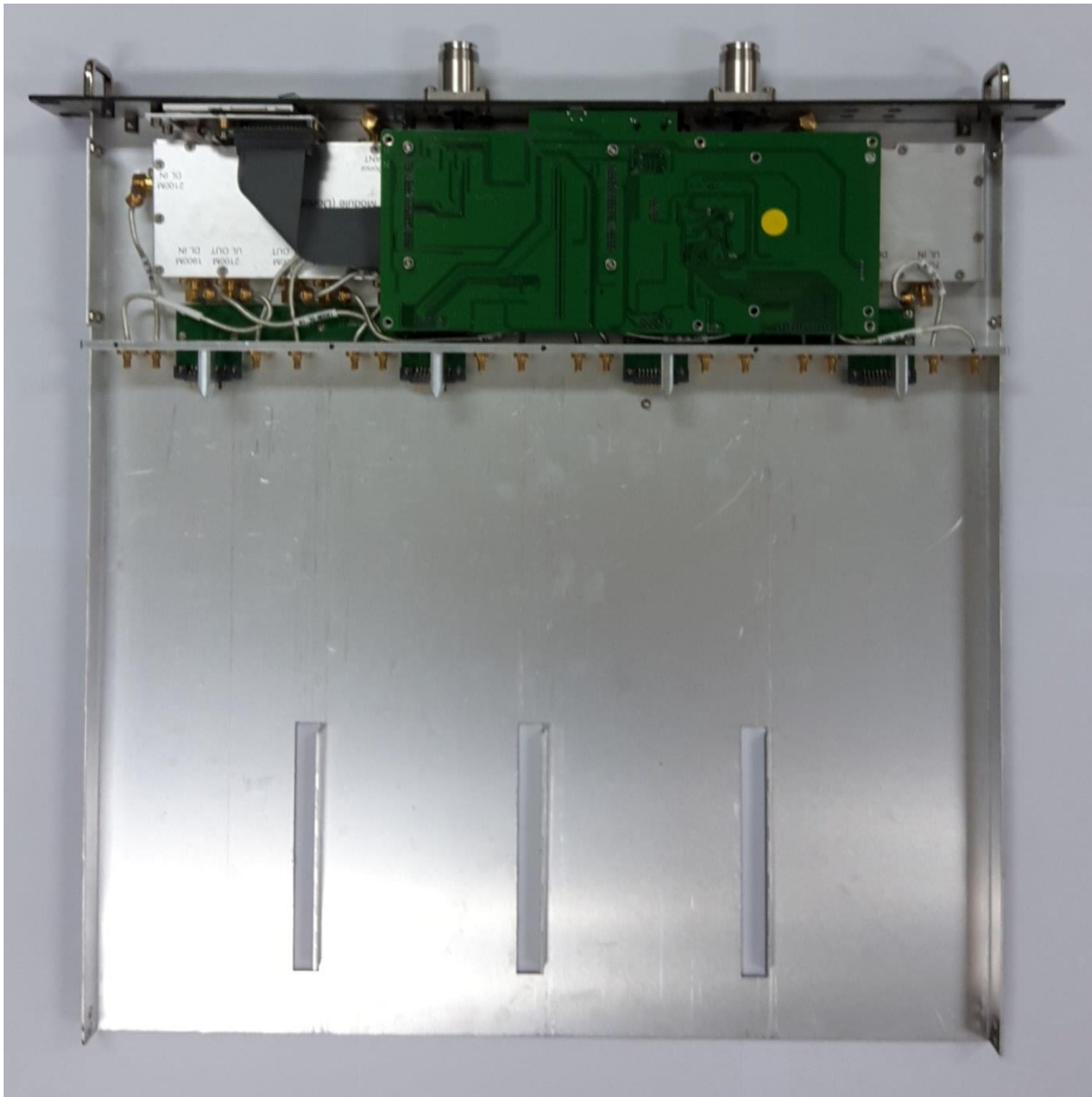


## **INTERNAL PHOTOGRAPHS**

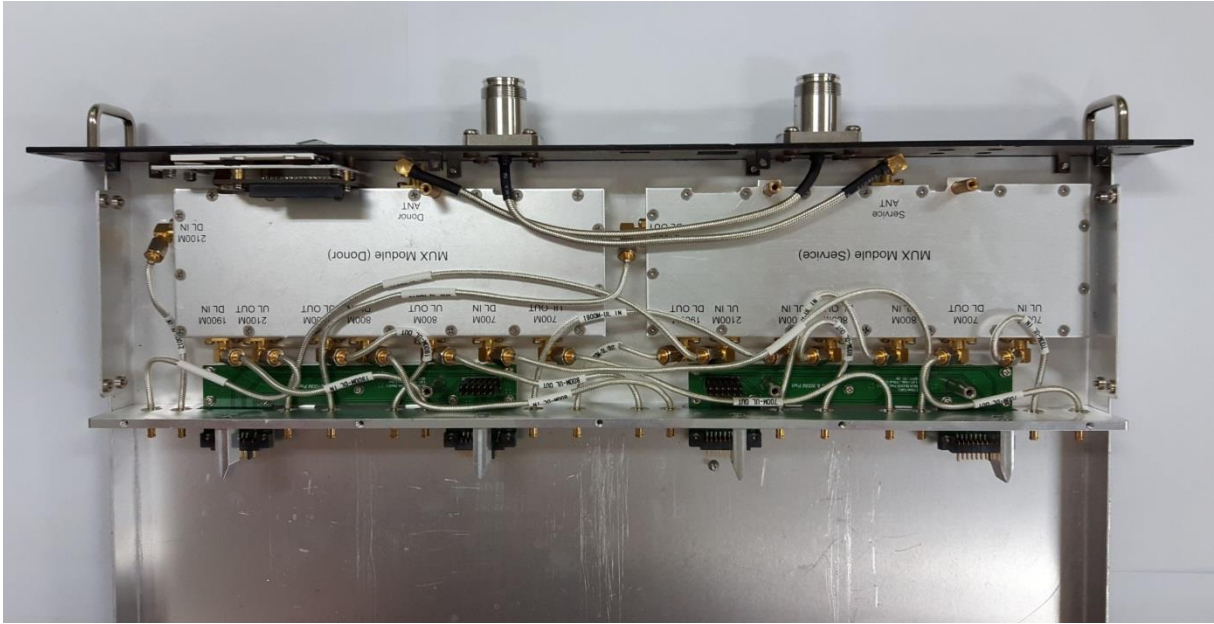
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**[IN CASE]**



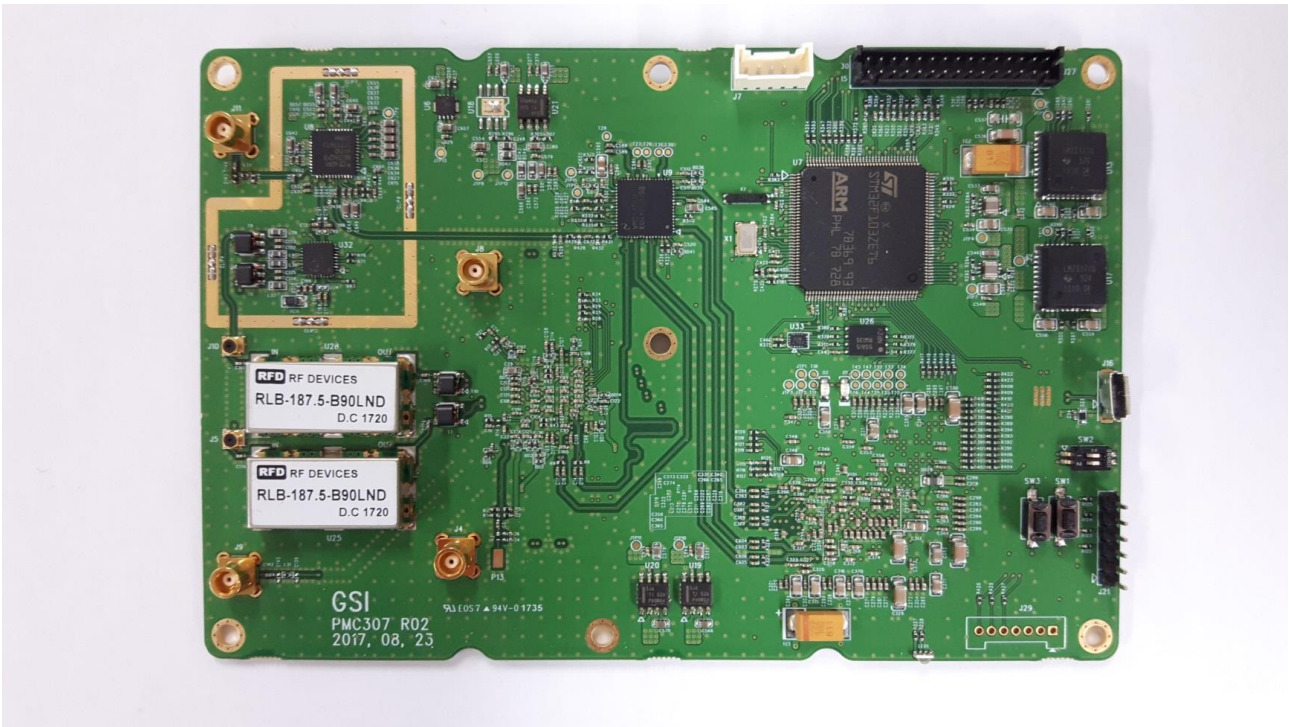
**[IN CASE]**



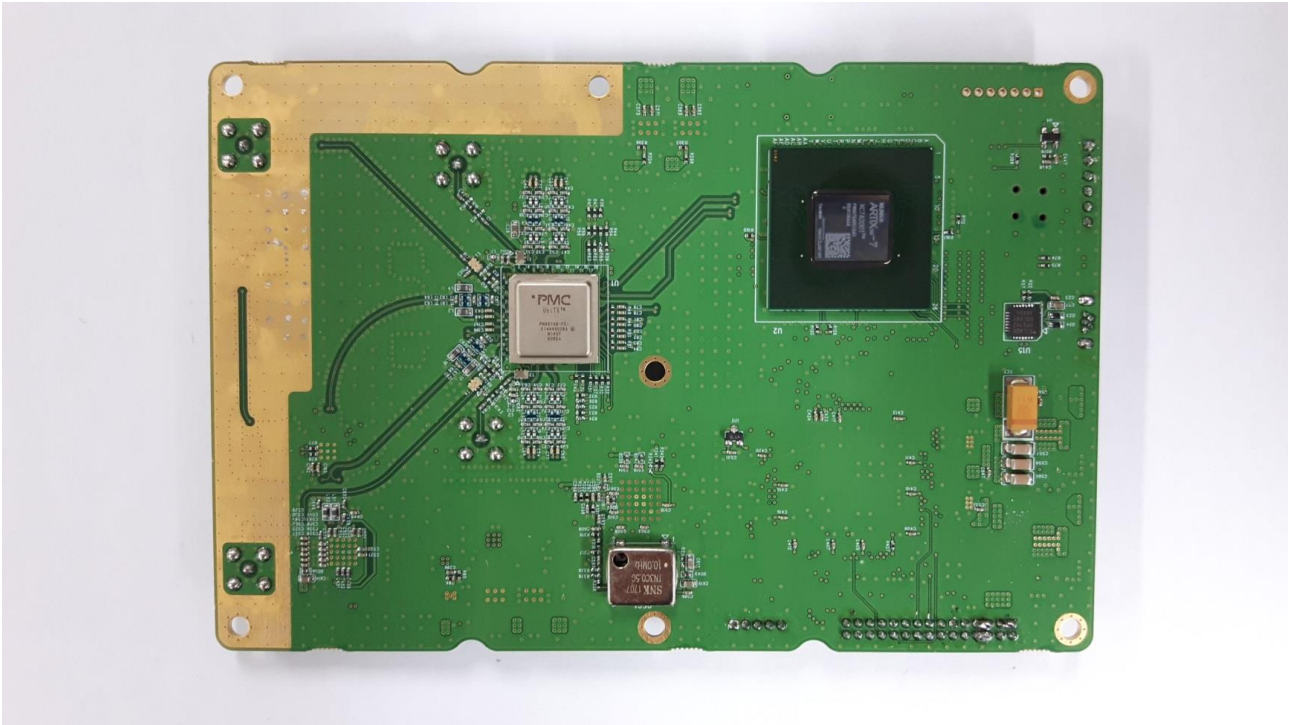
**[IN CASE]**



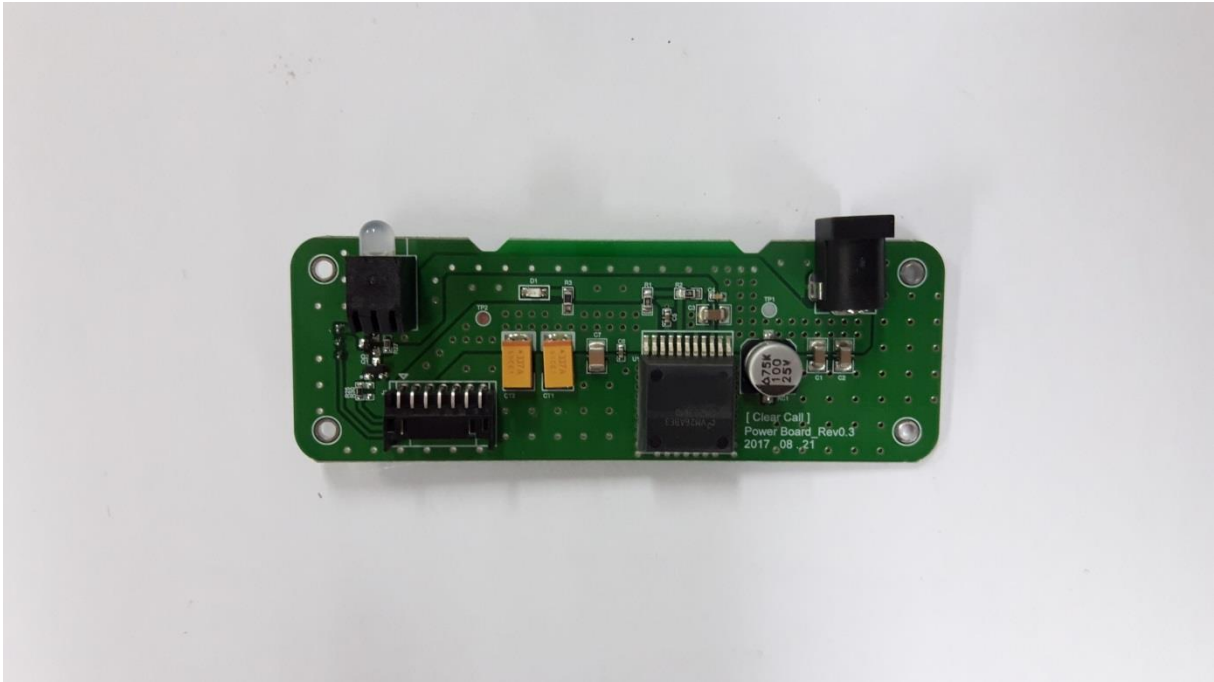
**[Module - IN CASE]**



**[DFM - Top]**

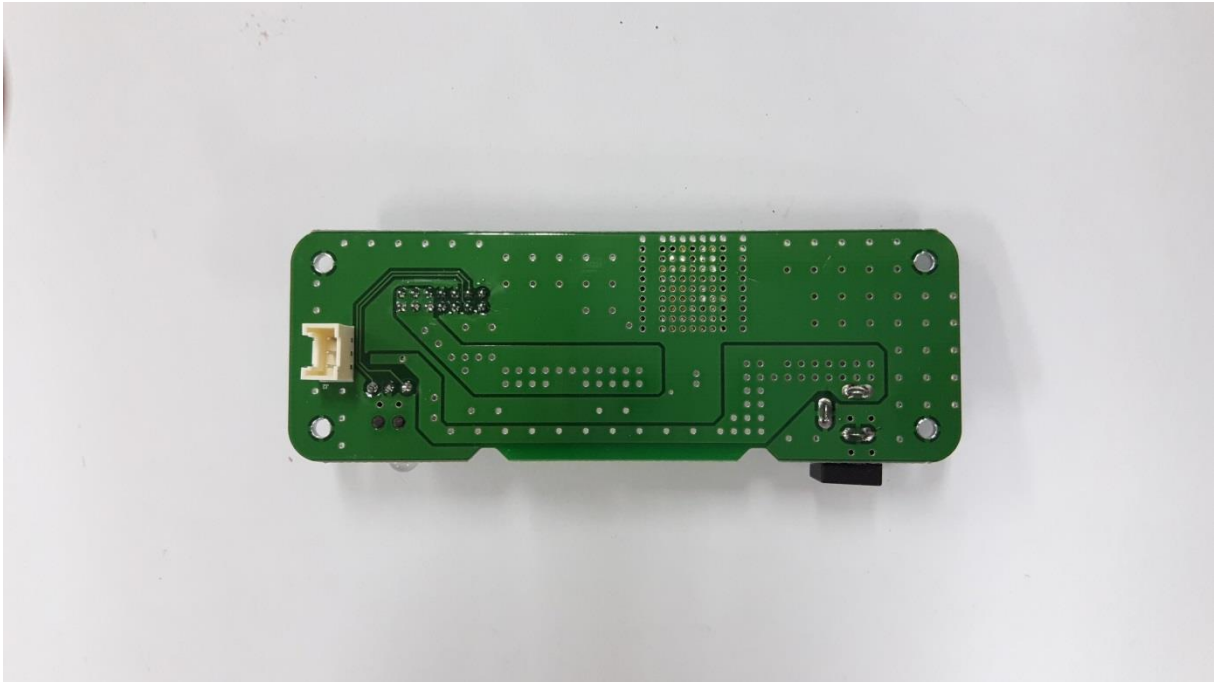


**[DFM - Bottom]**

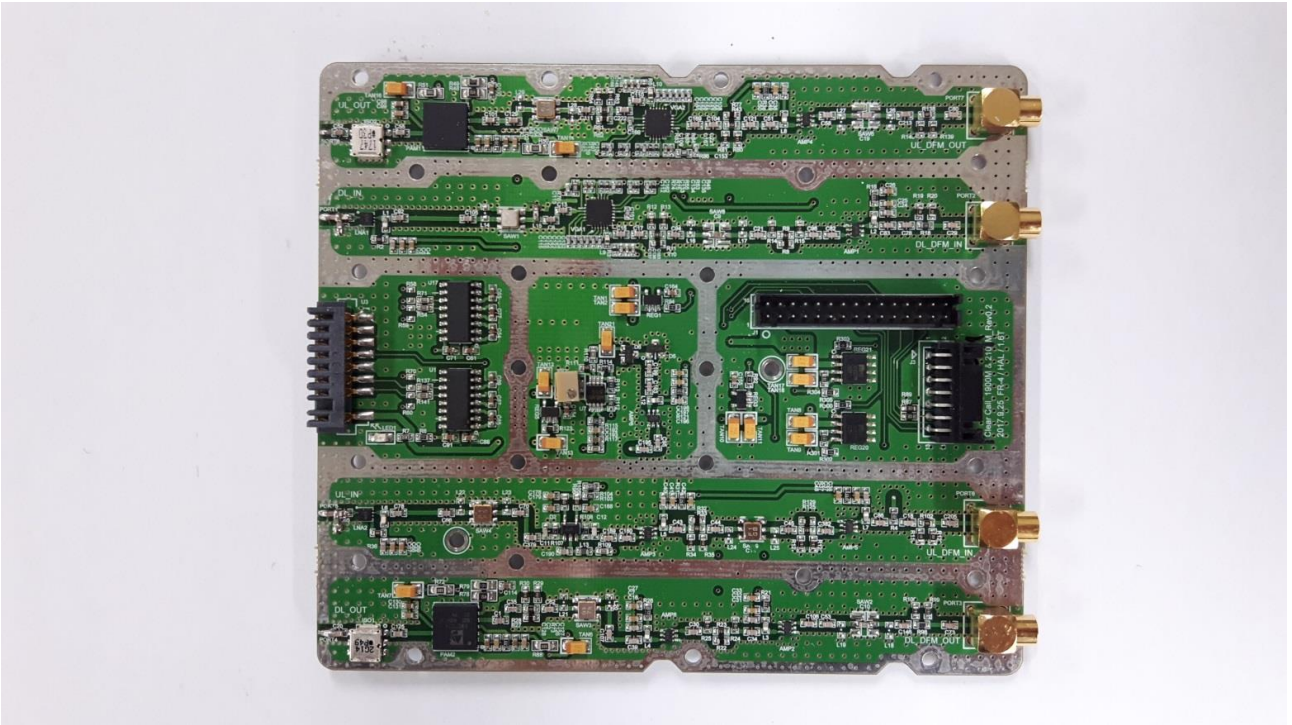


**[Power - Top]**

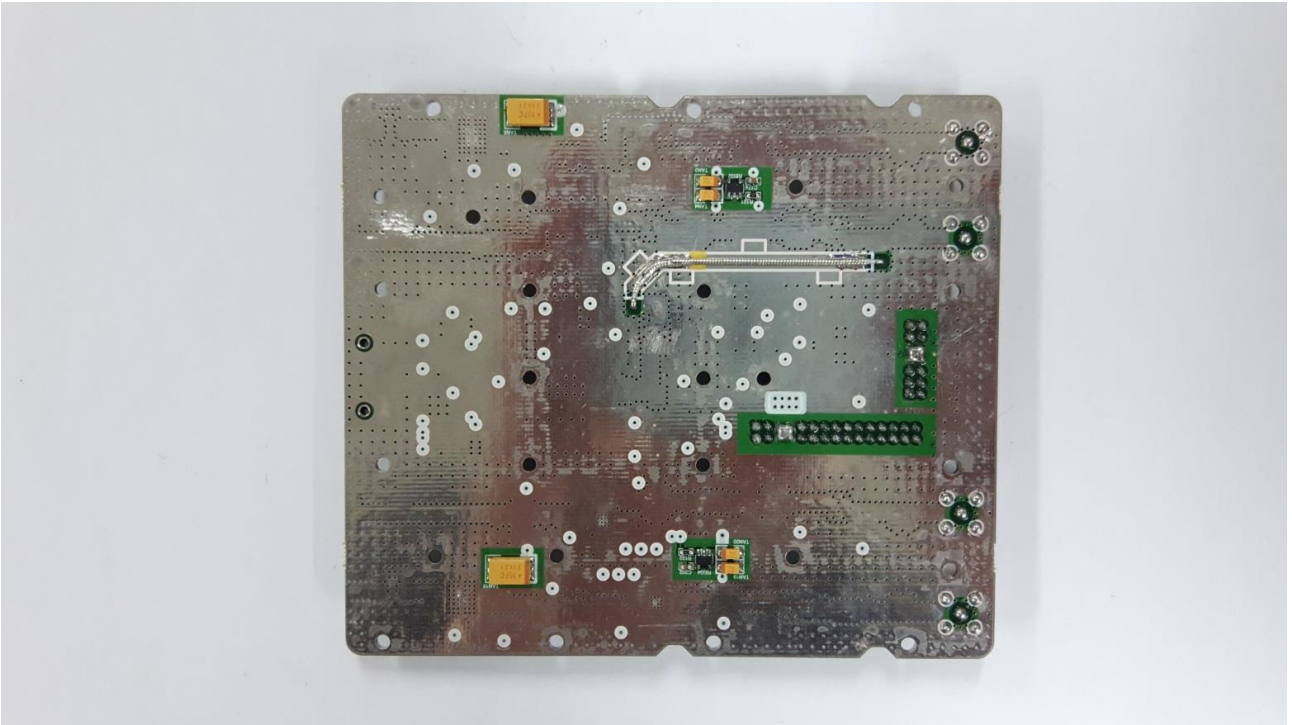




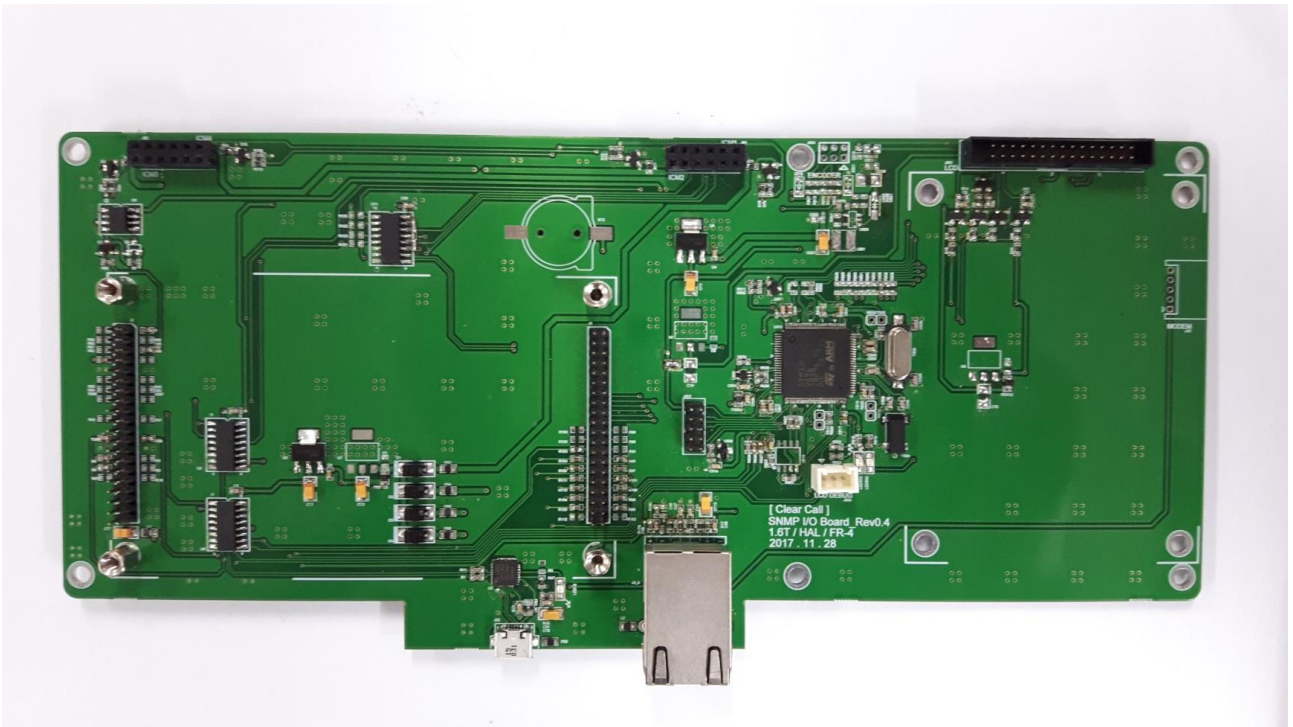
**[Power - Bottom]**



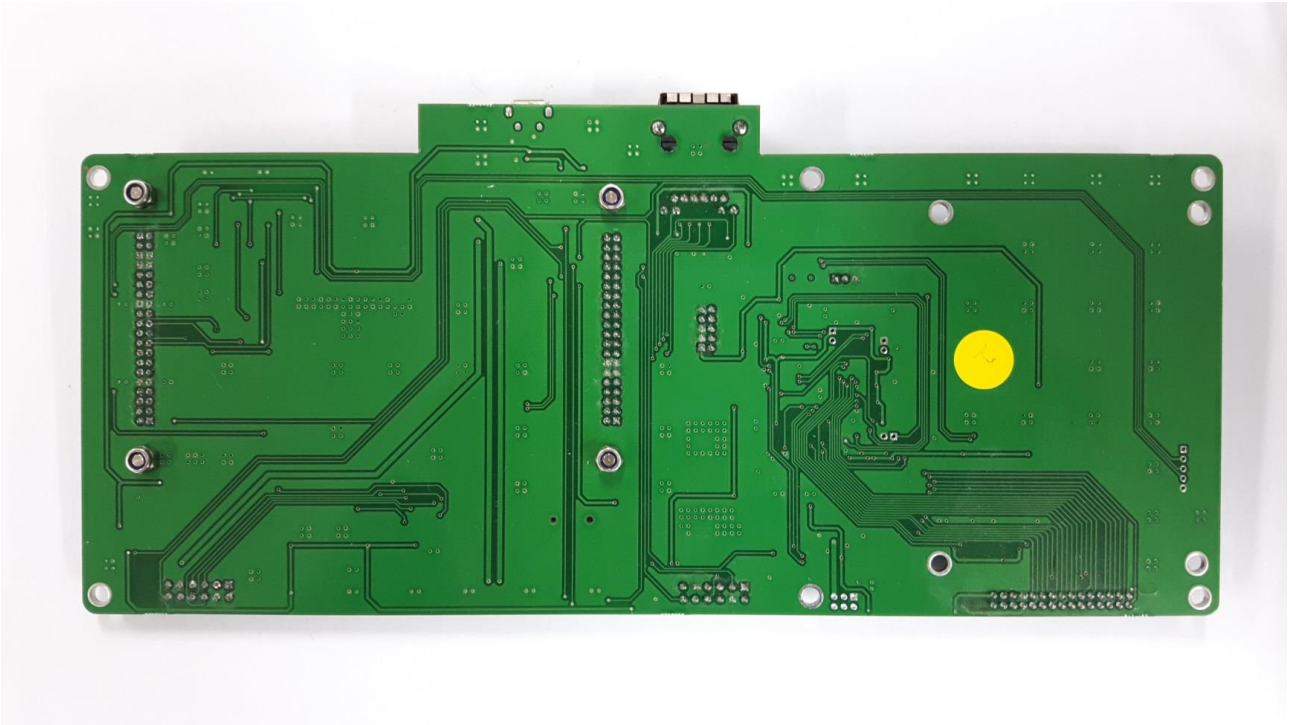
**[2100 MHz - Top]**



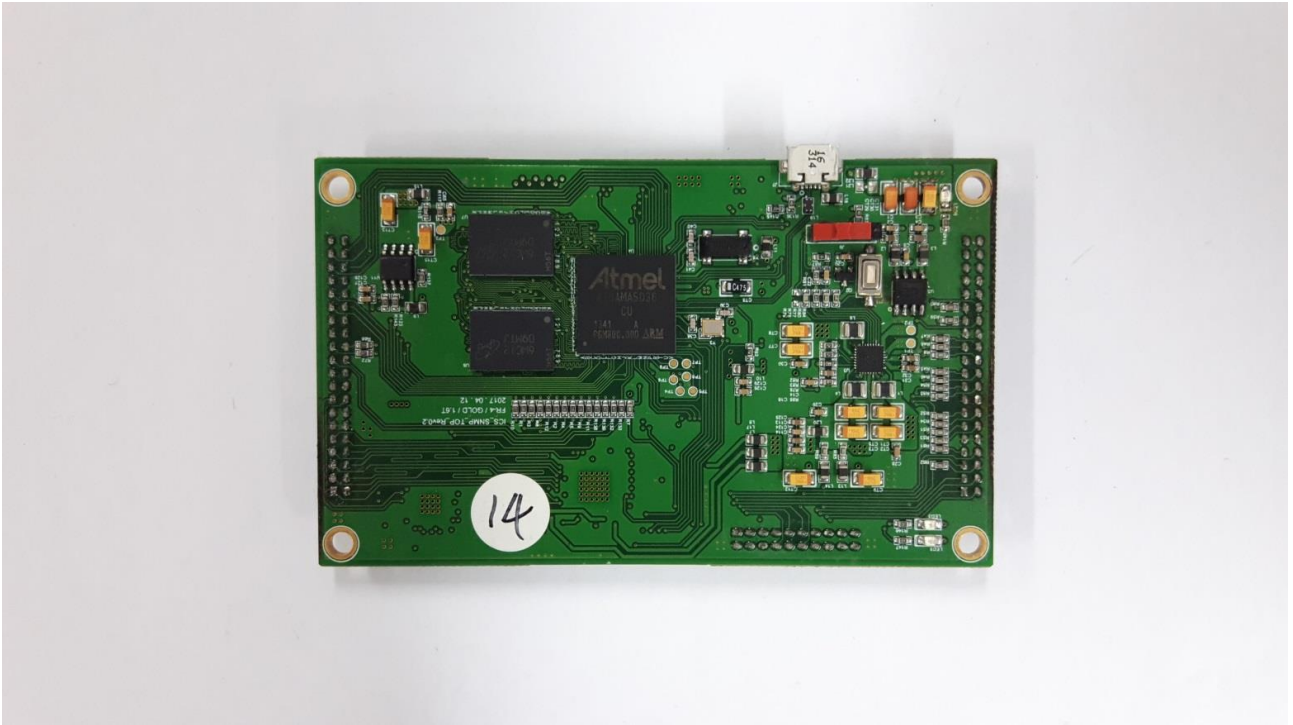
**[2100 MHz - Bottom]**



**[SNMP IO - Top]**



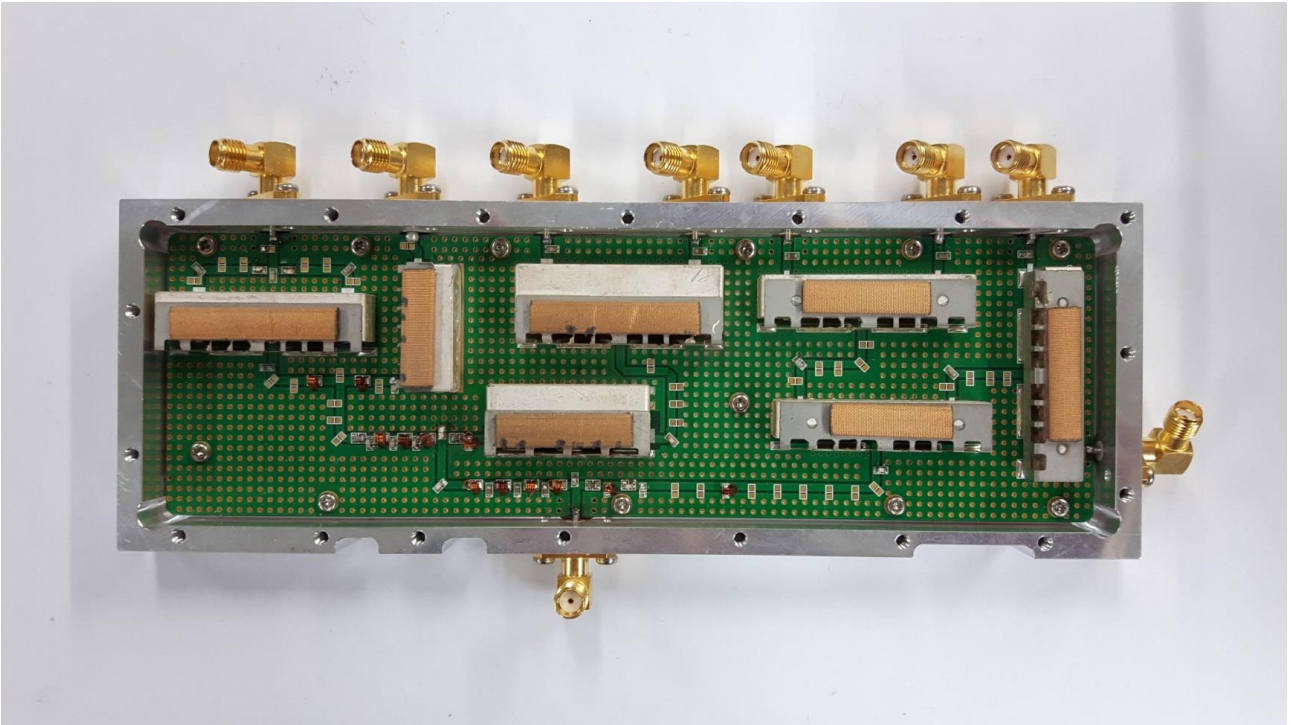
**[SNMP IO - Bottom]**



**[SNMP - Top]**

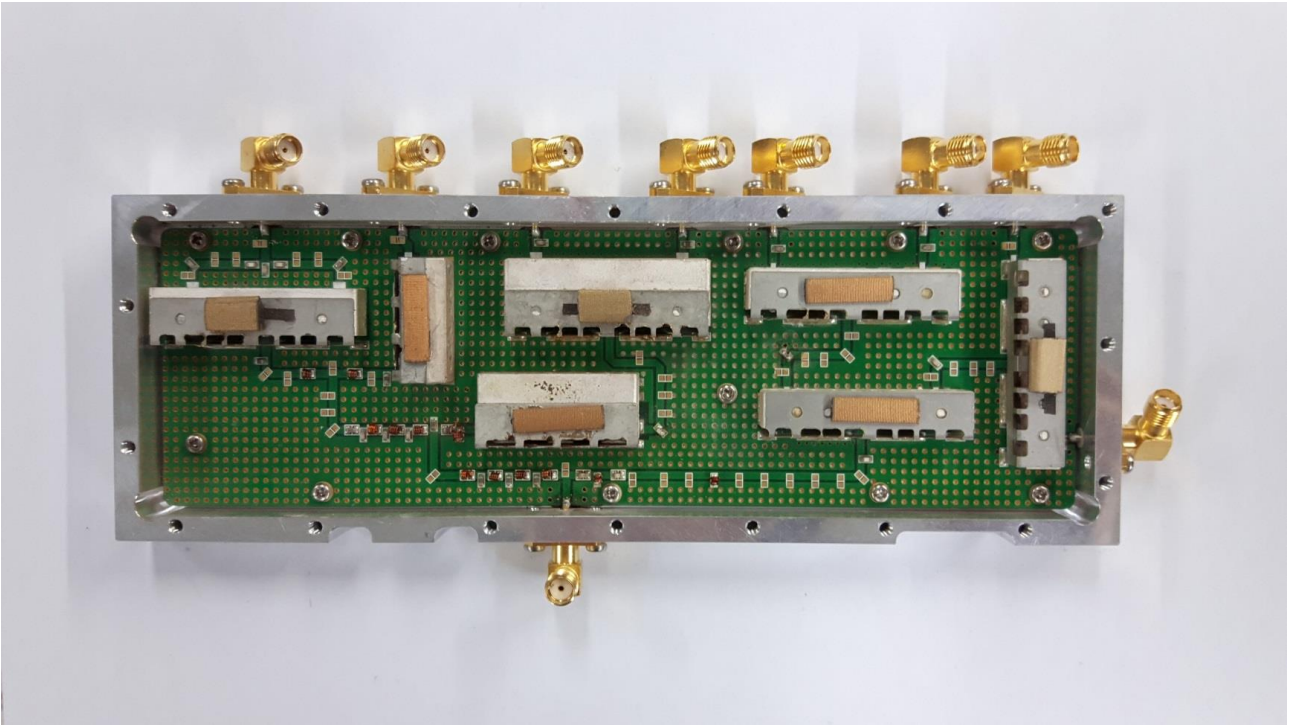


**[SNMP - Bottom]**

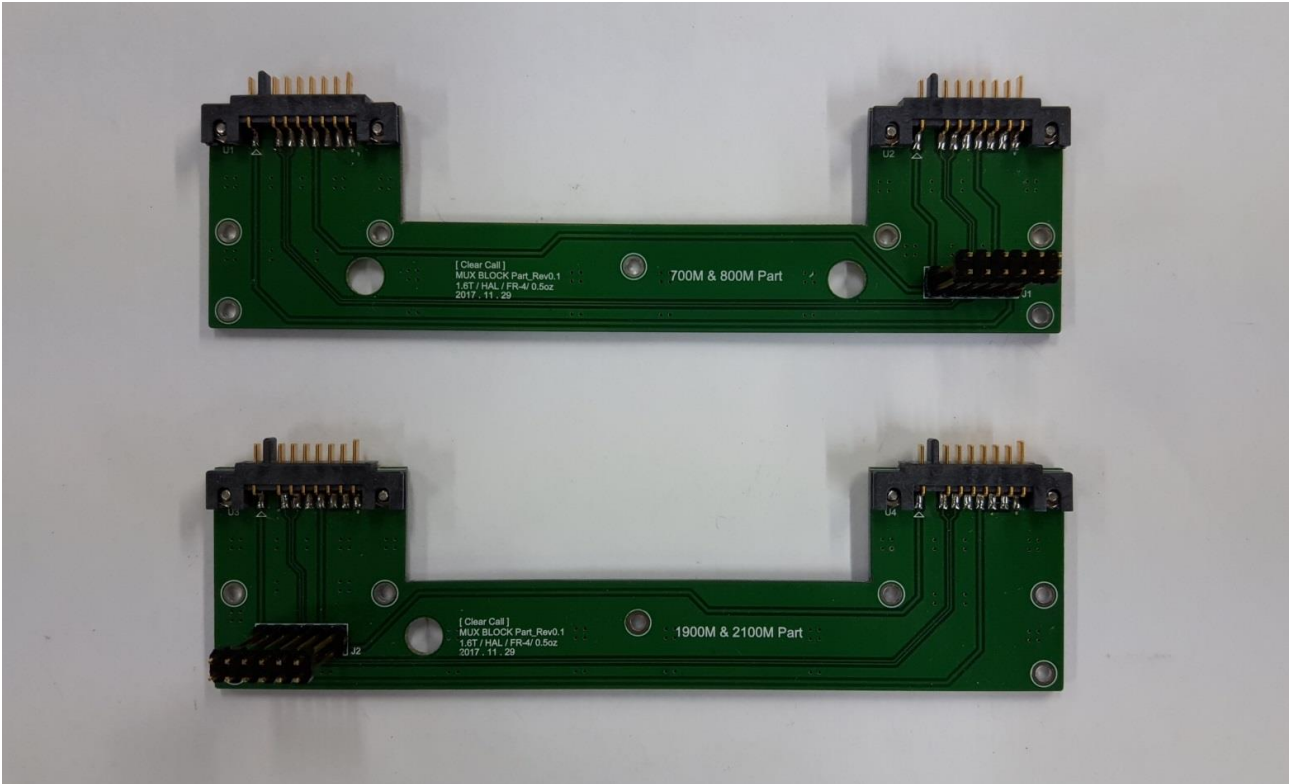


**[MUX Donor]**

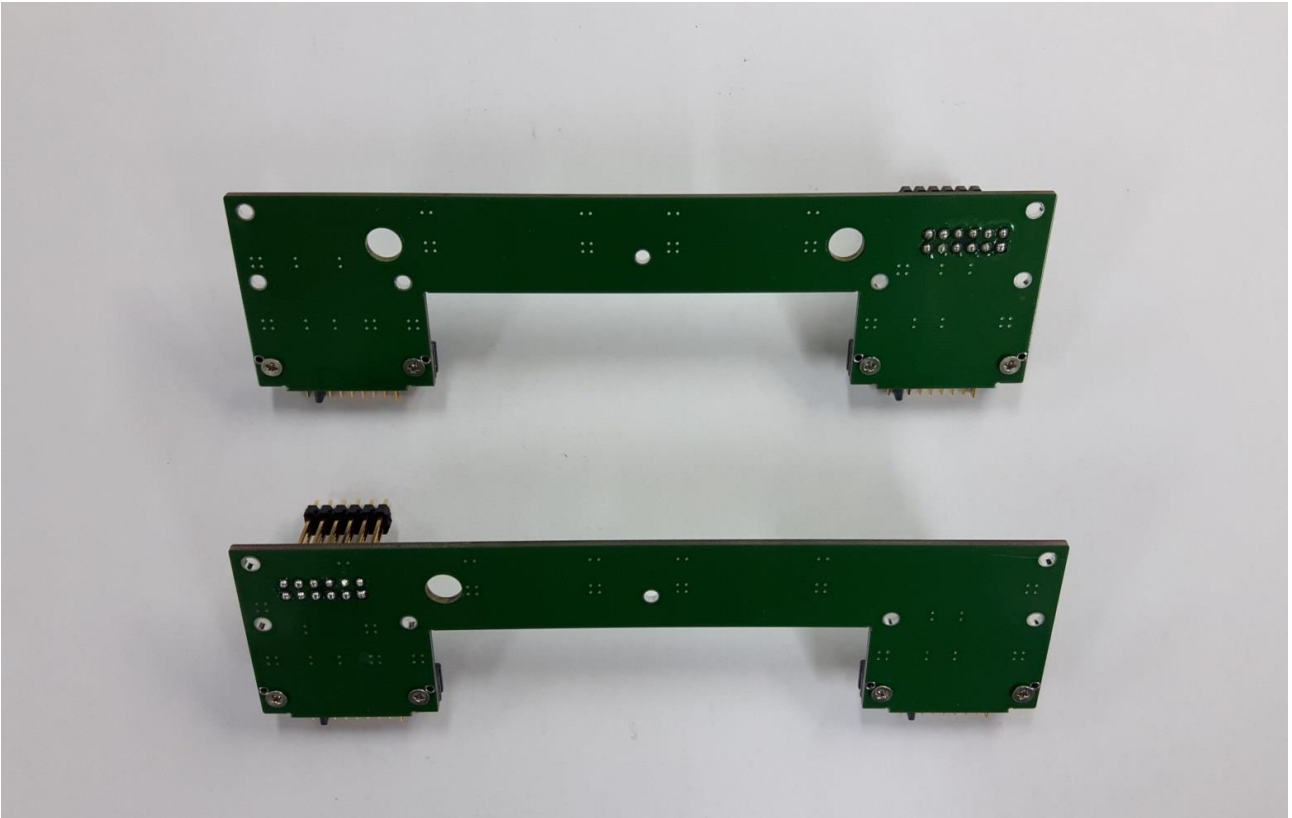




**[MUX Service]**



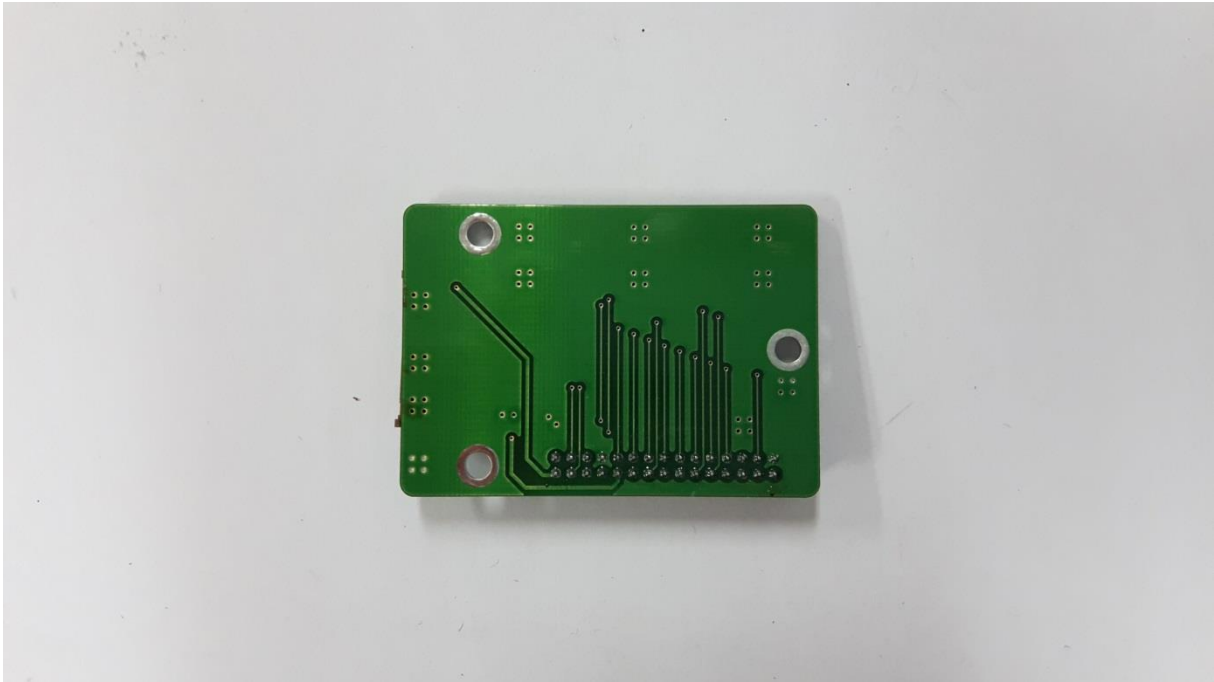
**[MUX Block - Top]**



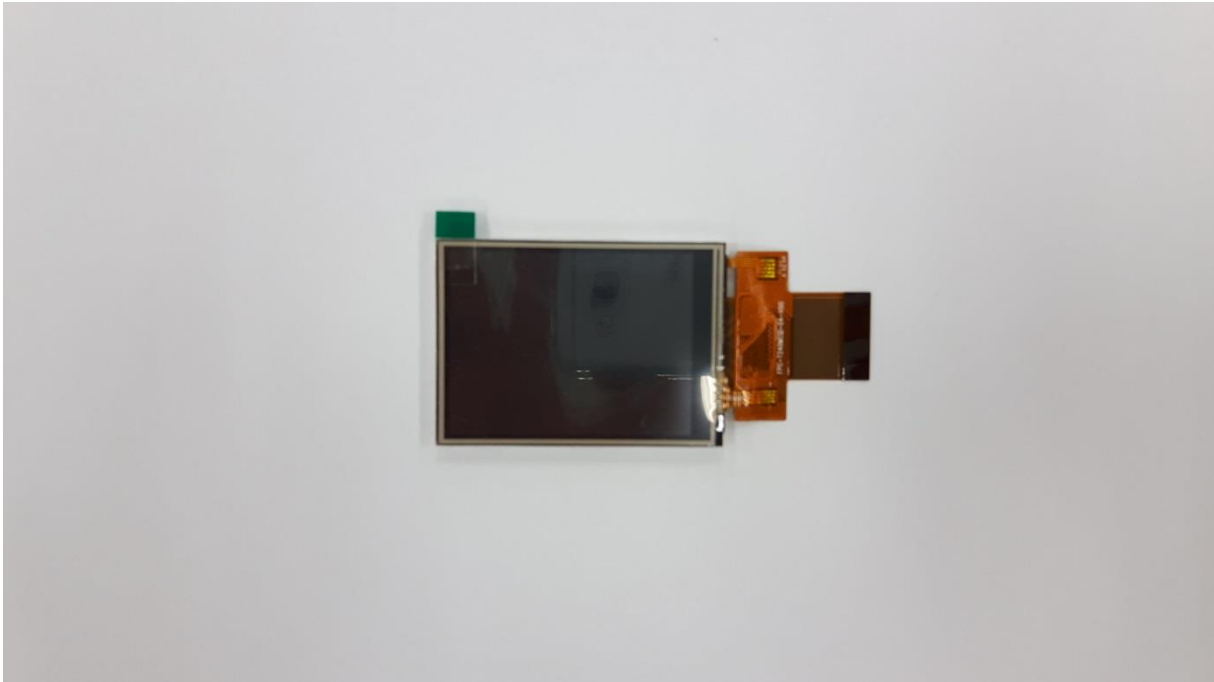
**[MUX Block - Bottom]**



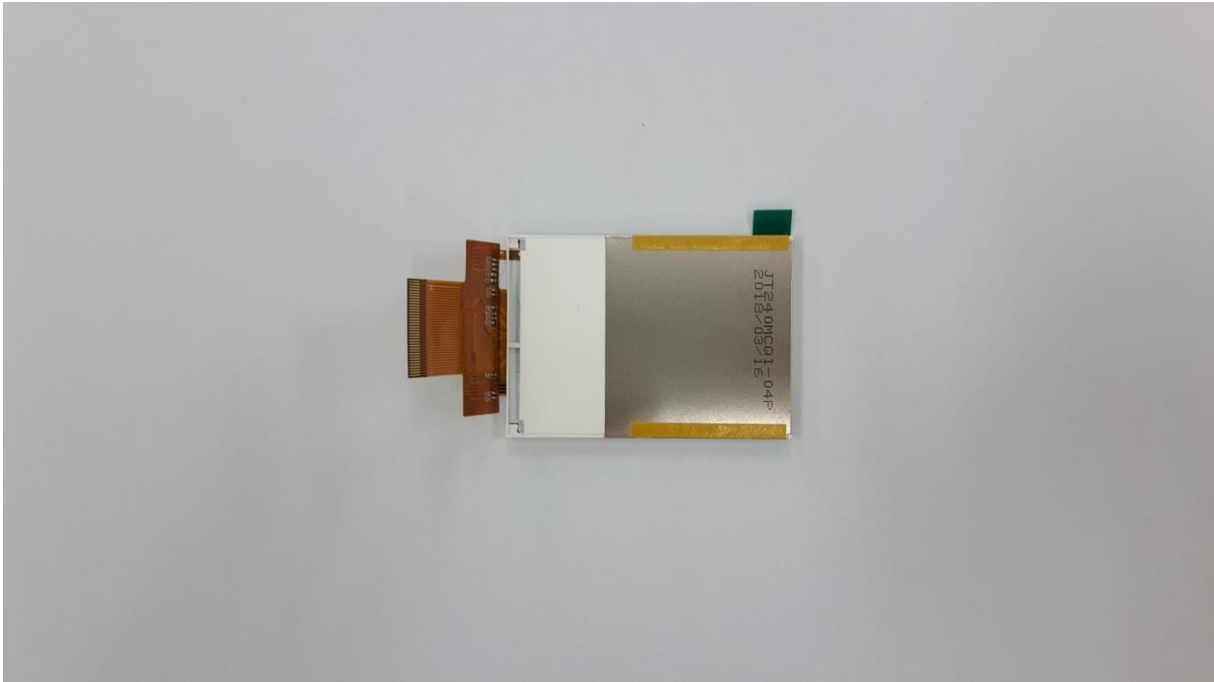
**[LCD Interface - Top]**



**[LCD Interface - Bottom]**



**[LCD - Top]**



**[LCD - Bottom]**