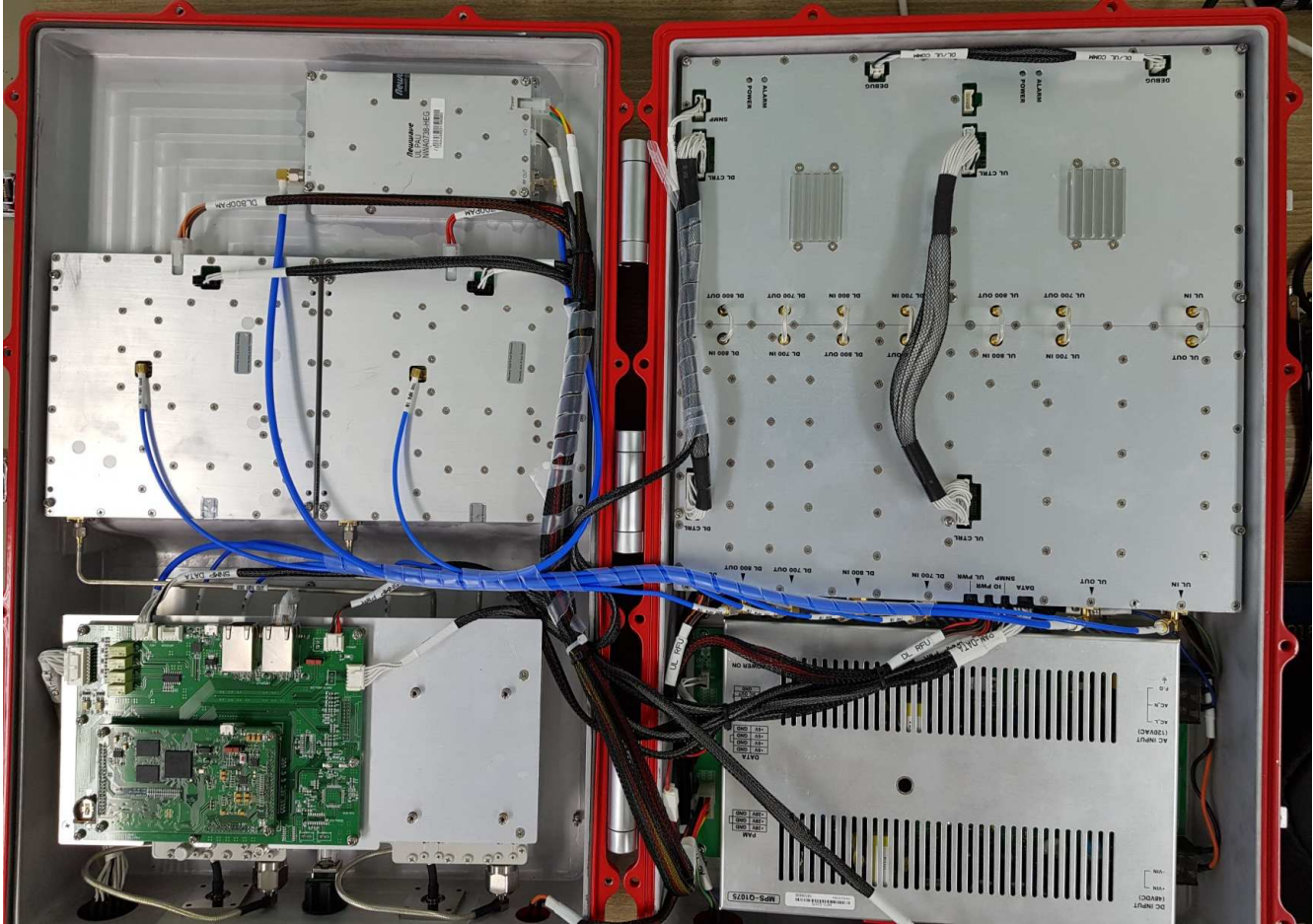
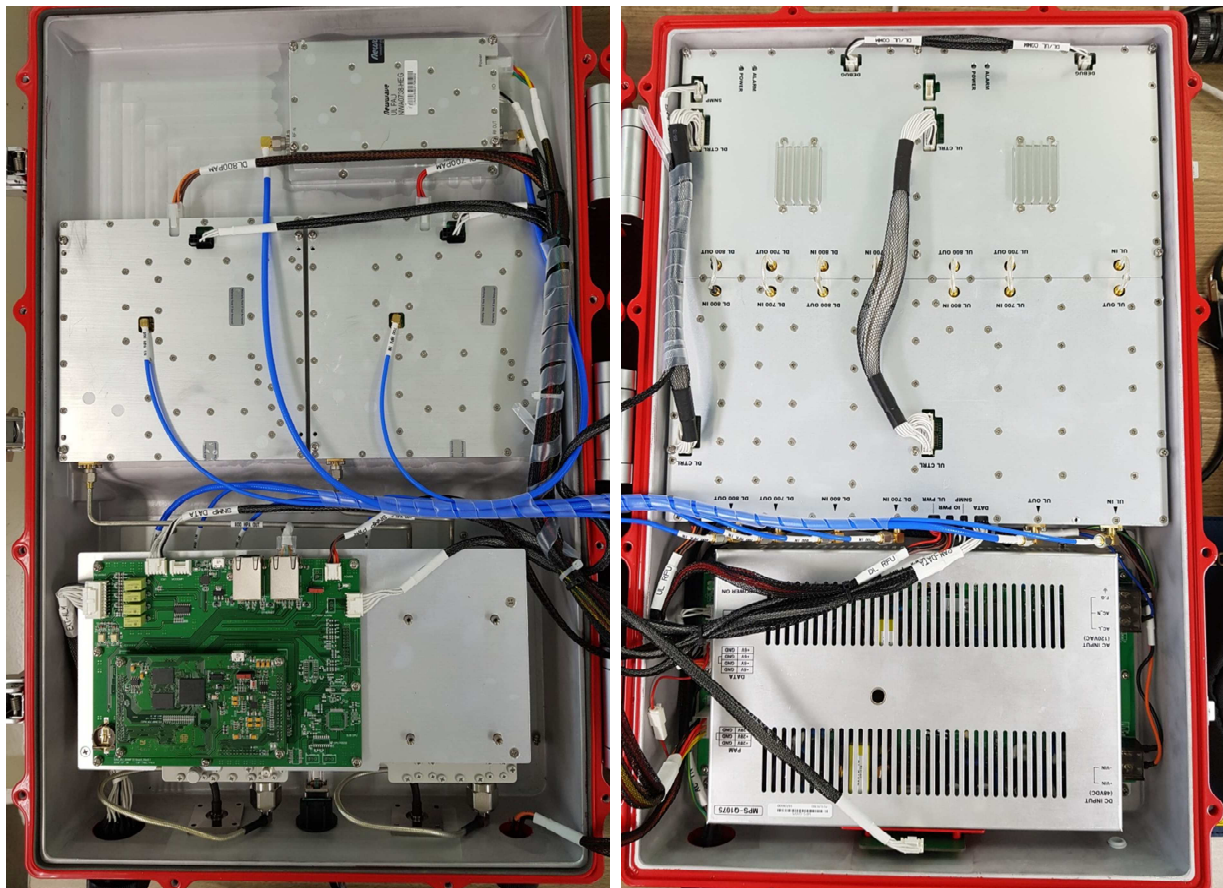


1. Internal Photo

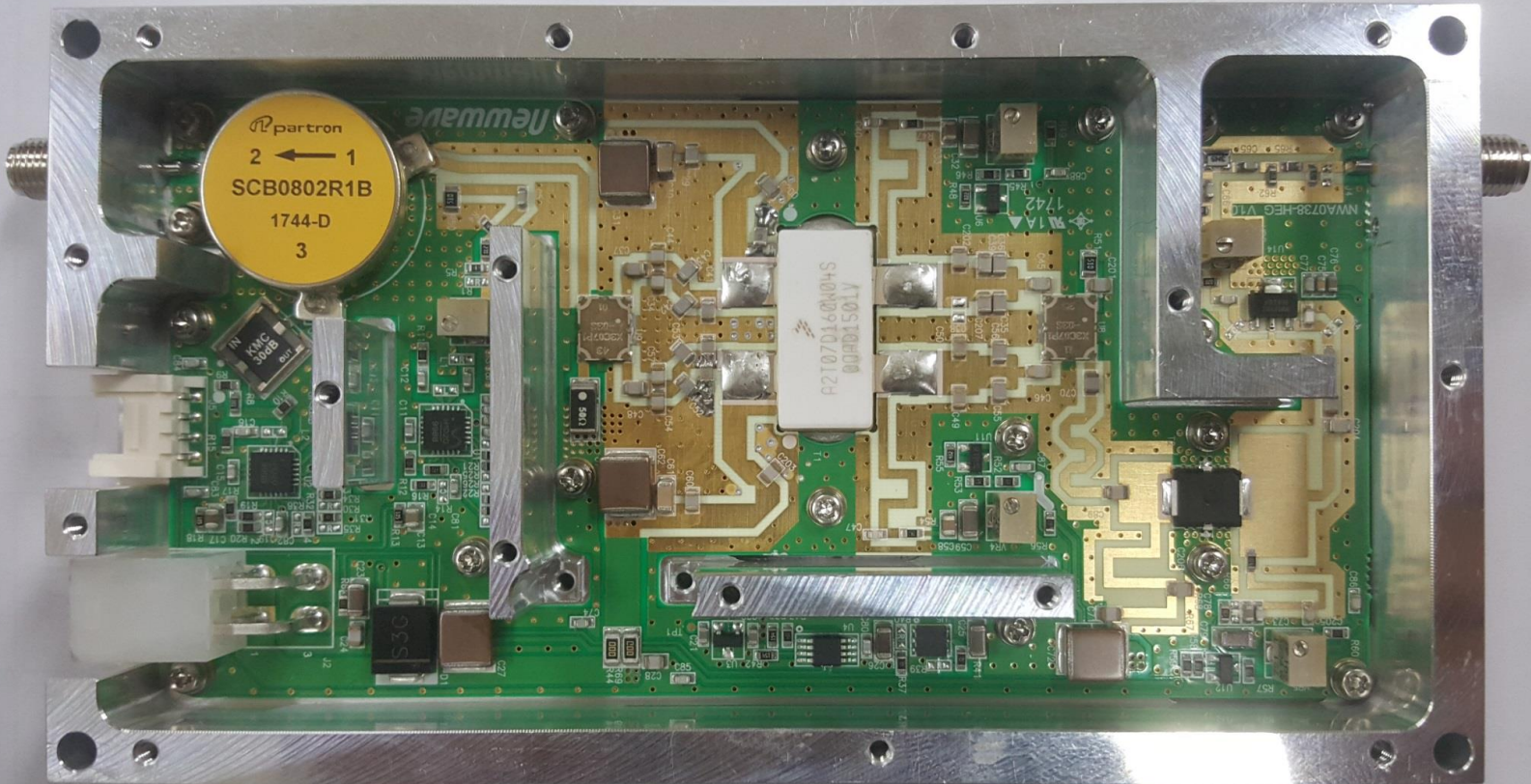


2. Internal Photo

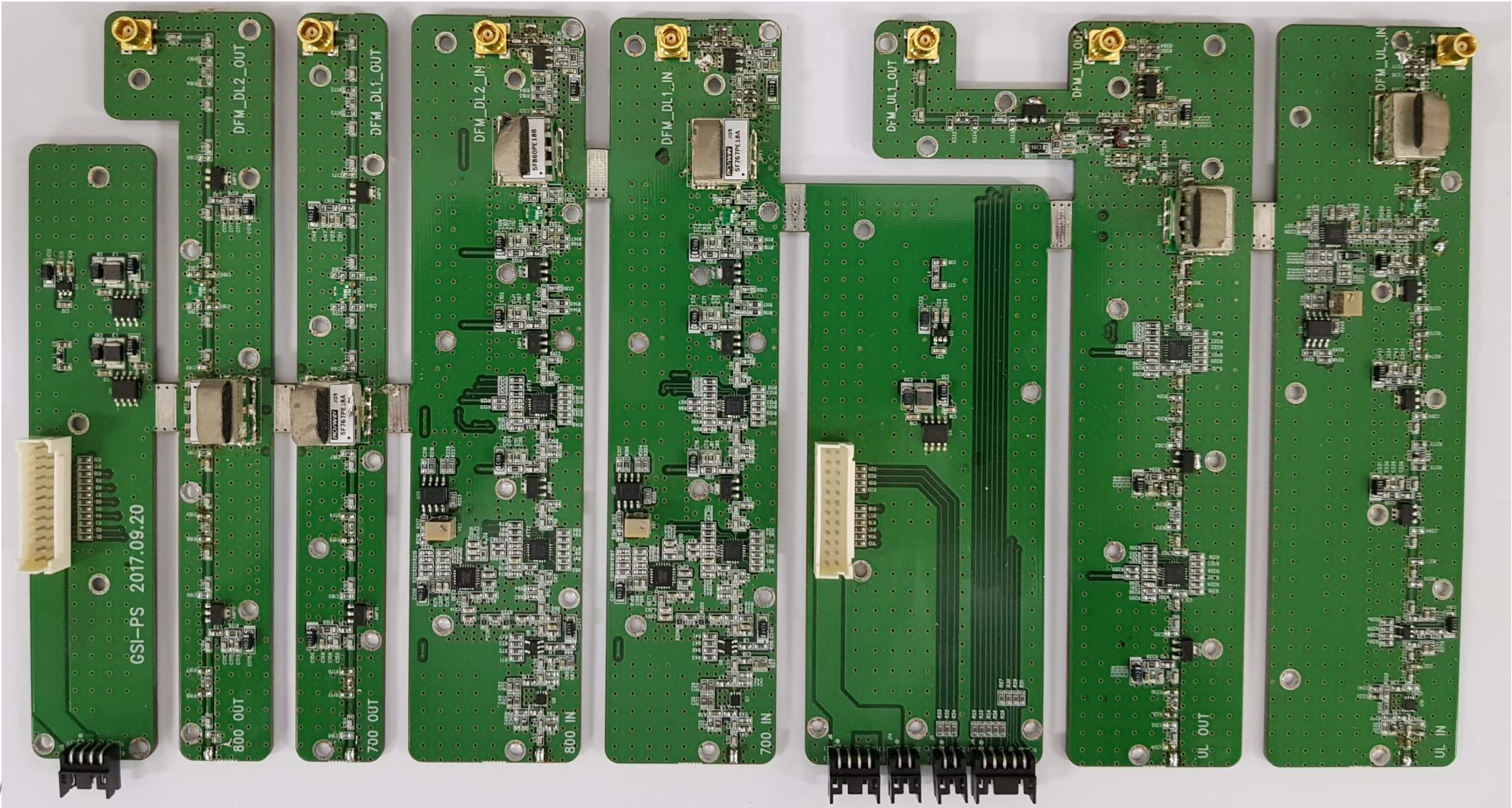


Bottom Side

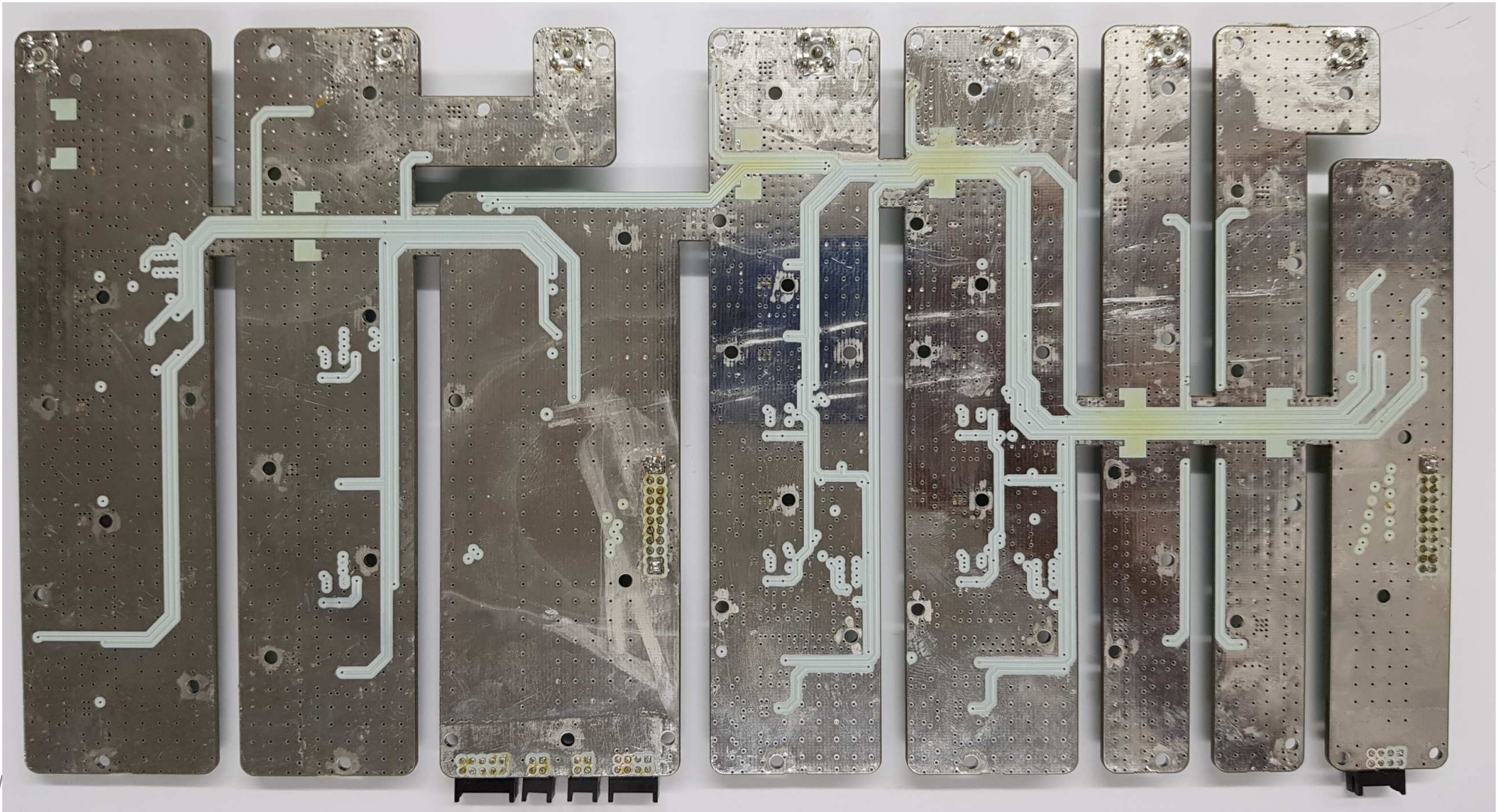
Cover Side



1. RFU PCB Photo – Top Layer

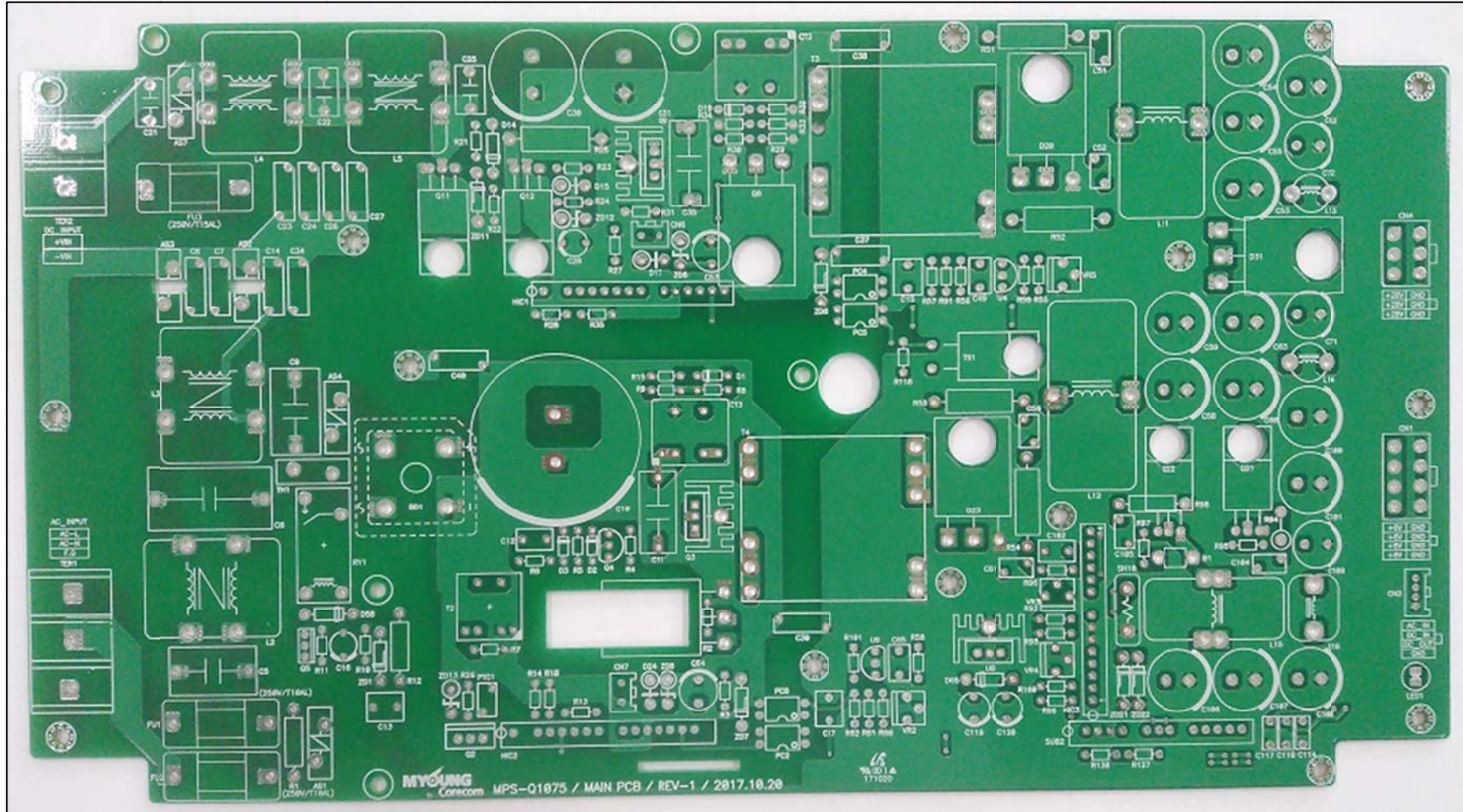


2. RFU PCB Photo – Bottom Layer

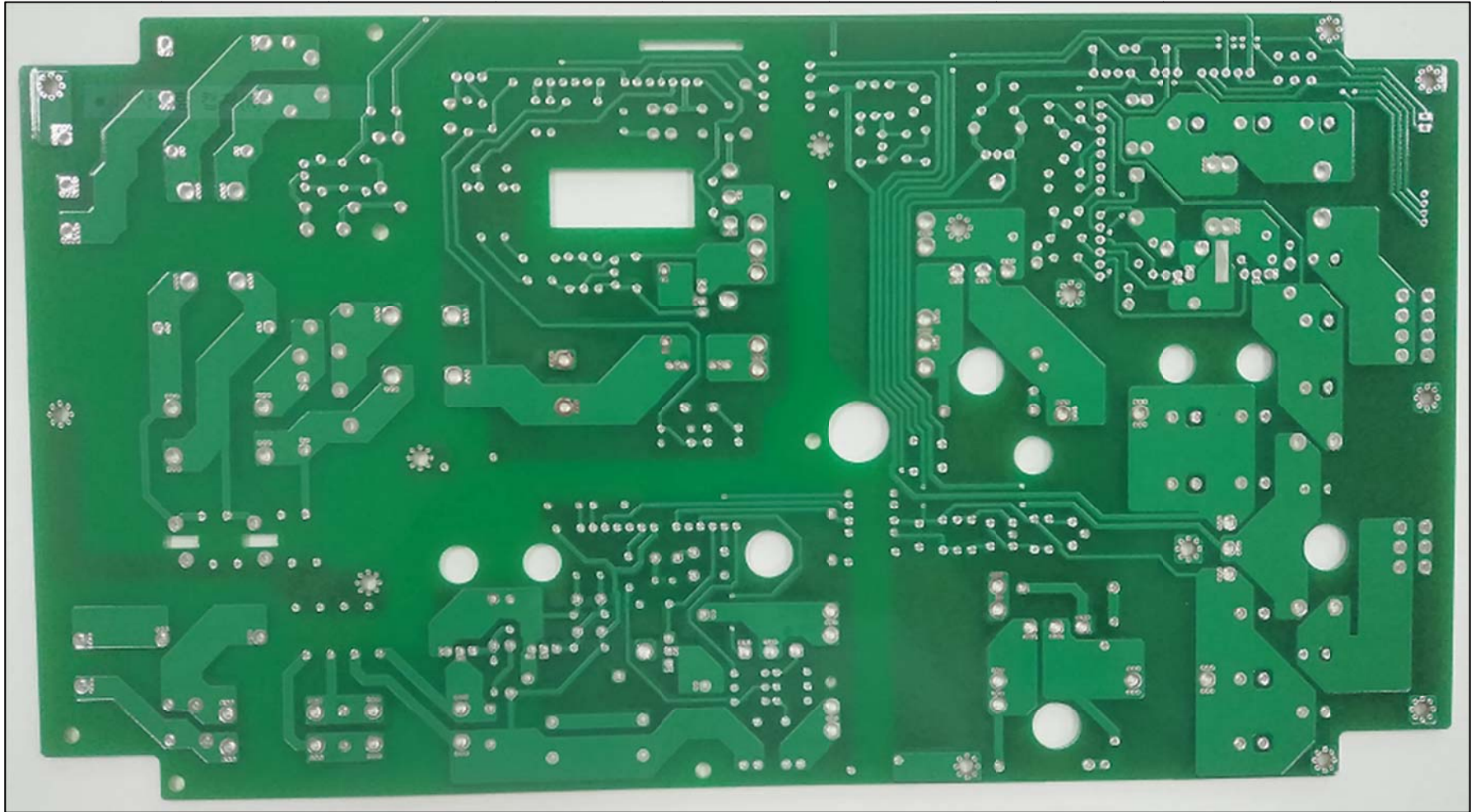


All

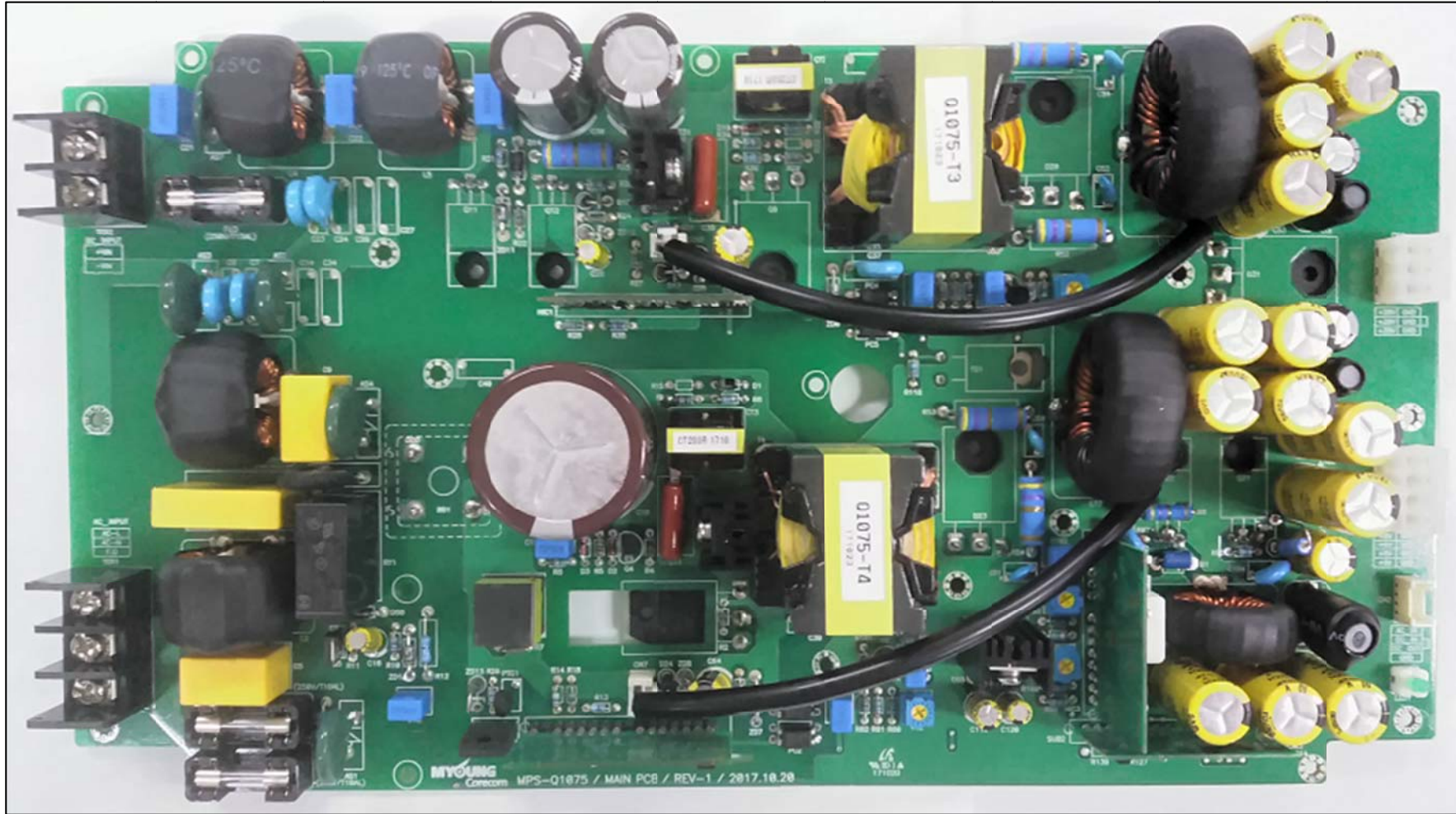
< 북미향 PS-ELITE PSU (MPS-Q1075)_MAIN PCB 윗면사진 >



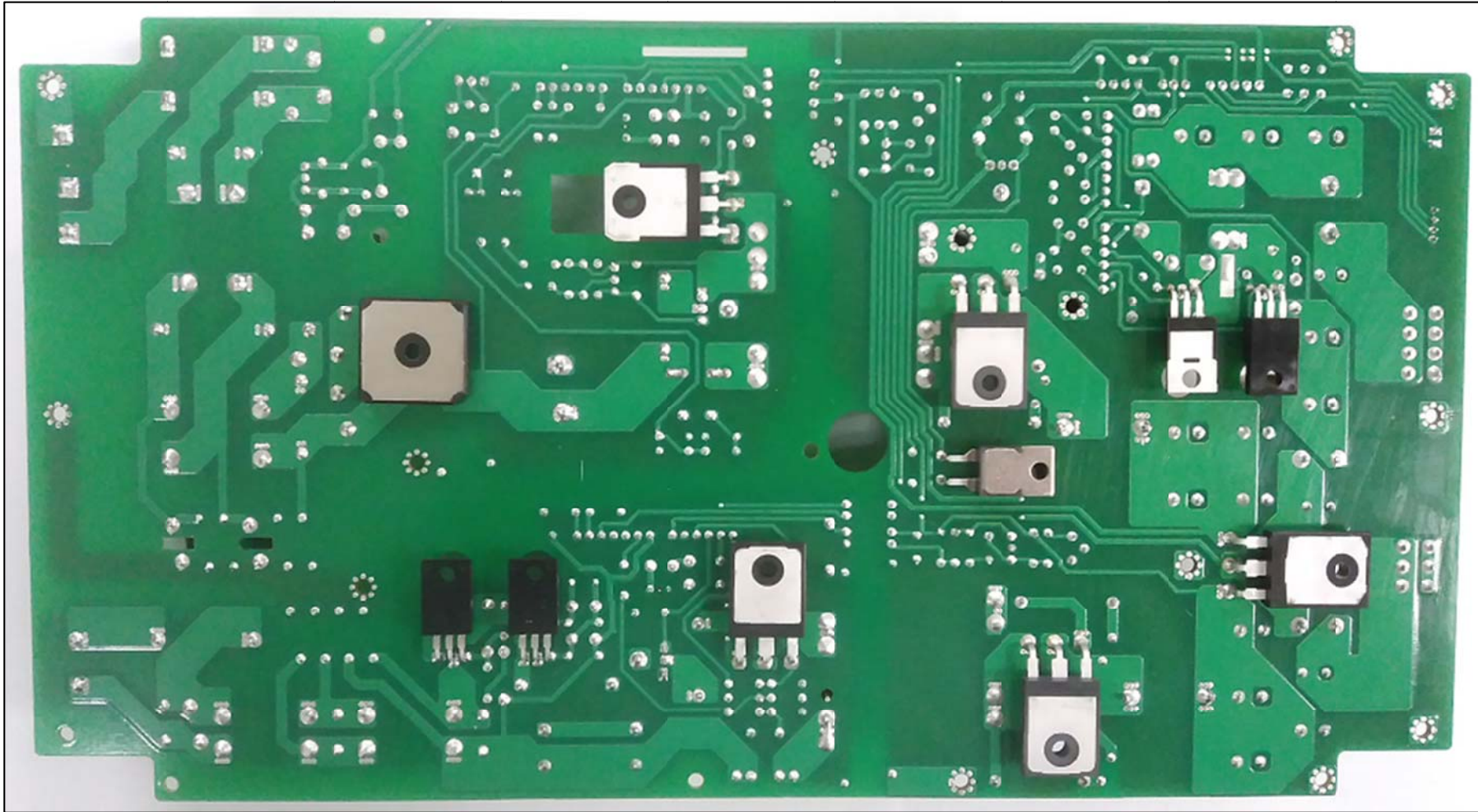
< 북미향 PS-ELITE PSU (MPS-Q1075)_MAIN PCB 밑면사진 >



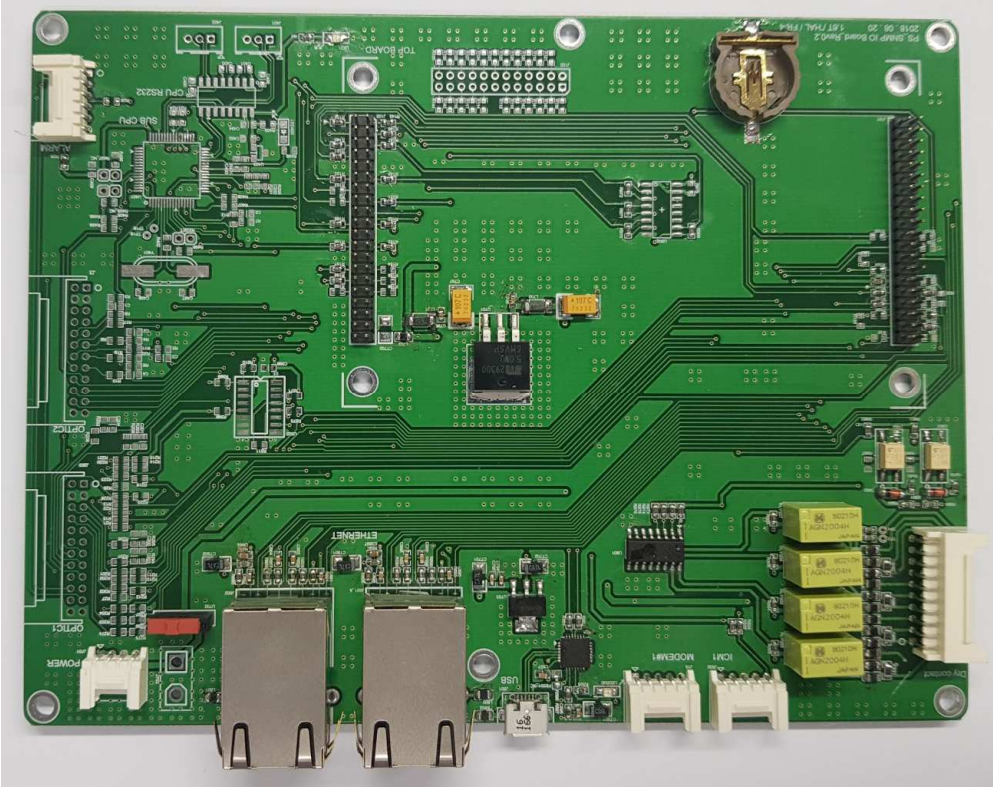
< 북미향 PS-ELITE PSU (MPS-Q1075)_MAIN PCB A`SSY 윗면사진 >



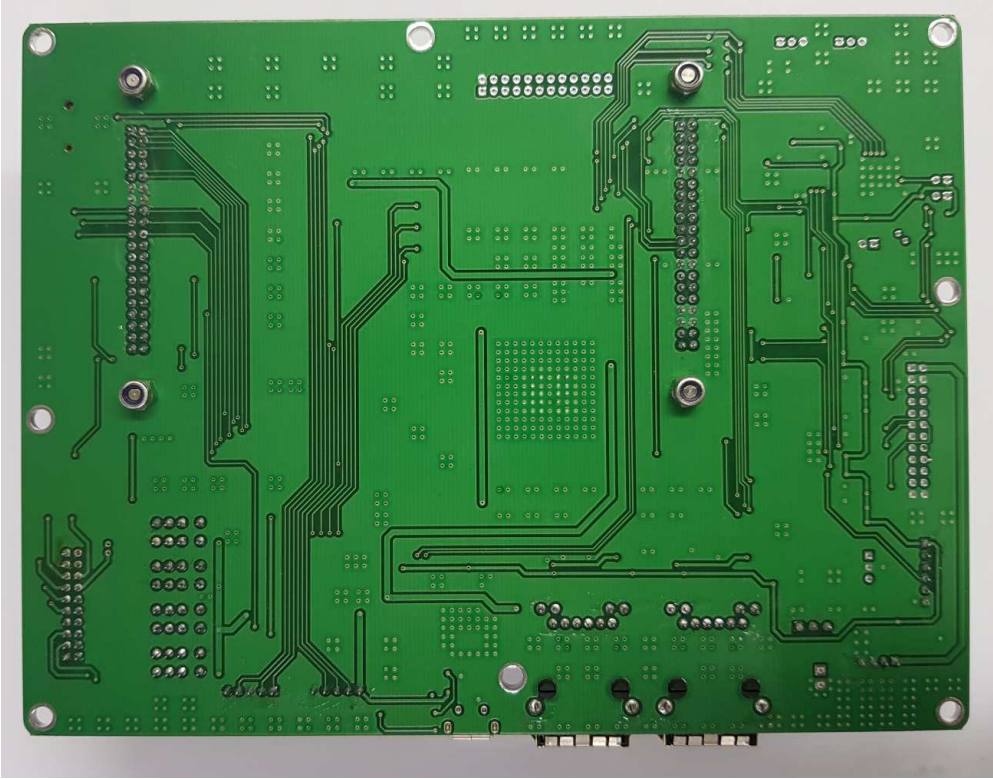
< 북미향 PS-ELITE PSU (MPS-Q1075)_MAIN PCB A`SSY 밑면사진 >



1. I/O B'rd PCB Photo

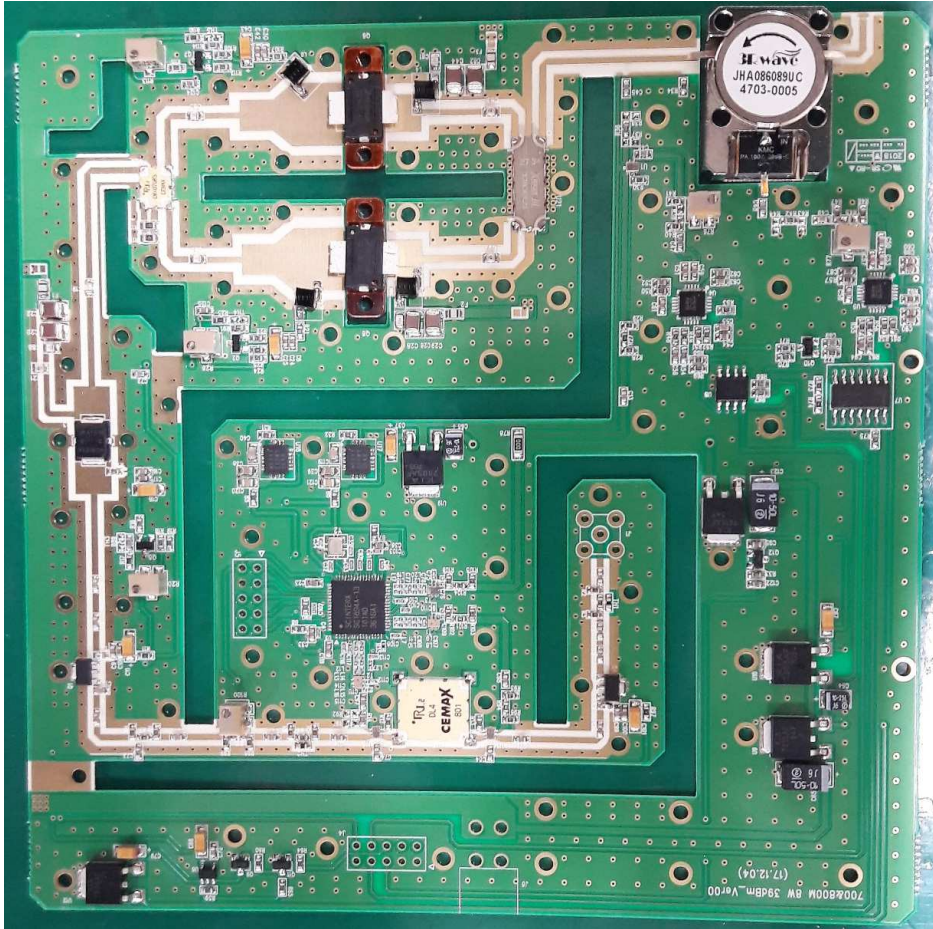


Top Layer

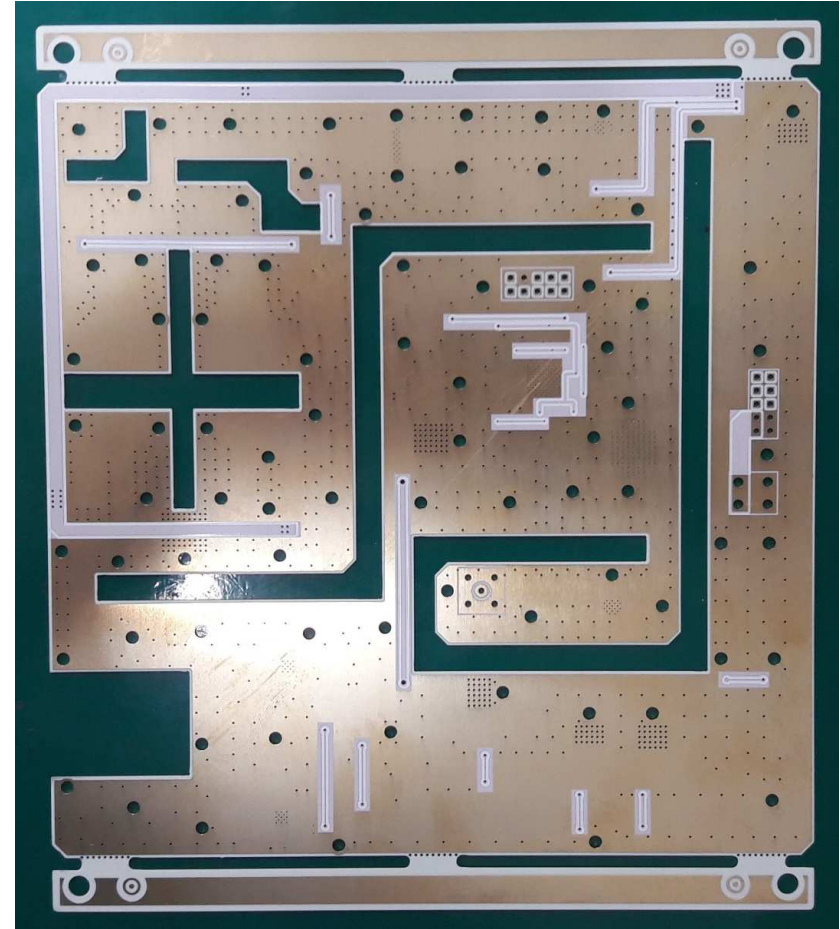


Bottom Layer

1. PCB Photo

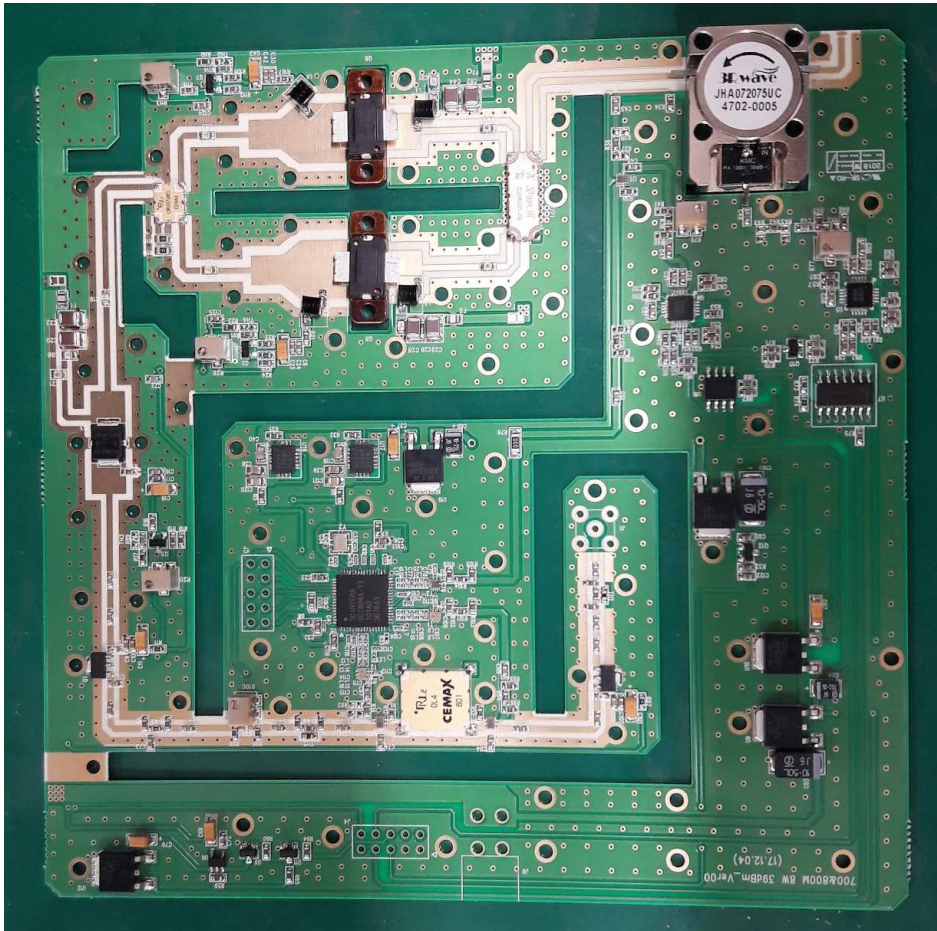


Top Layer

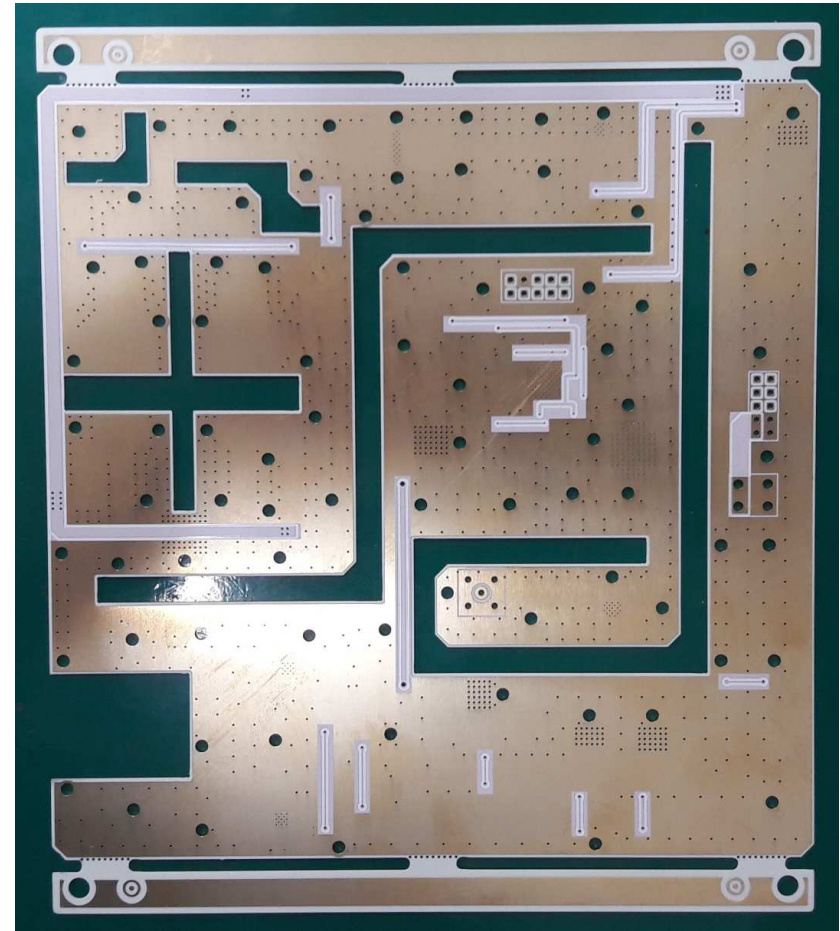


Bottom Layer

1. PCB Layout

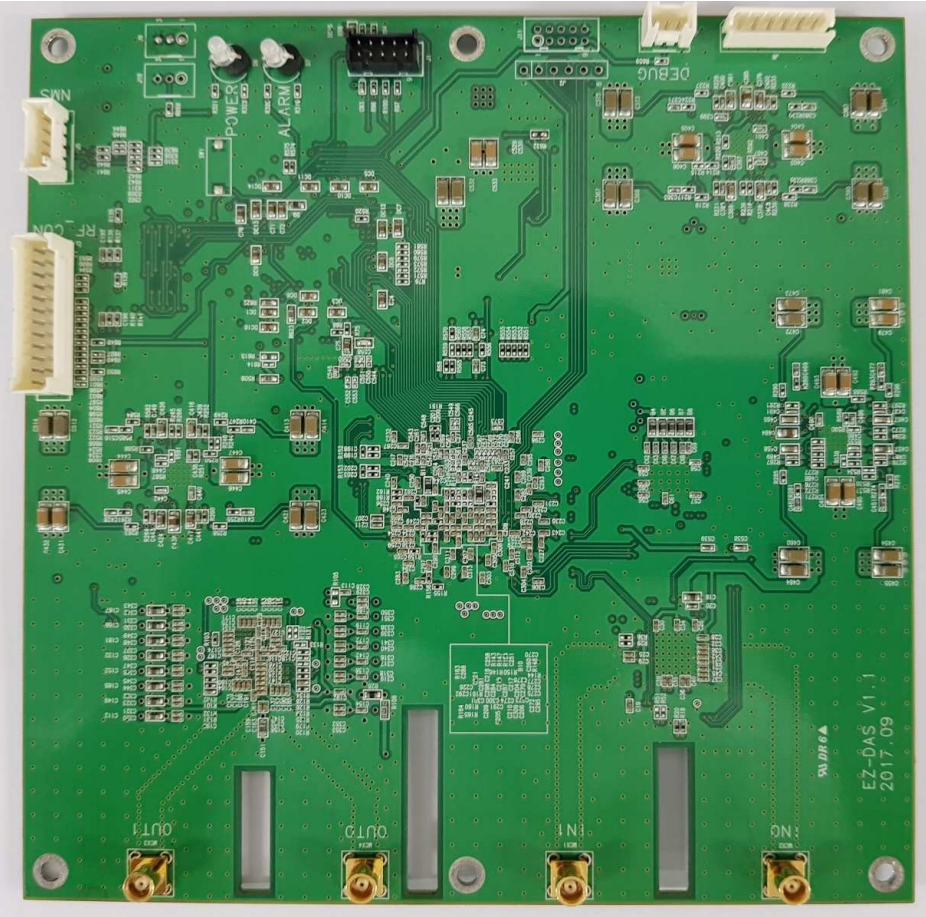


Top Layer

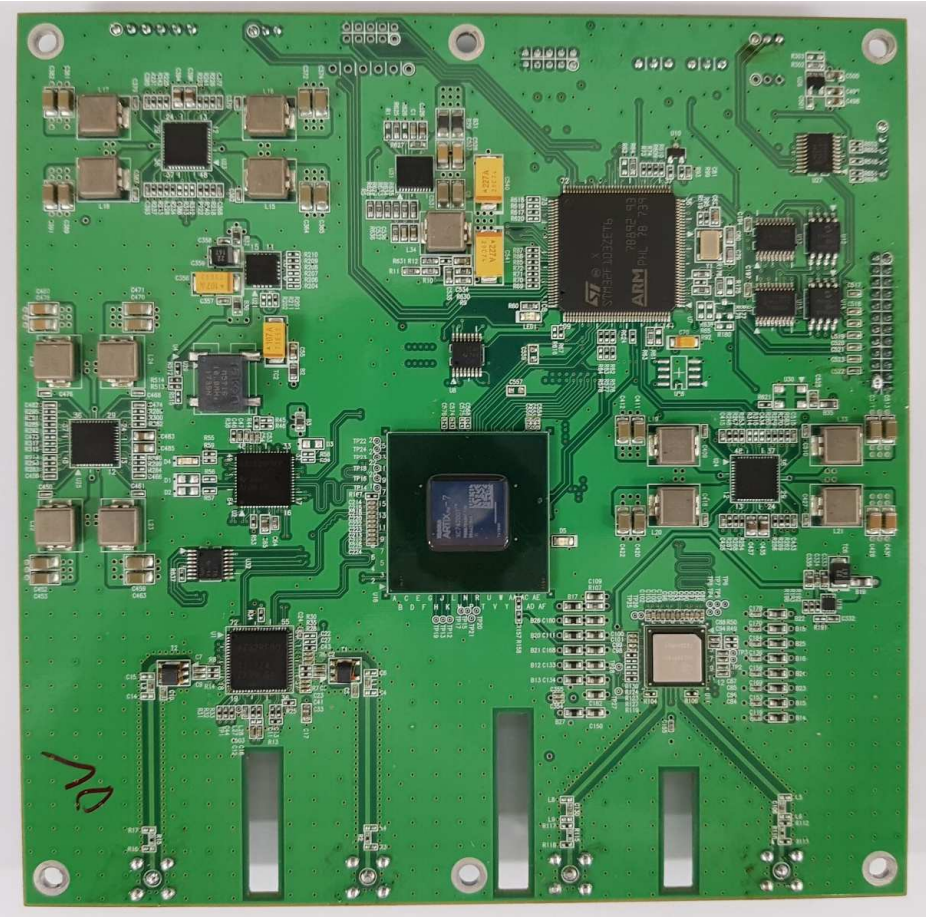


Bottom Layer

1. DFM PCB Photo – DL DFM

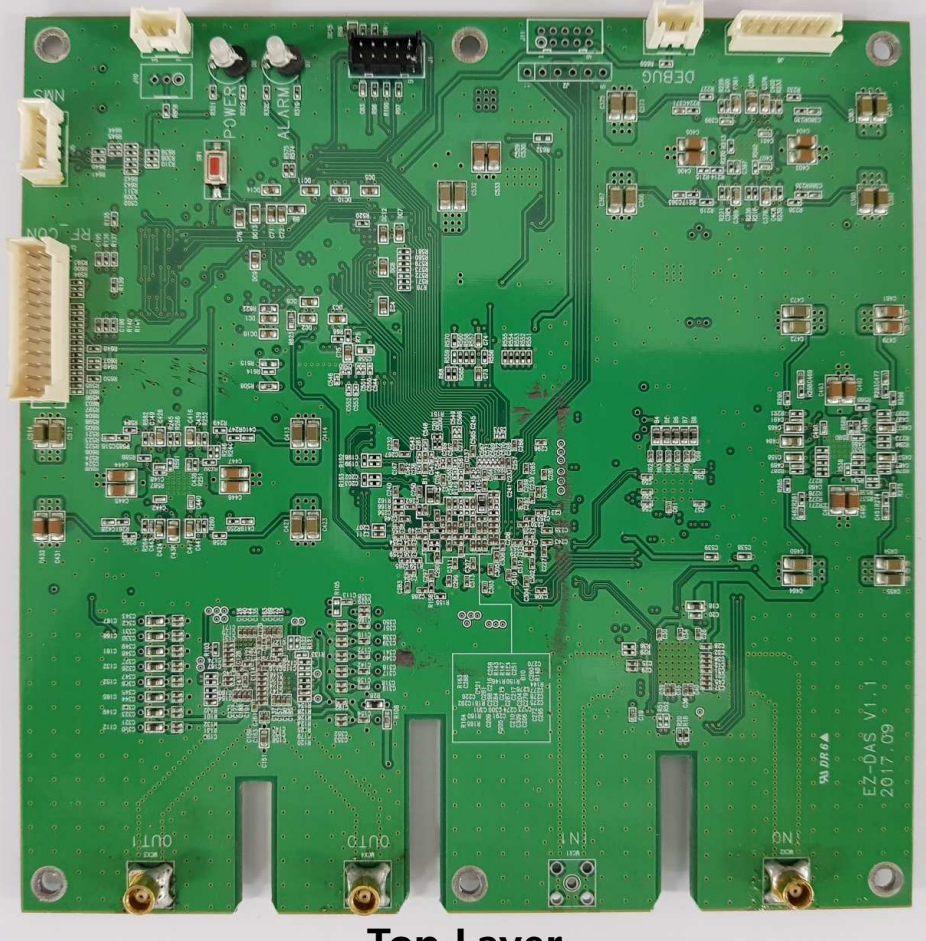


Top Layer

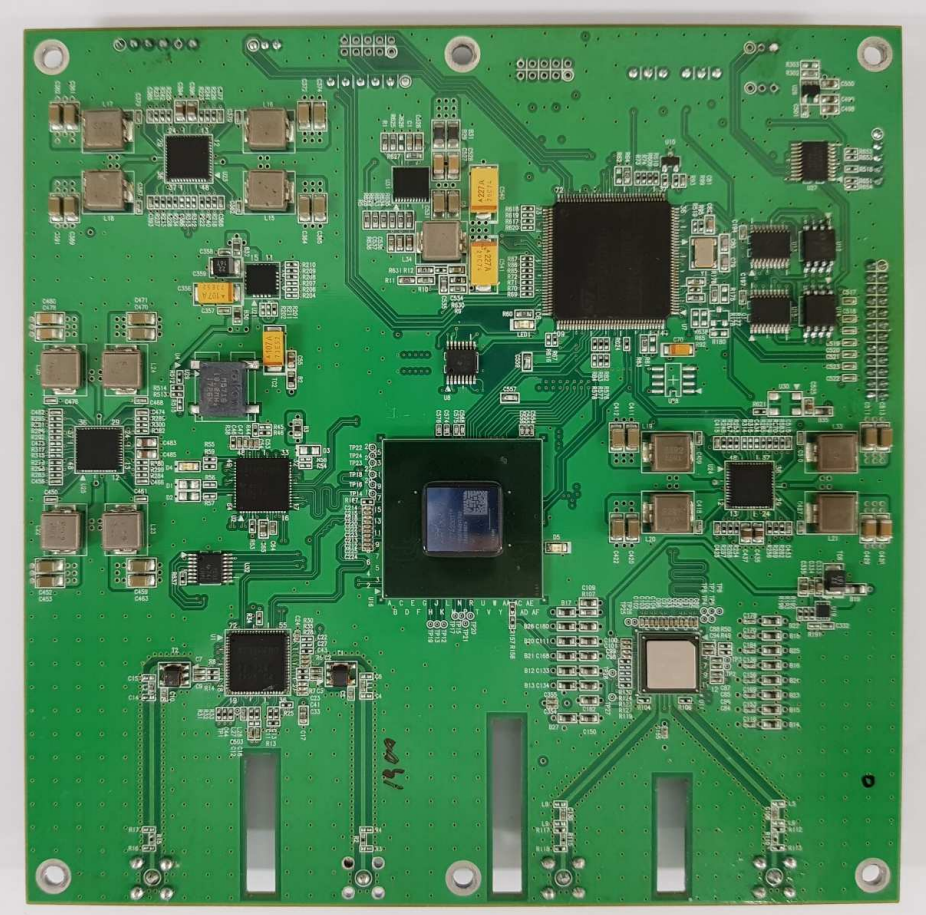


Bottom Layer

2. DFM PCB Photo – UL DFM

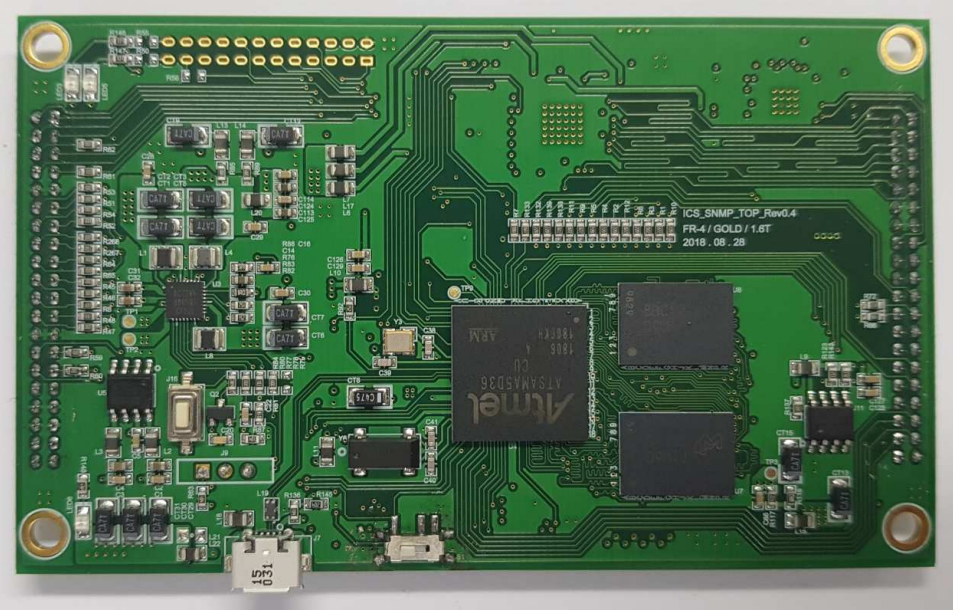


Top Layer

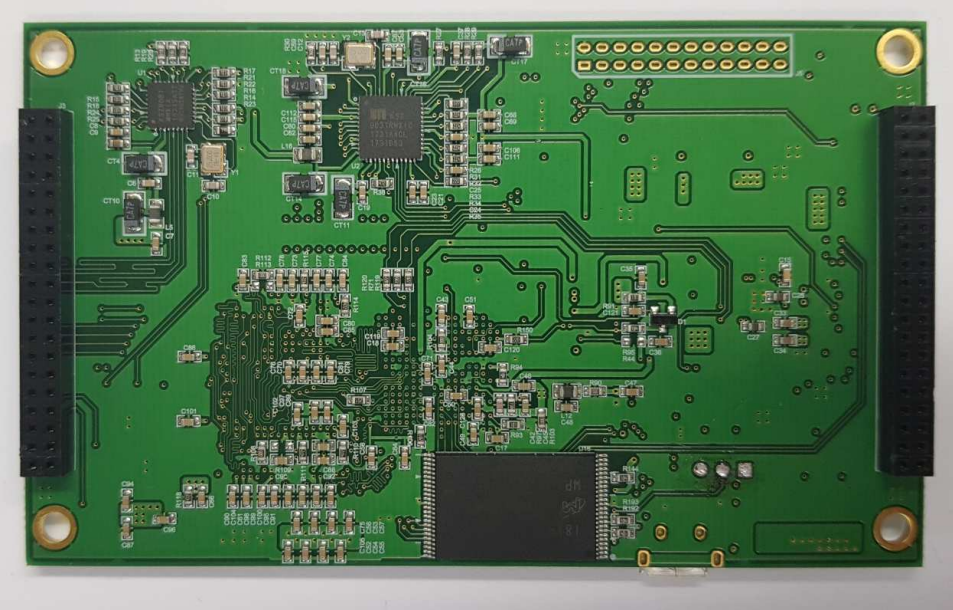


Bottom Layer

1. Application B'rd PCB Photo



Top Layer



Bottom Layer