

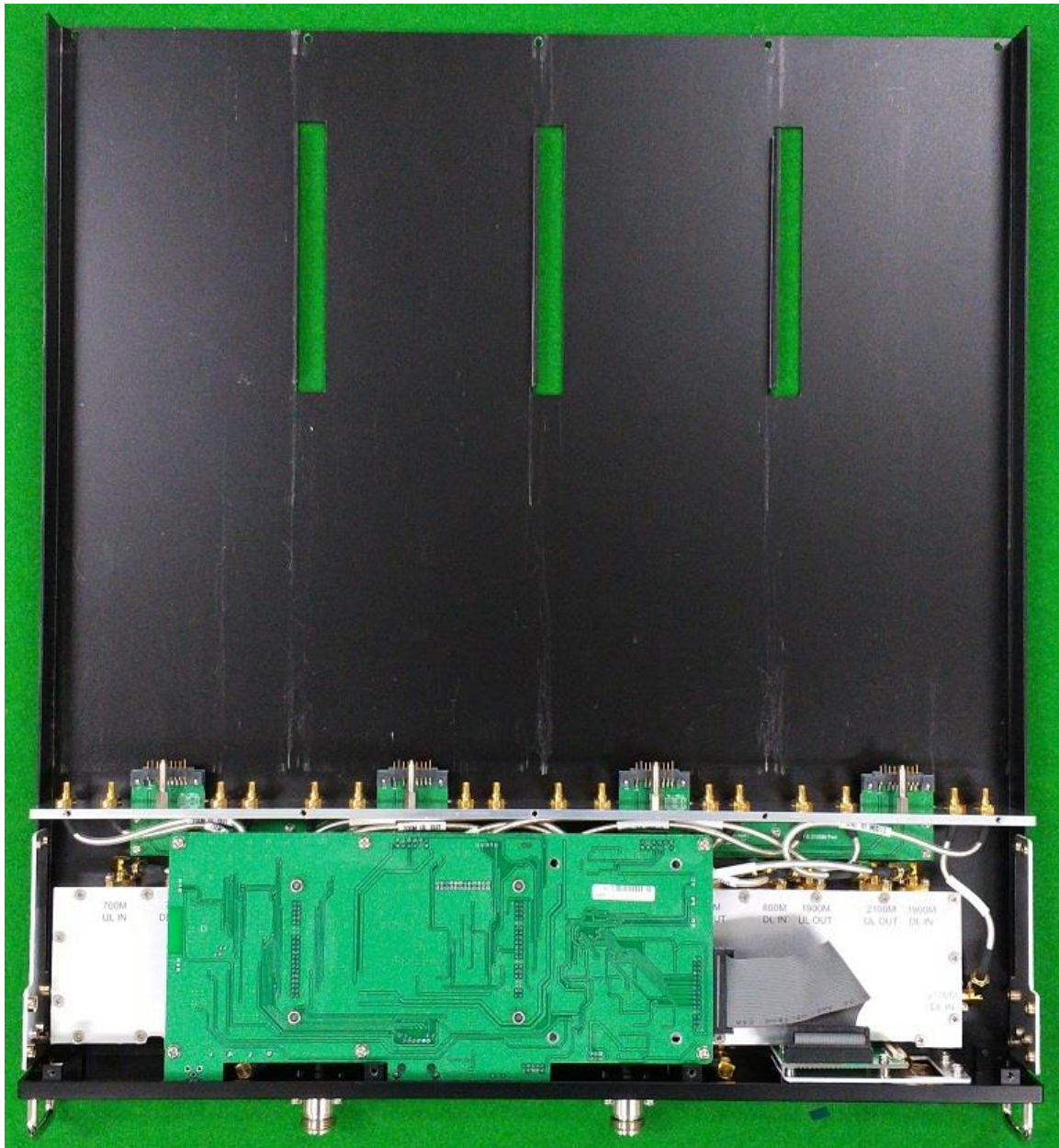
## **INTERNAL PHOTOGRAPHS**

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**[Internal photo\_1]**



**[Internal photo\_2]**

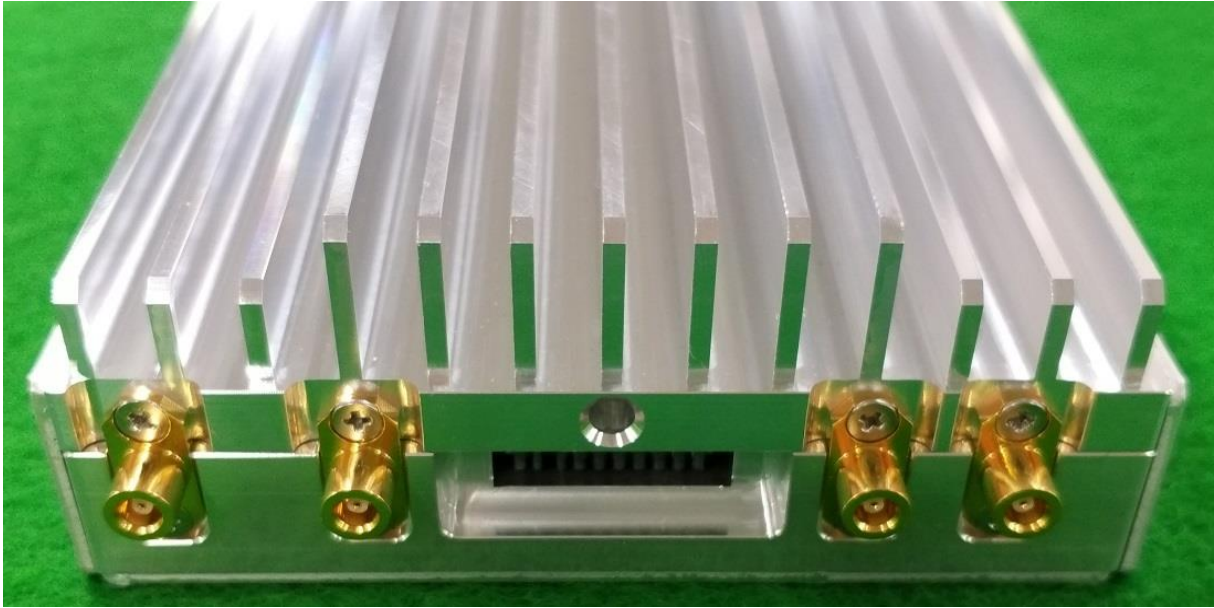




**[Internal photo\_3]**



**[Module Front]**



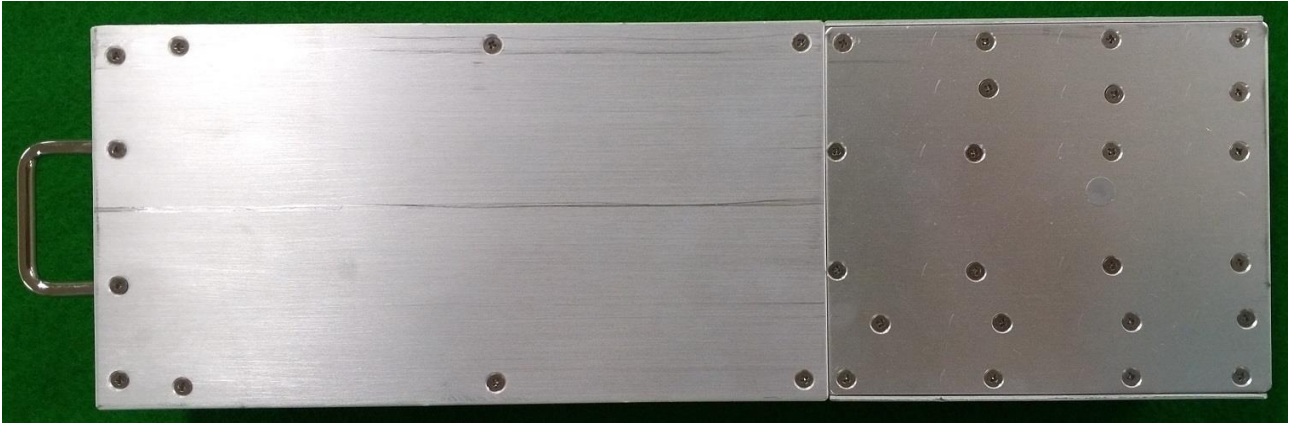
**[Module Rear]**



**[Module Top]**



**[Module Bottom]**





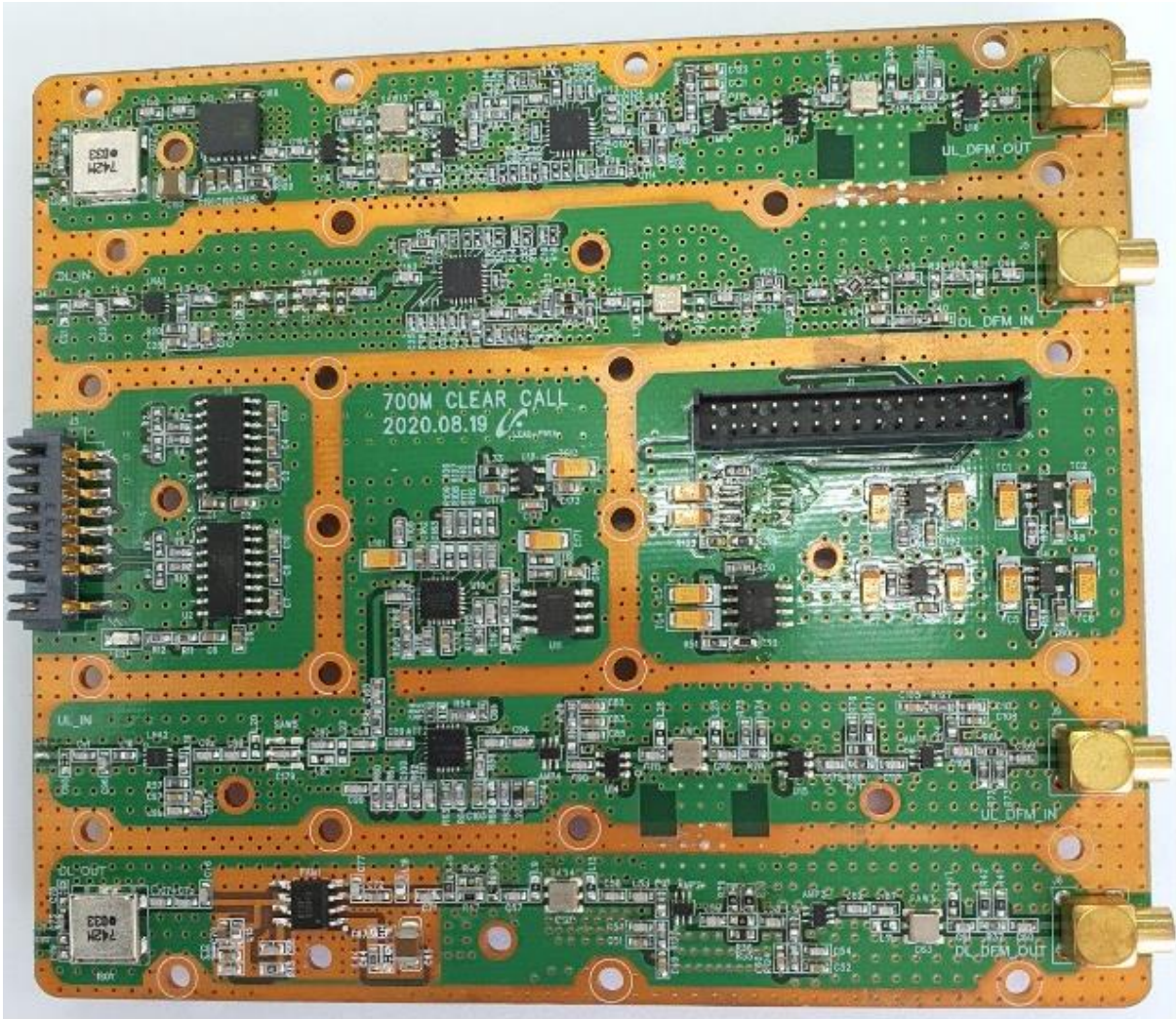
**[Module Left]**



**[Module Right]**

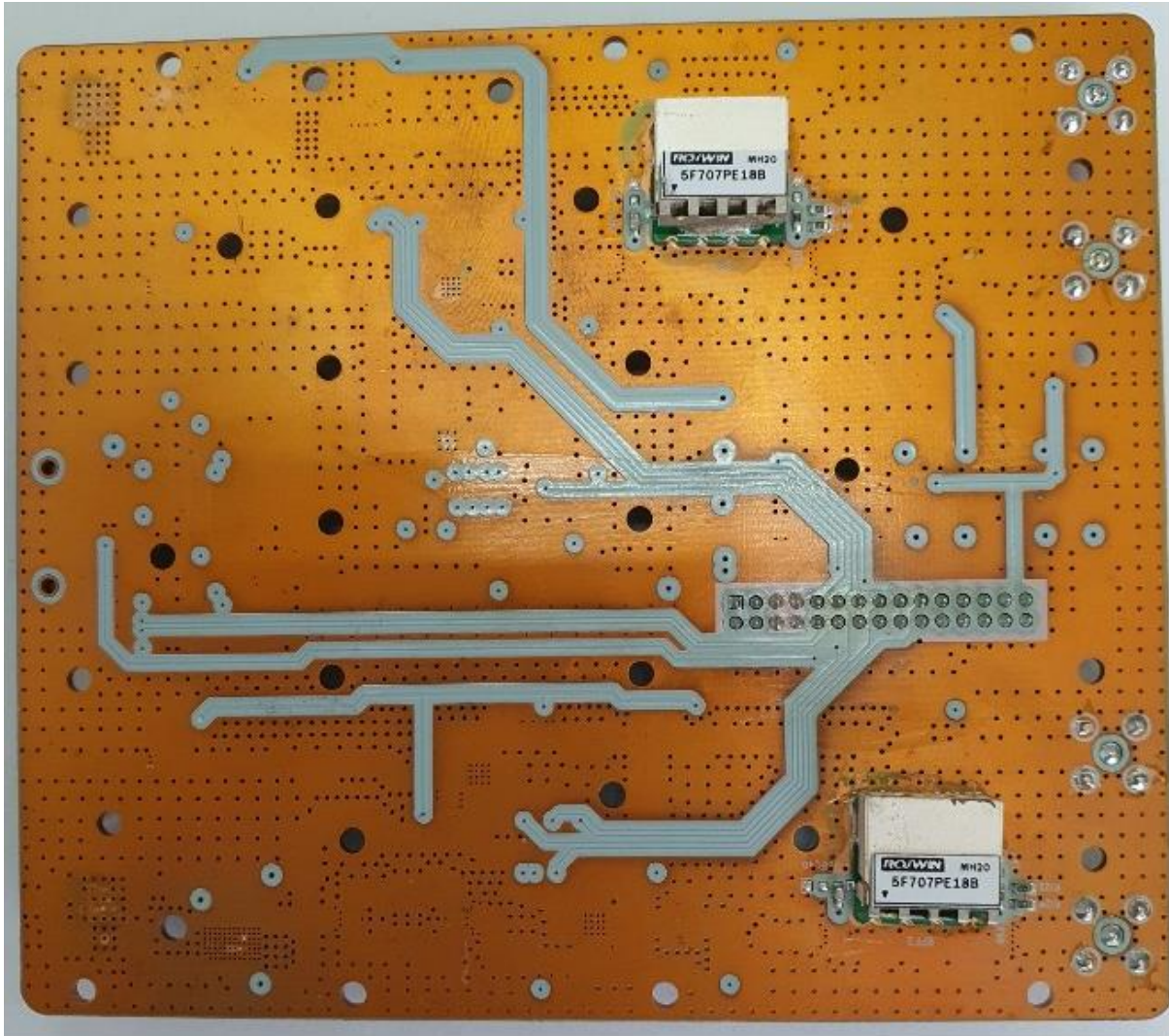


**[700MHz Top]**



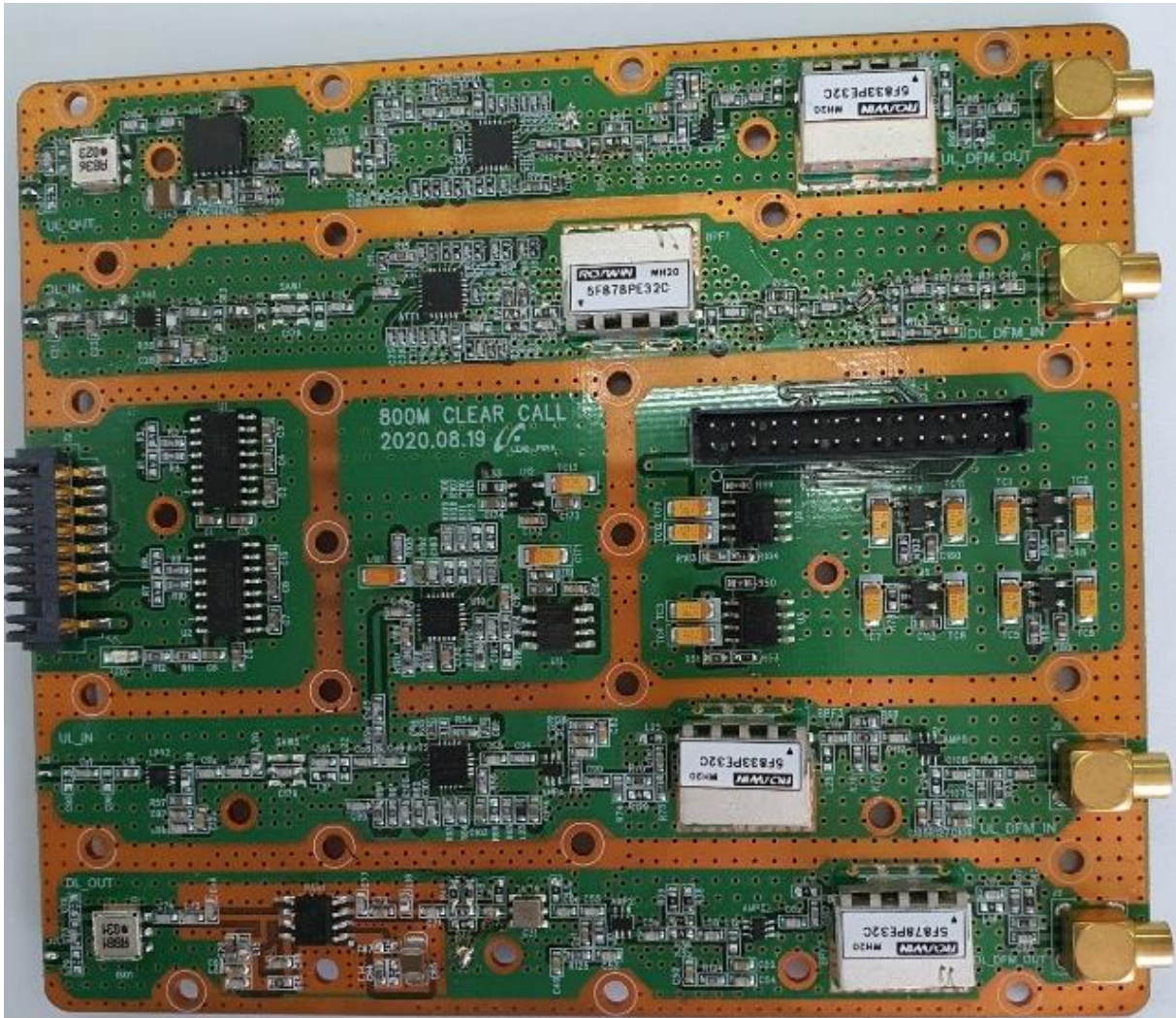


**[700MHz Bottom]**

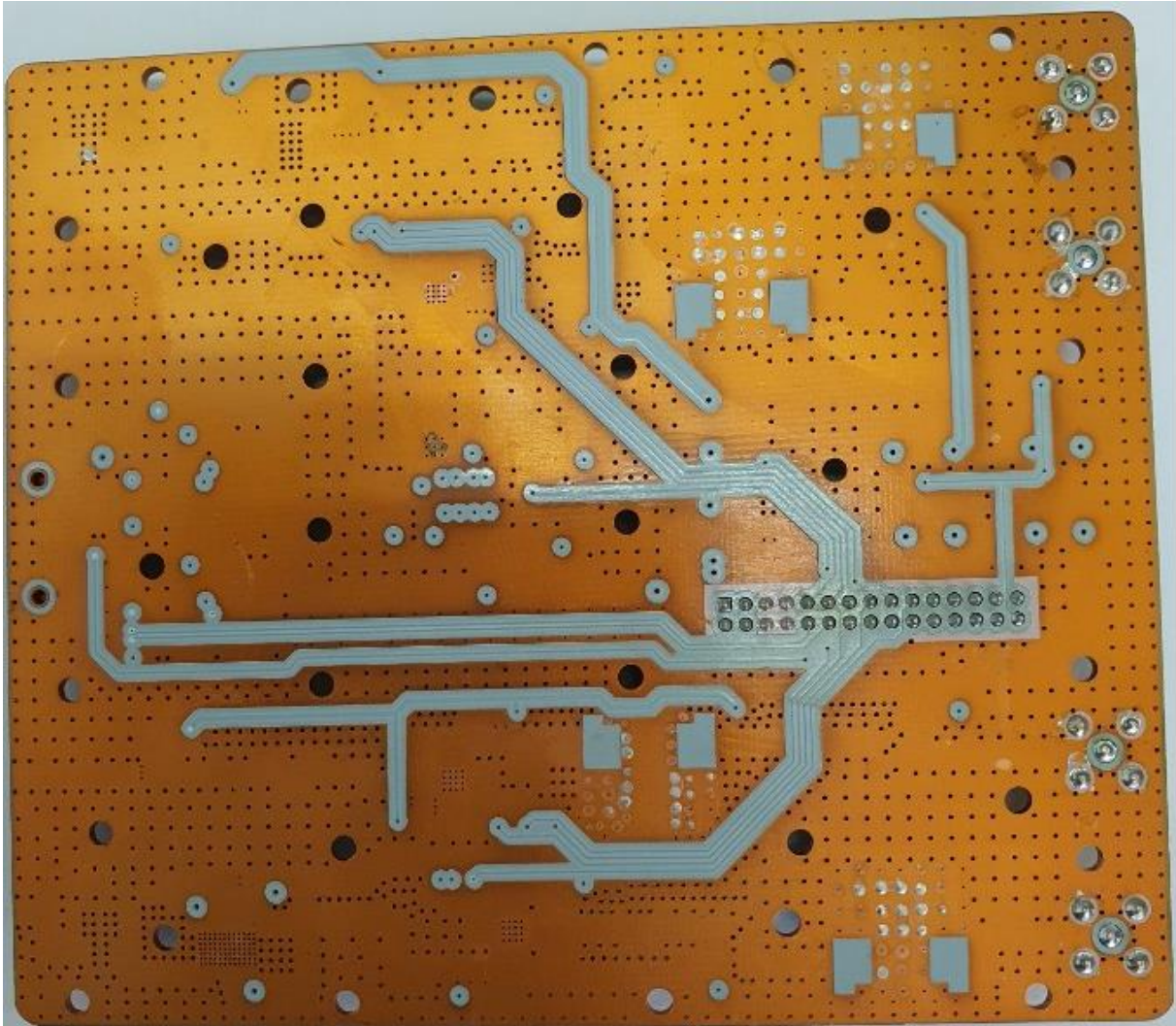




**[800MHz Top]**

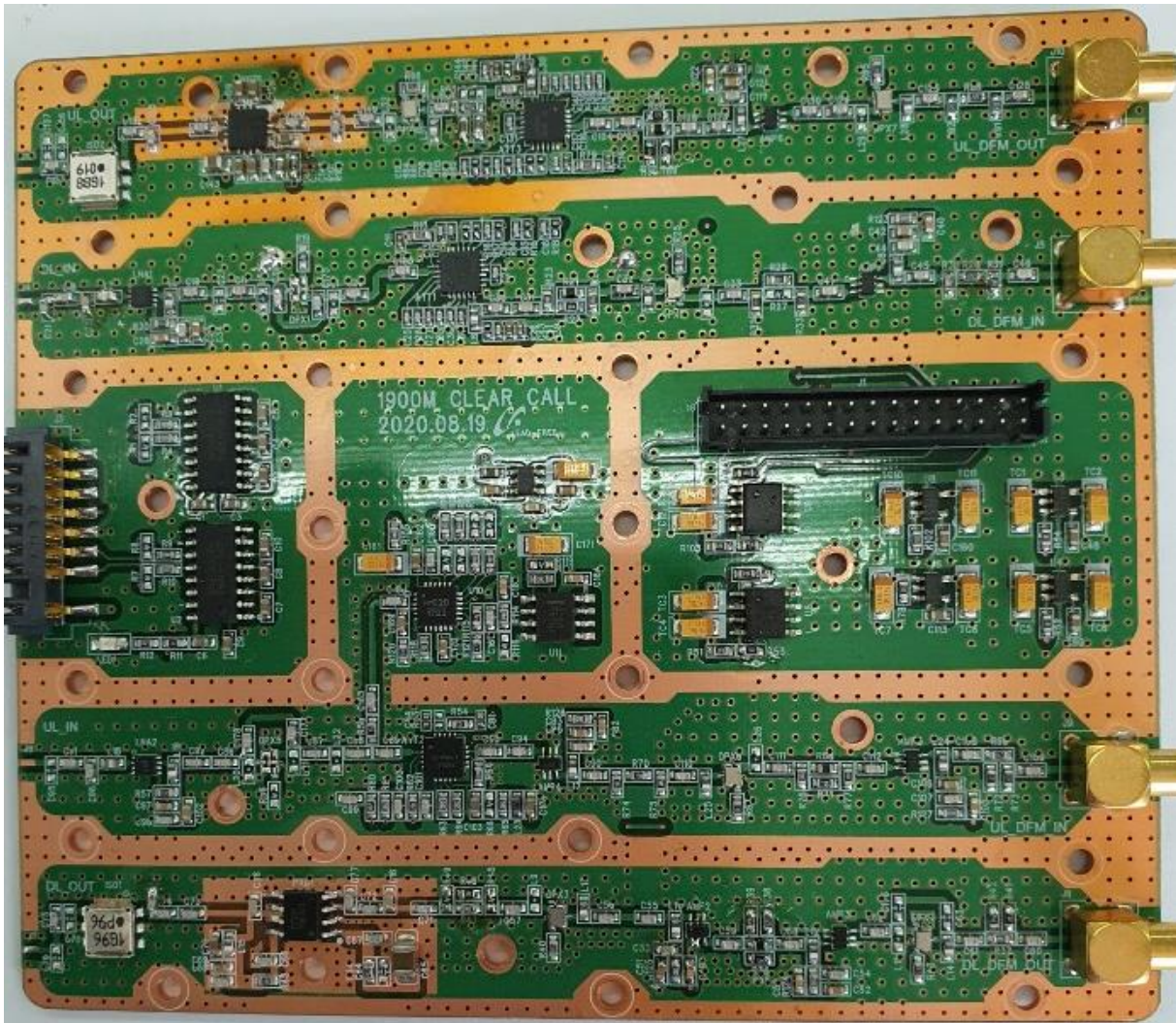


**[800MHz Bottom]**

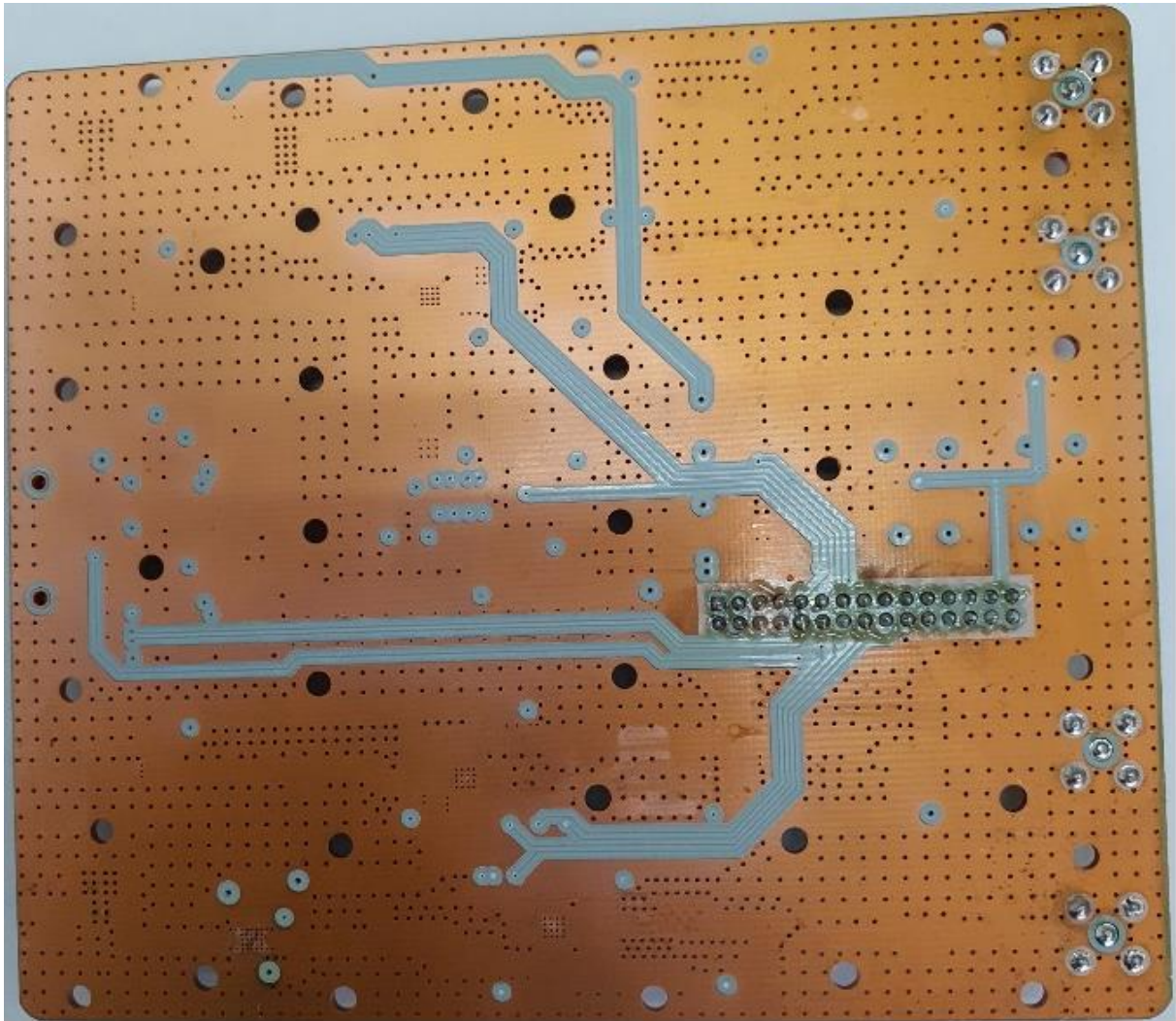




**[1900MHz Top]**

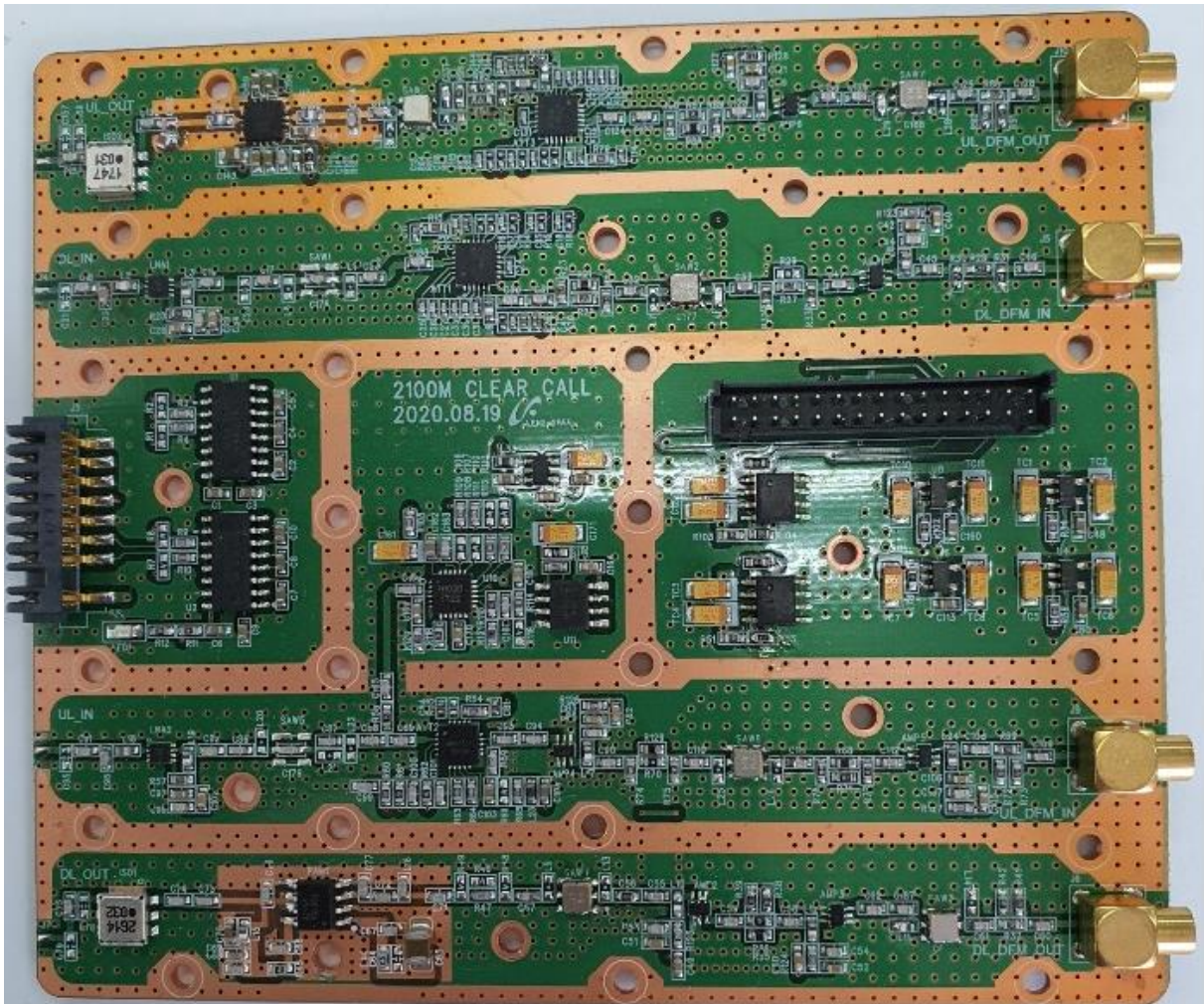


**[1900MHz Bottom]**

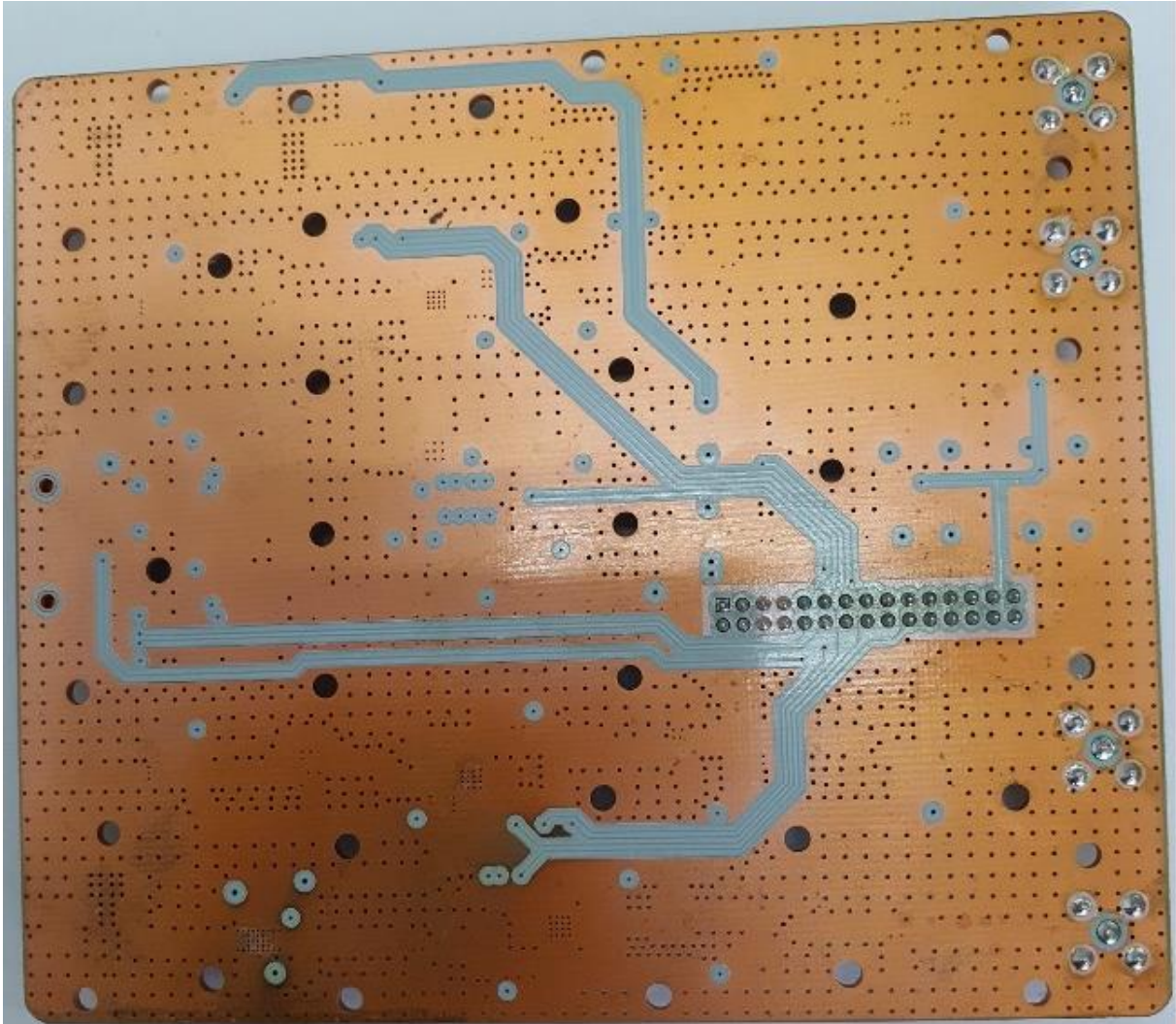




**[2100MHz Top]**

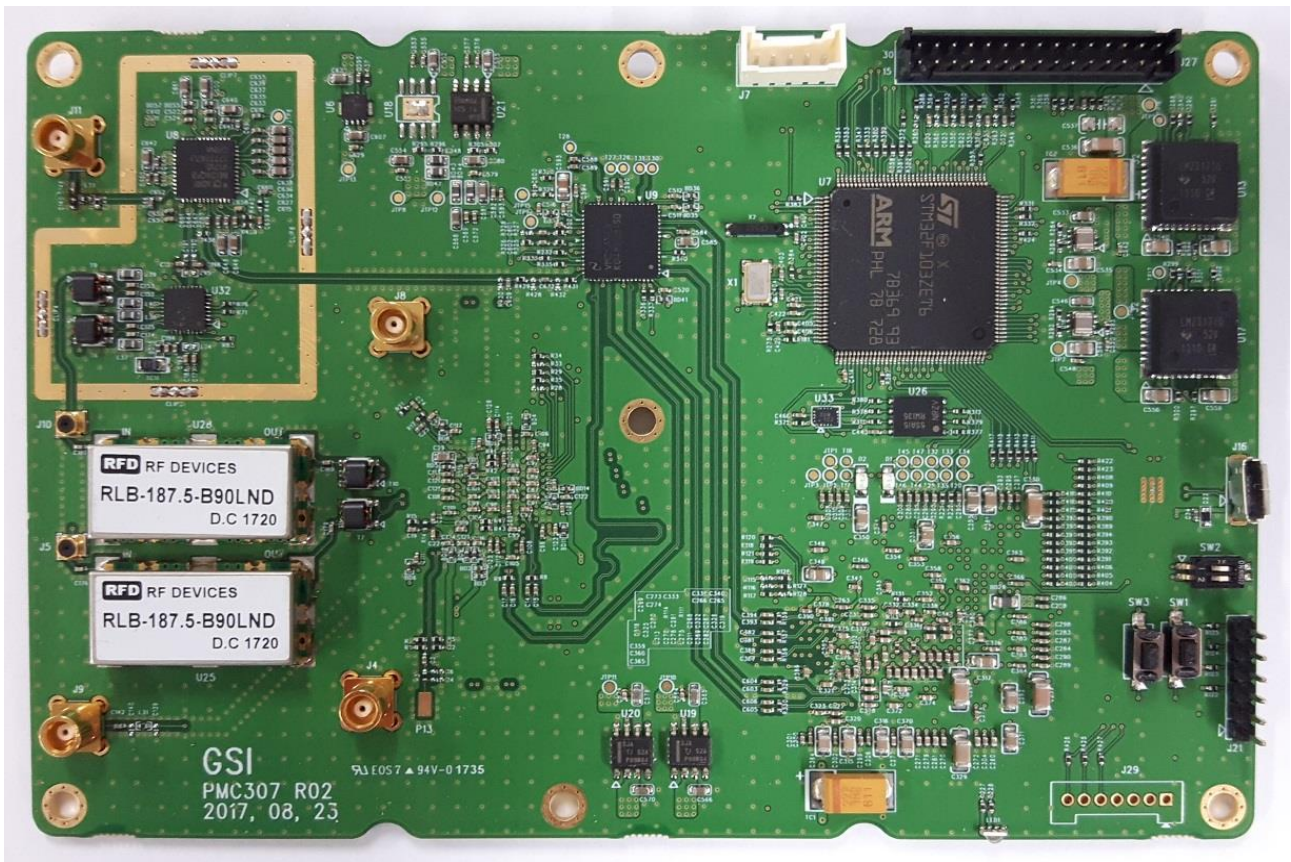


**[2100MHz Bottom]**

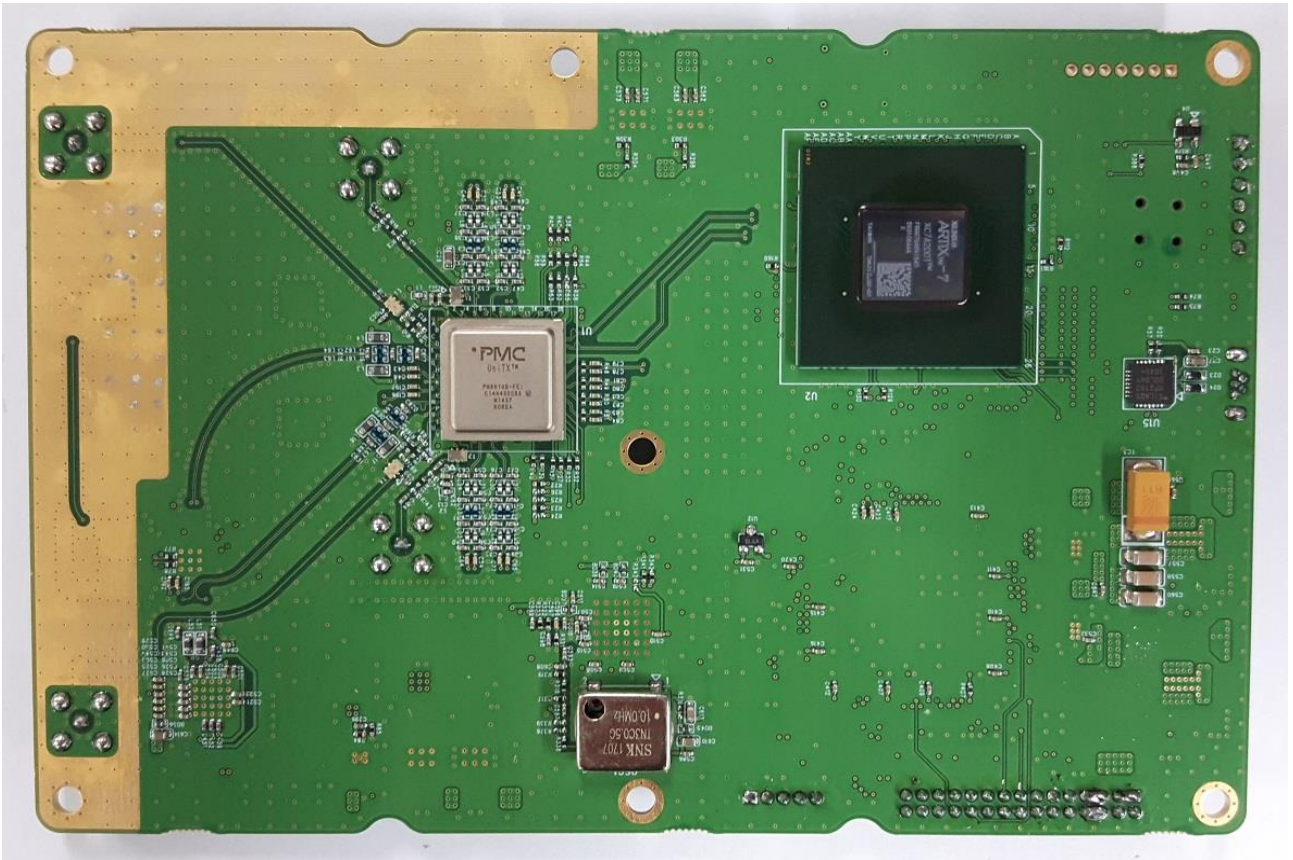




[DFM Top]

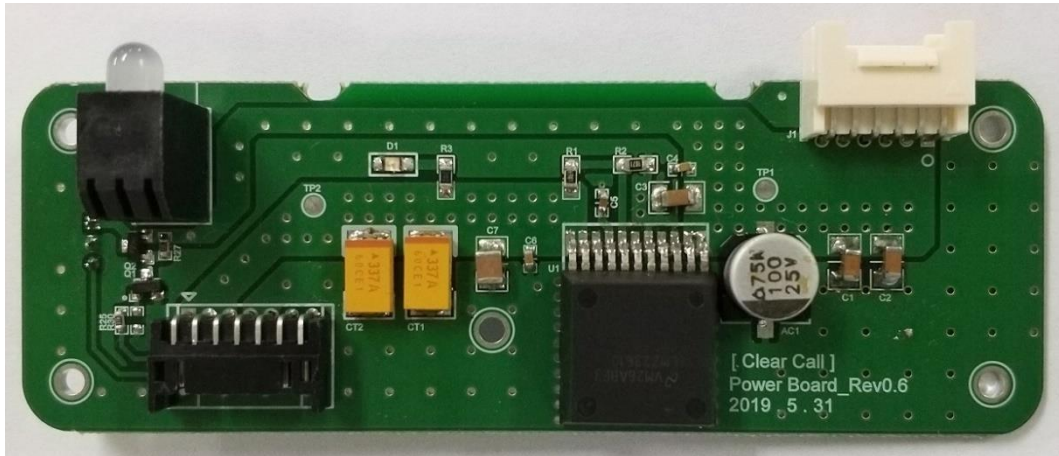


**[DFM Bottom]**

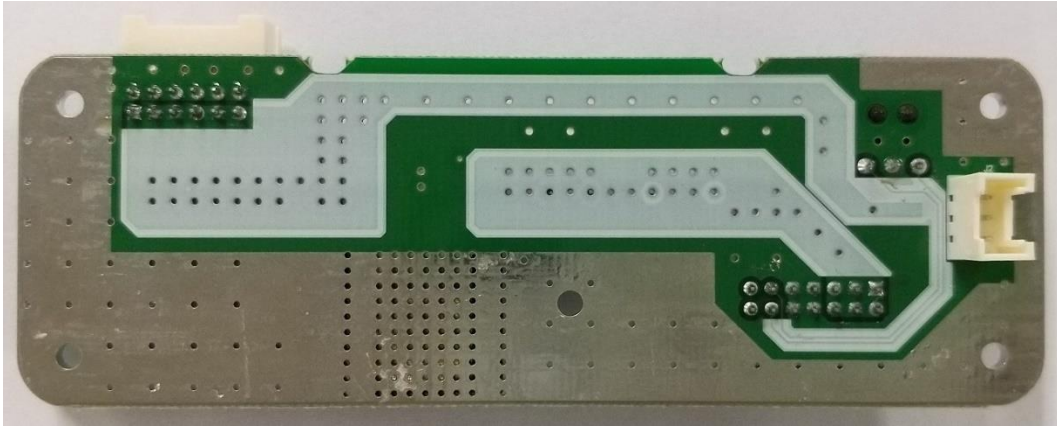




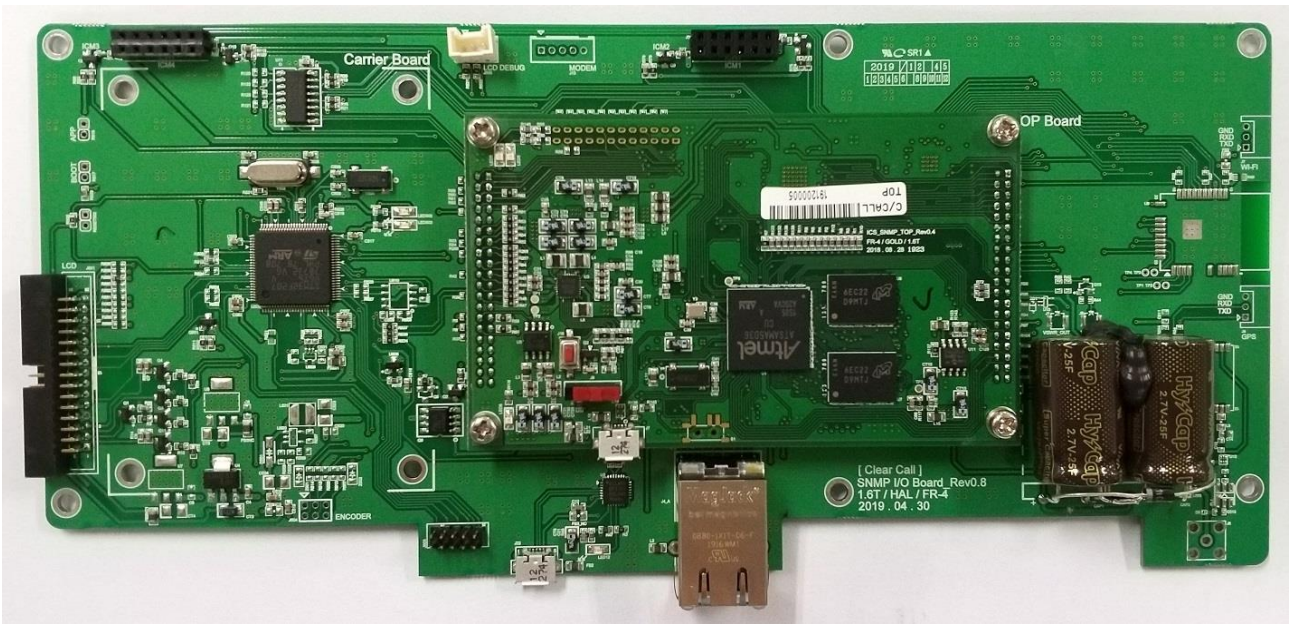
**[Power Top]**



**[Power Bottom]**

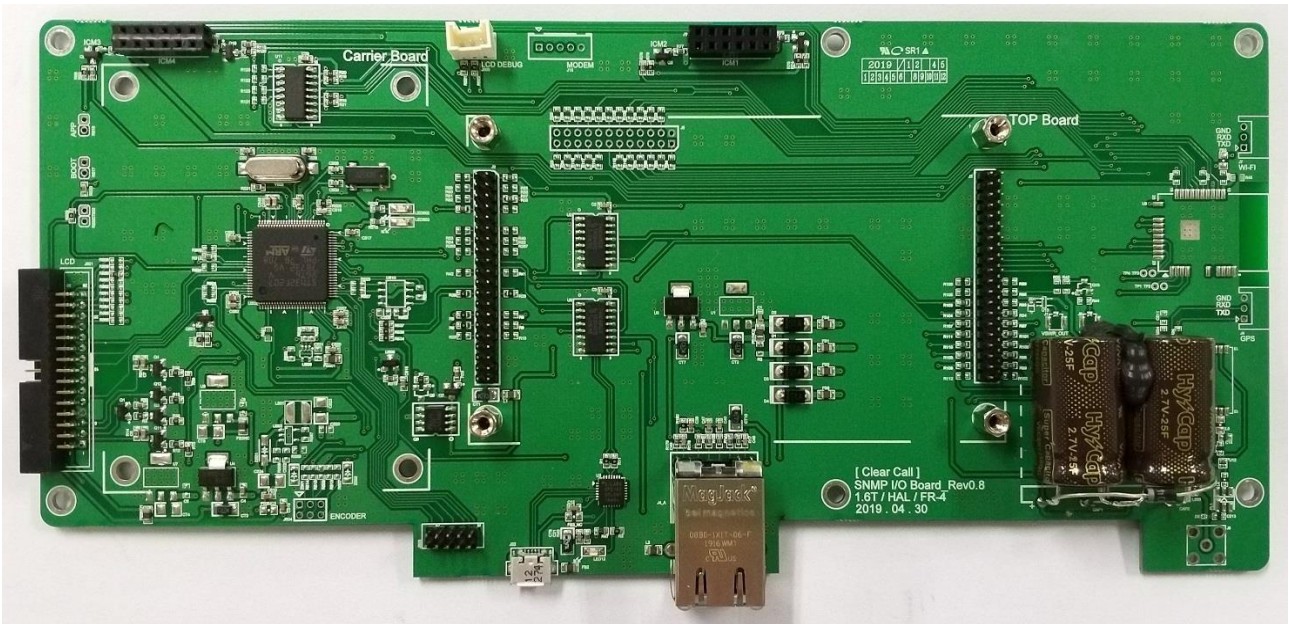


**[SNMP IO Top\_1]**

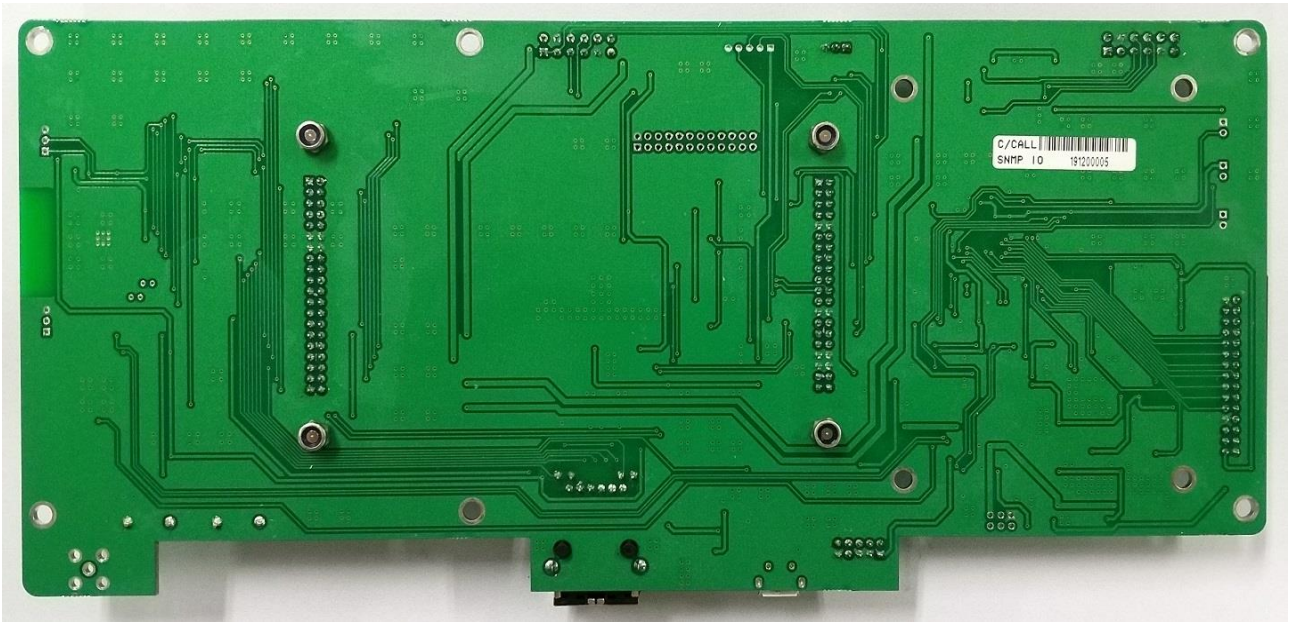




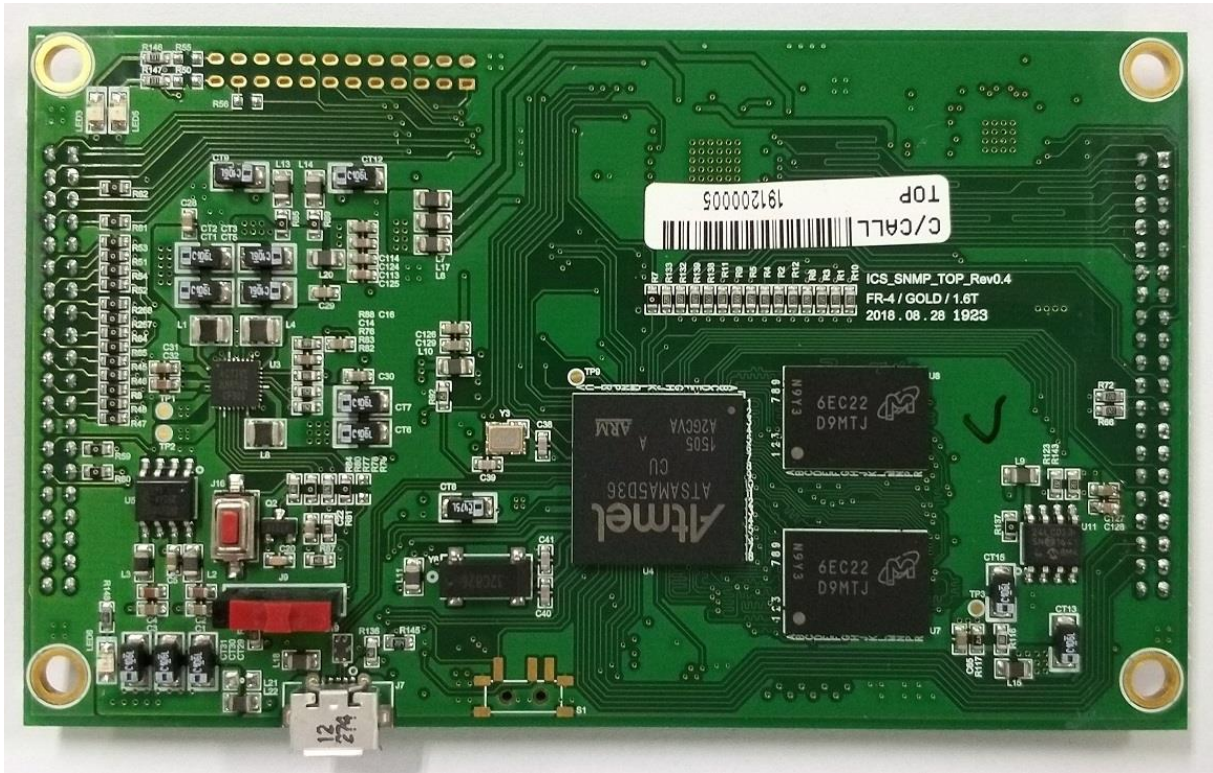
**[SNMP IO Top\_2]**



**[SNMP IO Bottom]**

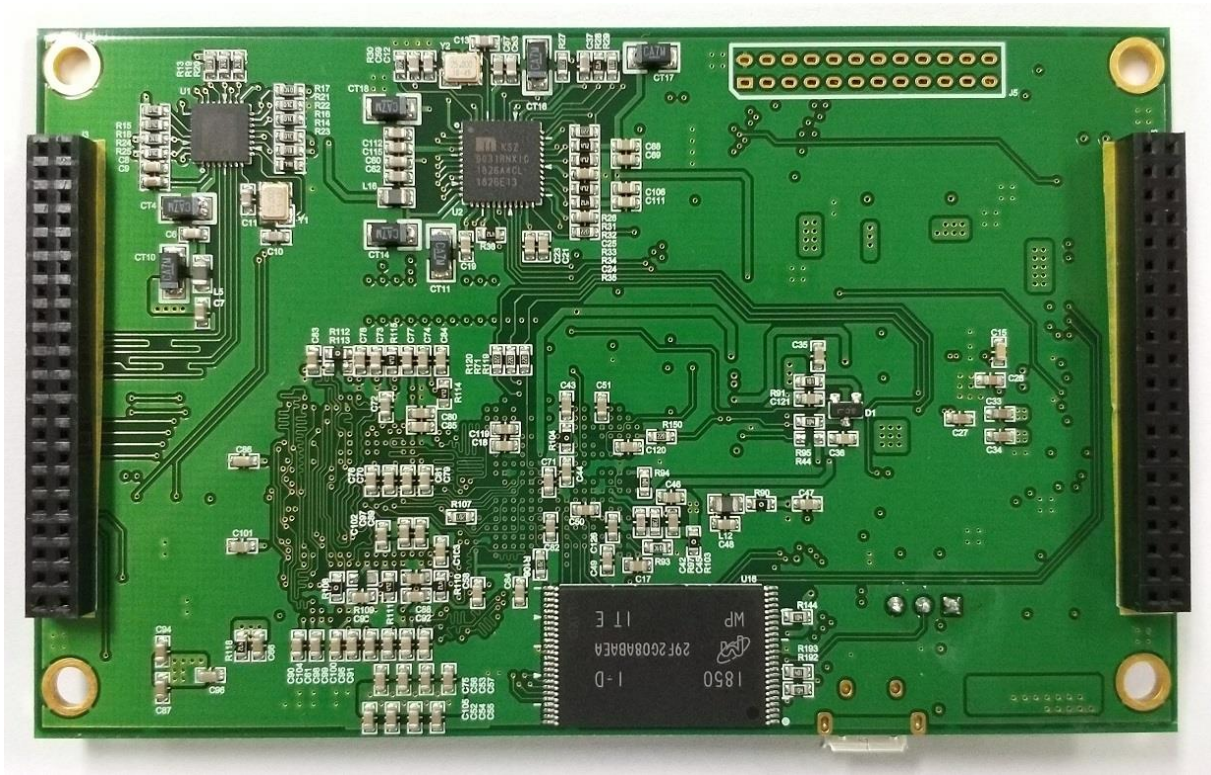


[SNMP Top]

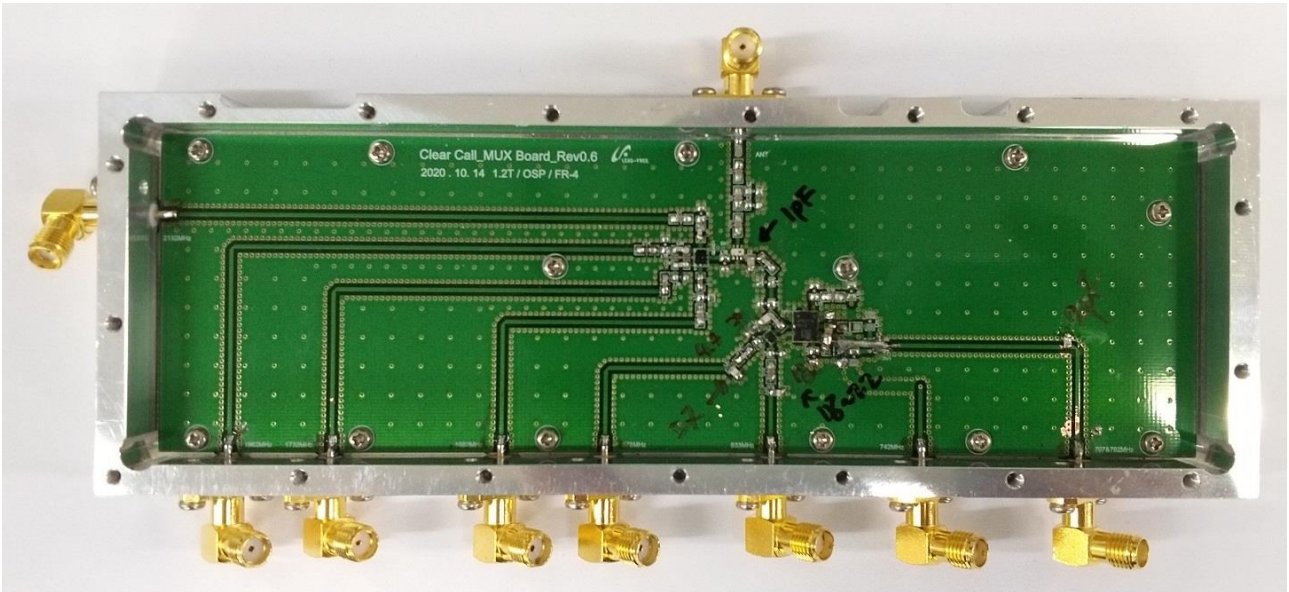




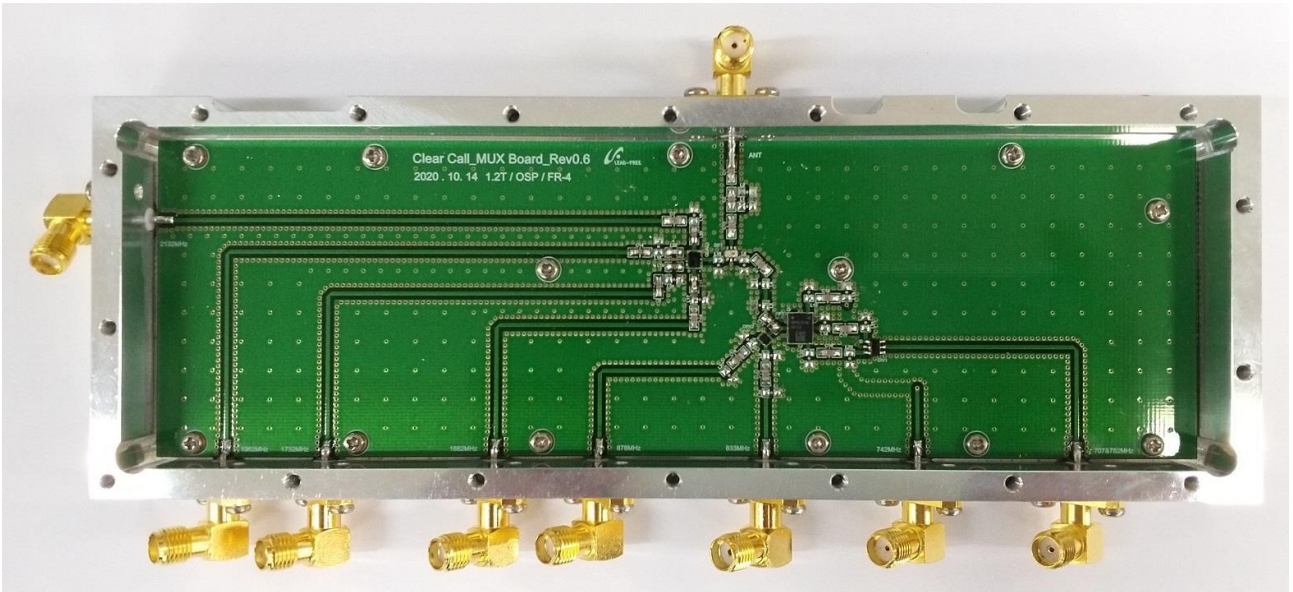
**[SNMP Bottom]**



**[MUX Donor]**



**[MUX Service]**

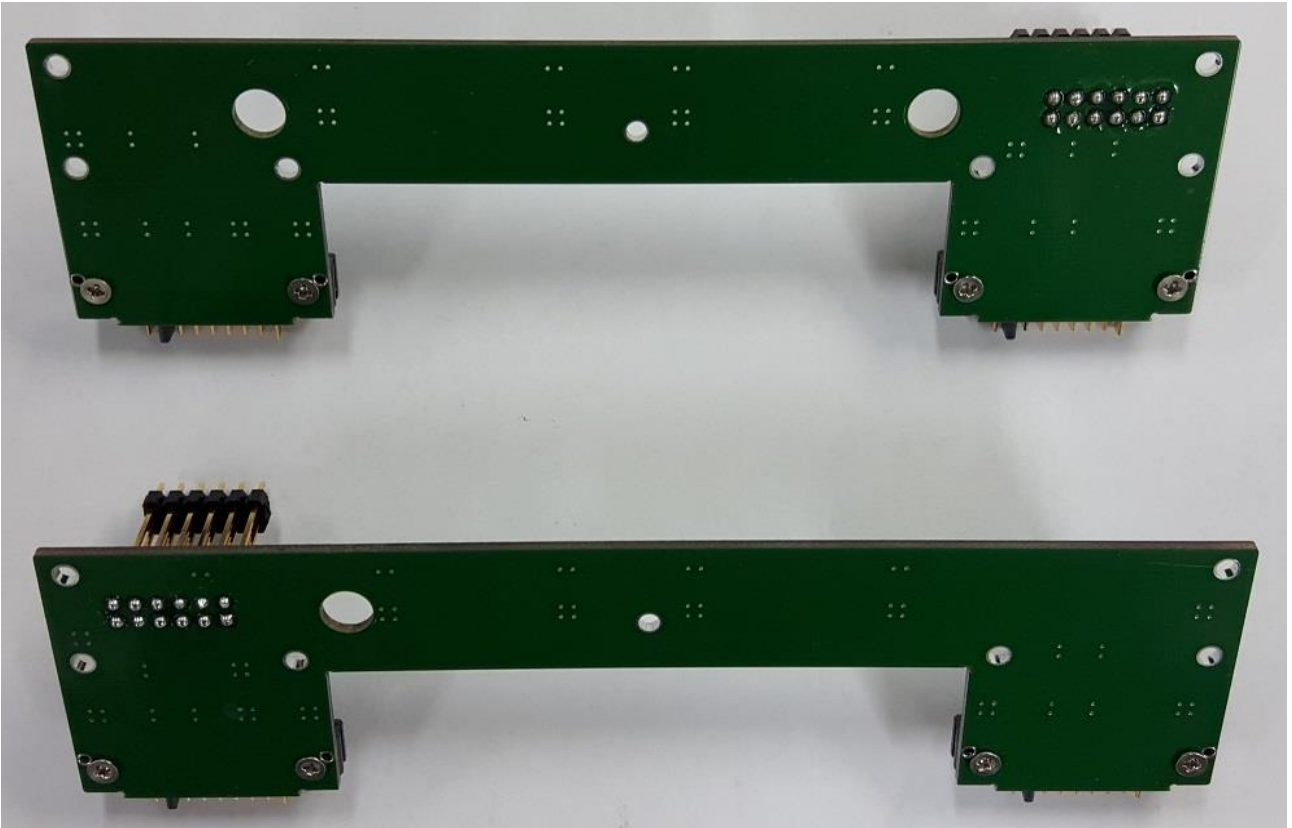




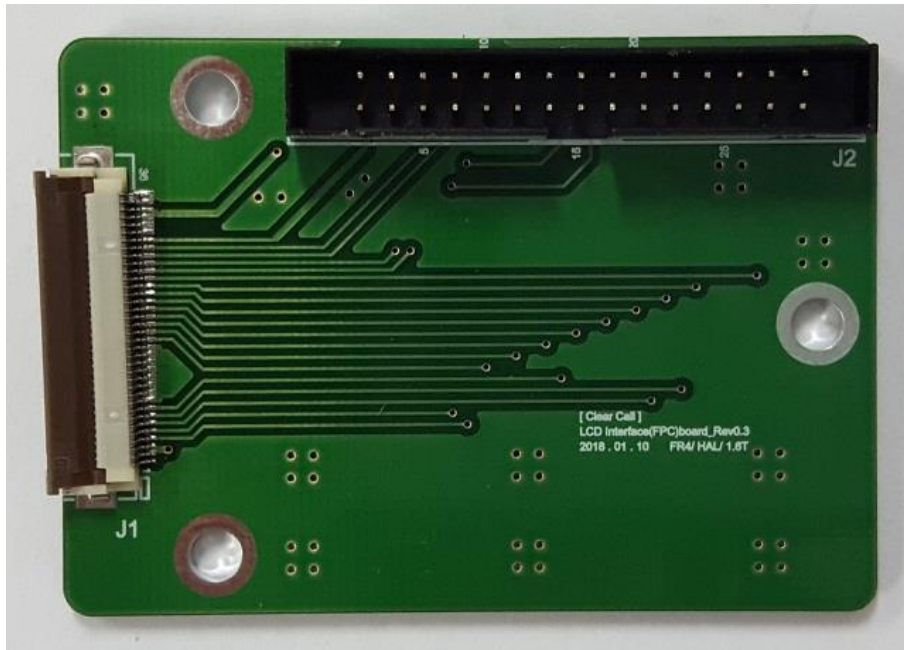
**[MUX Block Top]**



**[MUX Block Bottom]**

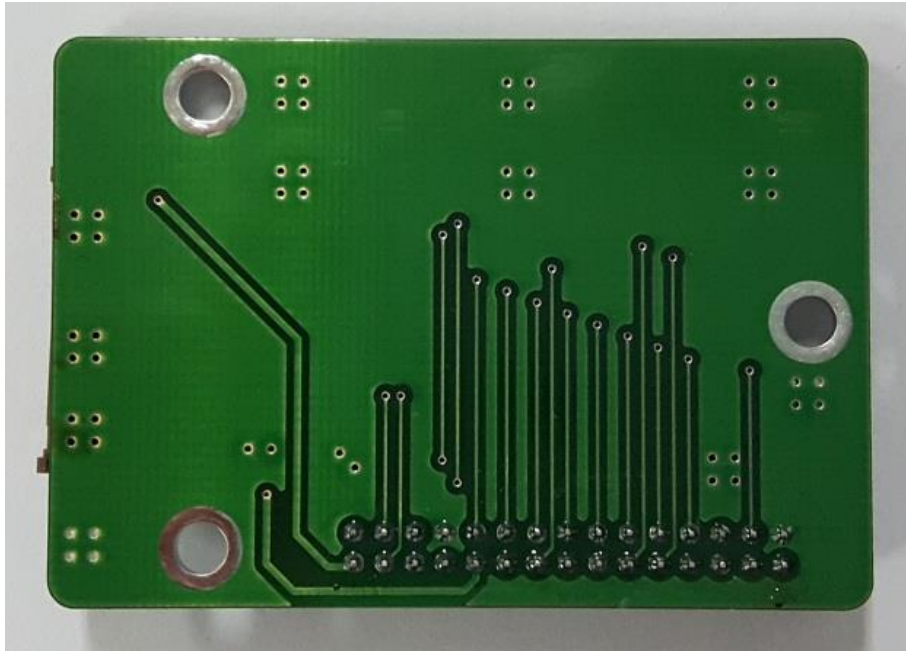


## [LCD Interface Top]

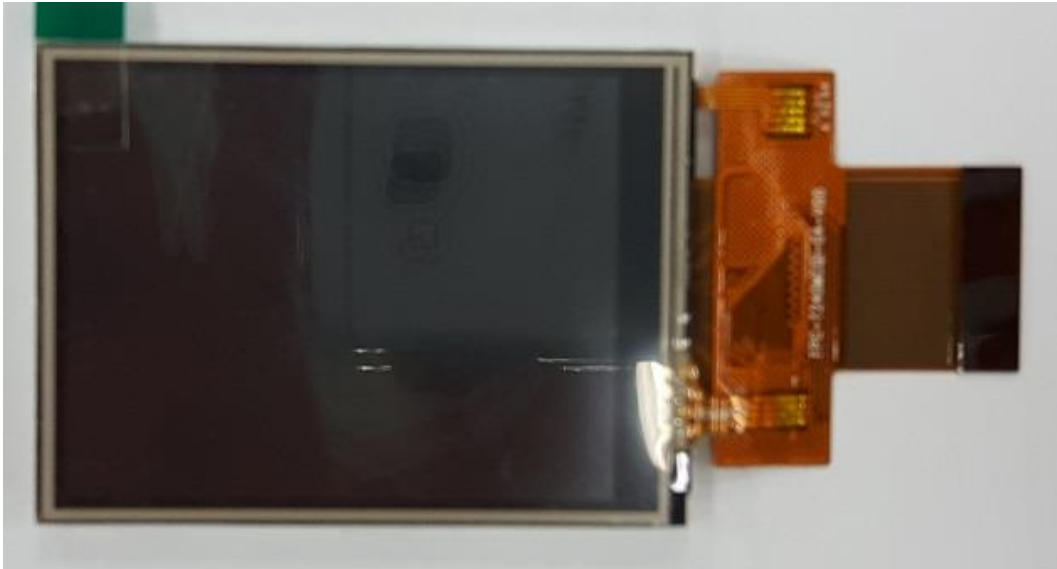




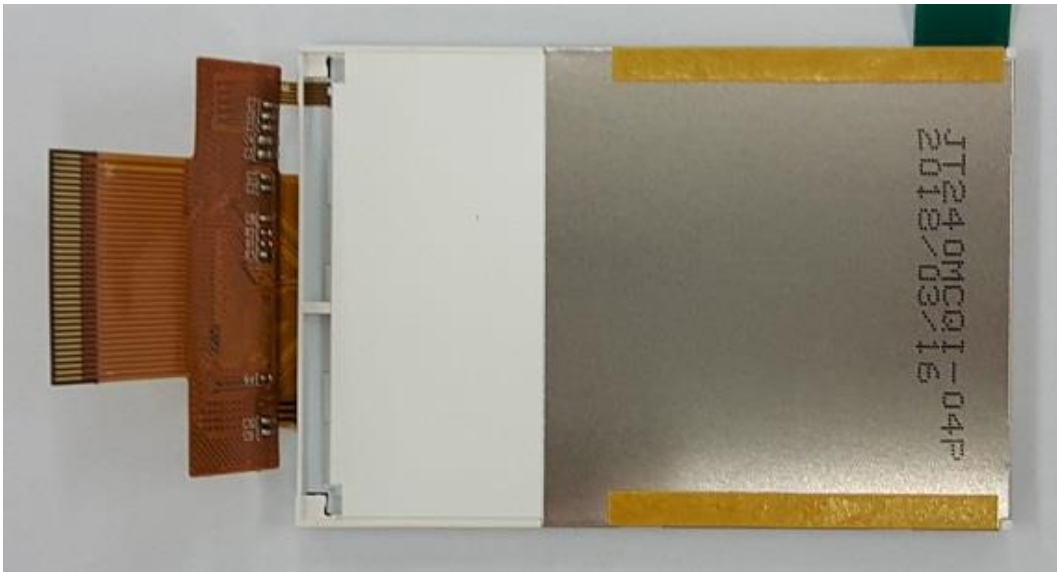
**[LCD Interface Bottom]**



**[LCD Front]**



**[LCD Rear]**





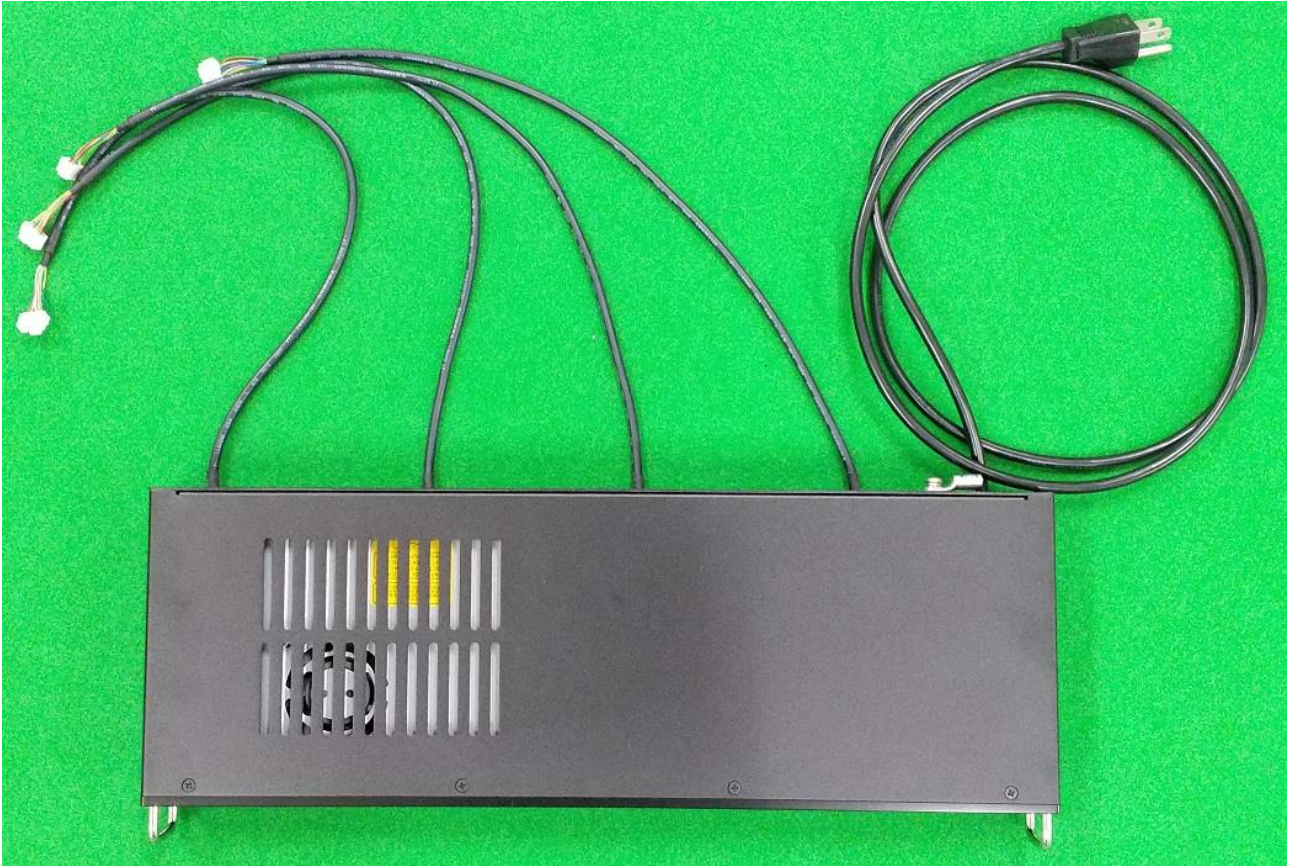
**[PSU Front]**



**[PSU Rear]**

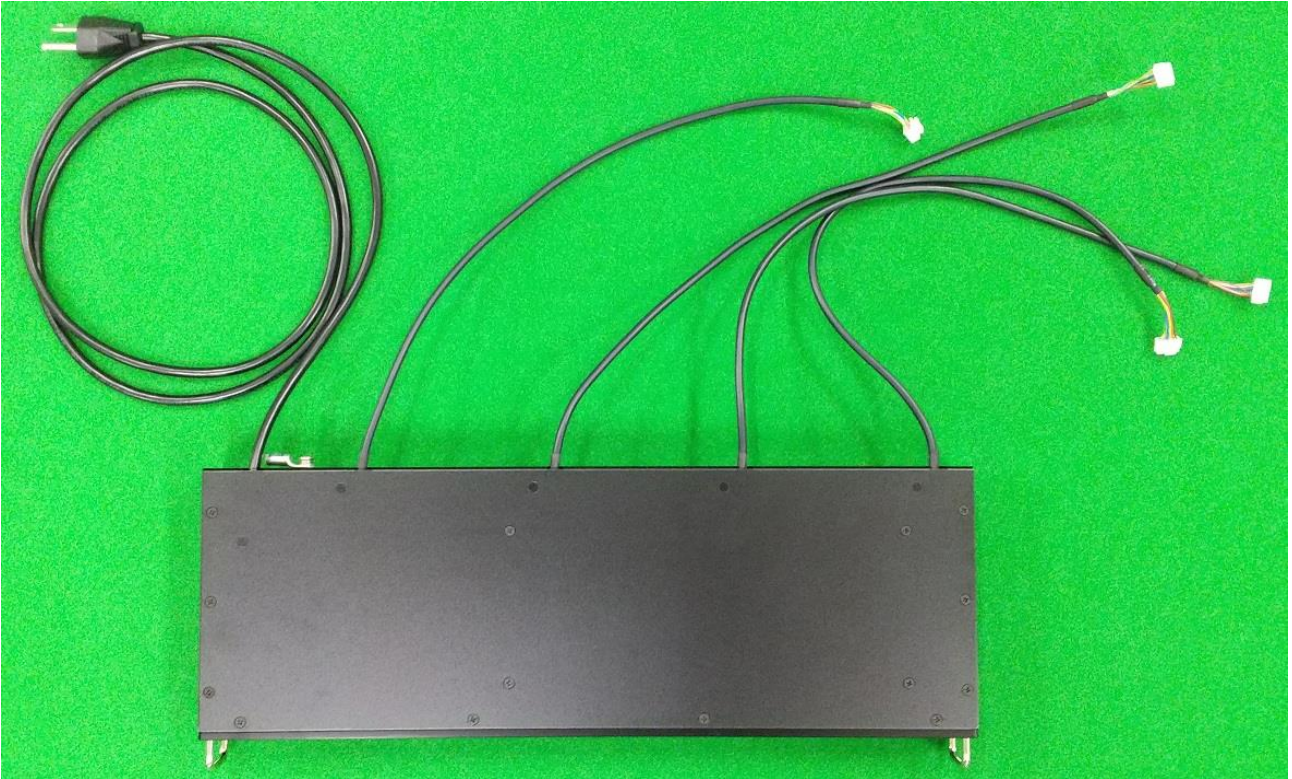


**[PSU Top]**





**[PSU Bottom]**



**[PSU Left]**



**[PSU Right]**





**[PSU Internal]**

