

5.5x MVT Board Schematic x2.0

SHEET DESCRIPTION

PAGE 1 - COVER SHEET

PAGE 2 - MCP (ADDR/DATA/SDRAM/NAND/AD/Timer) Block

PAGE 3 - MCP (Power supply) Block

PAGE 4 - TFT LCD

PAGE 5 - Sound codec

PAGE 6 - Key / IR / Learning

PAGE 7 - I2C / ZIGBEE / JTAG

PAGE 8 - Key Backlight

PAGE 9 - PMIC

PAGE 10 - Power

REFERENCE DESIGNATOR DESCRIPTION:

LAST TWO DIGITS INDICATE REFERENCE DESIGNATOR NUMBER.

FIRST ONE OR TWO DIGITS INDICATE SHEET NUMBER.

LETTER INDICATES COMPONENT TYPE.

C : Ceramic capacitor

CT : Tantalum capacitor

CON : Connector

D : Diode

F : Fuse

FB : Ferrite bead

IR : IR-LED

J : Jump

K : Switch

L : Inductor

LED : LED

MIC : Ceramic MIC.

Q : Transistor

R : Resistor

RP : Resistor pack

TP : Test Pin

U : Active component

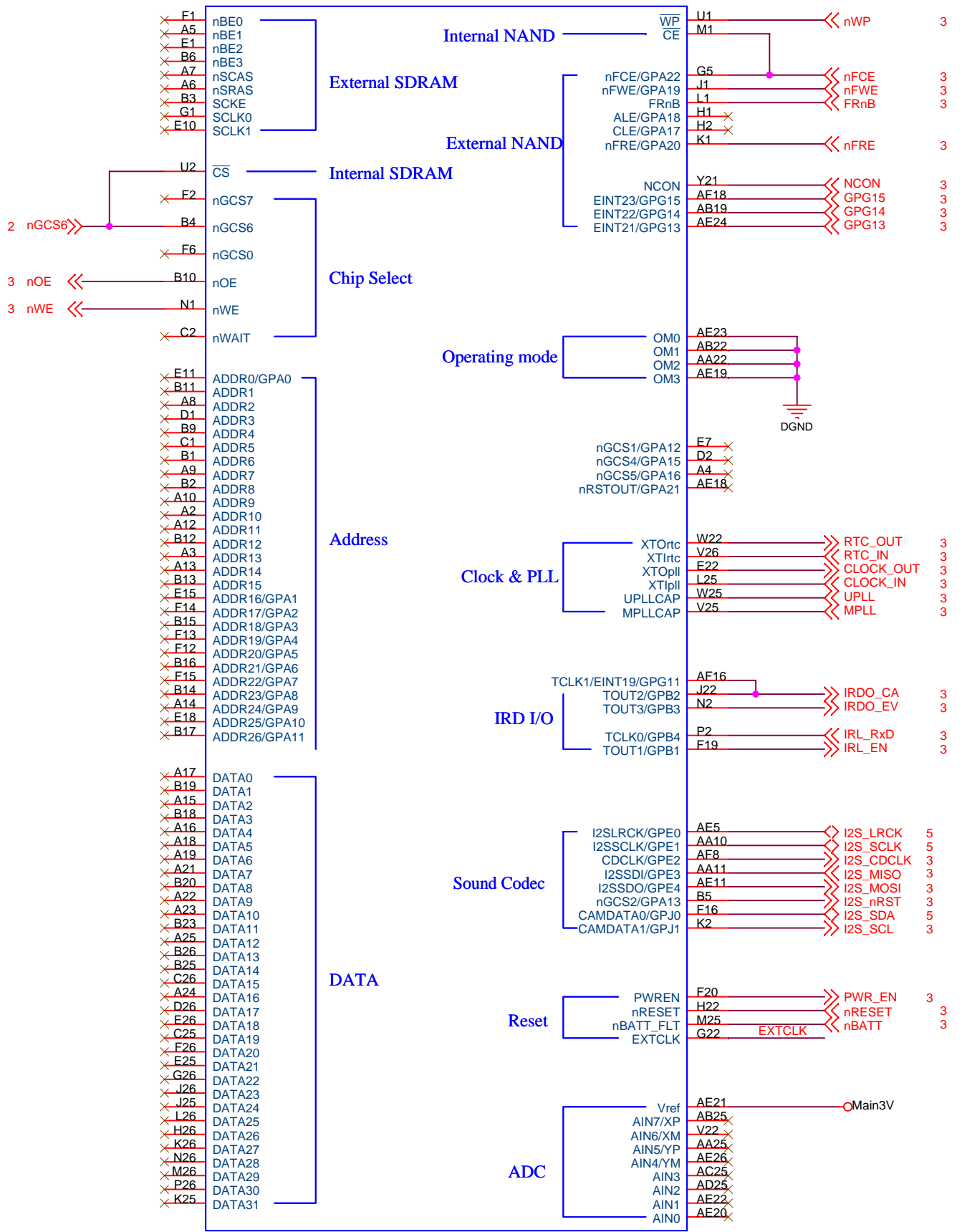
VR : Volume Resistor

Y : Crystal

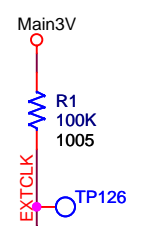
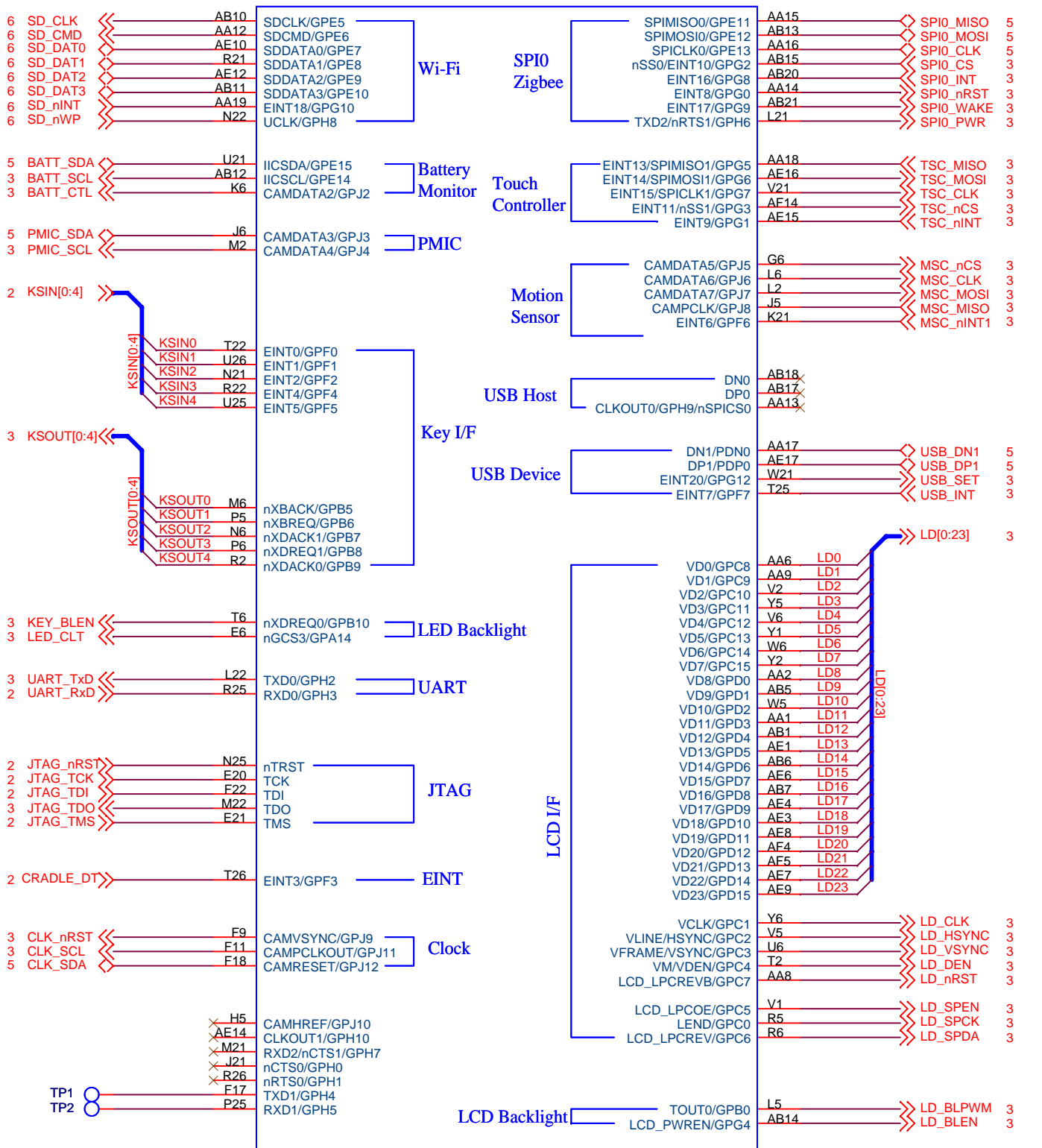
ZD : Zenner Diode

CONTROL STATUS DEFINITION:

U1A S3C2442B43-R1



U1B S3C2442B43-R1

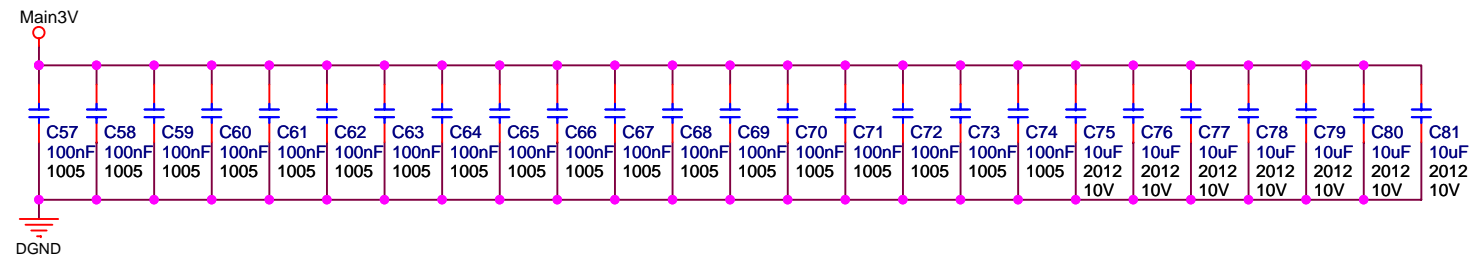
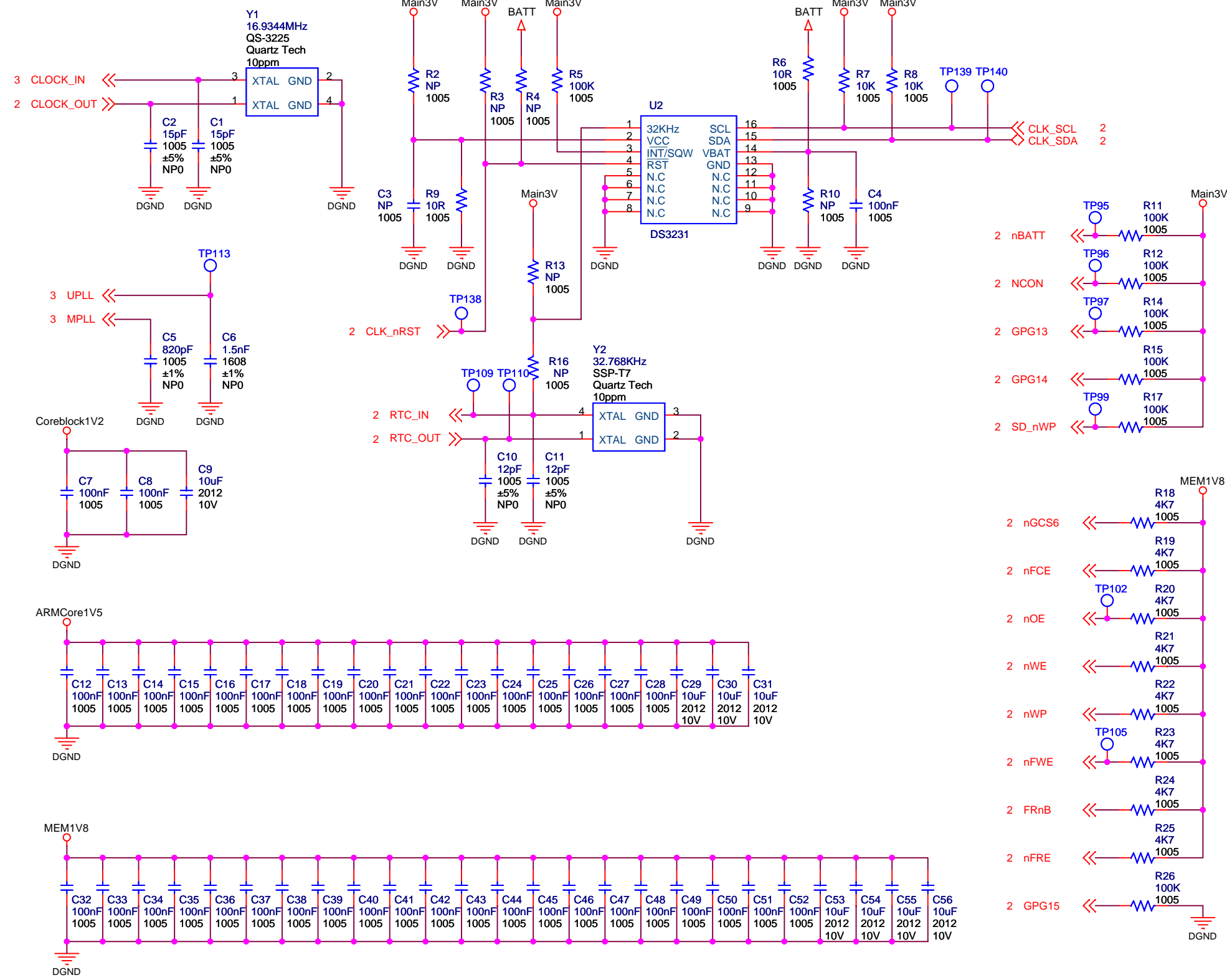
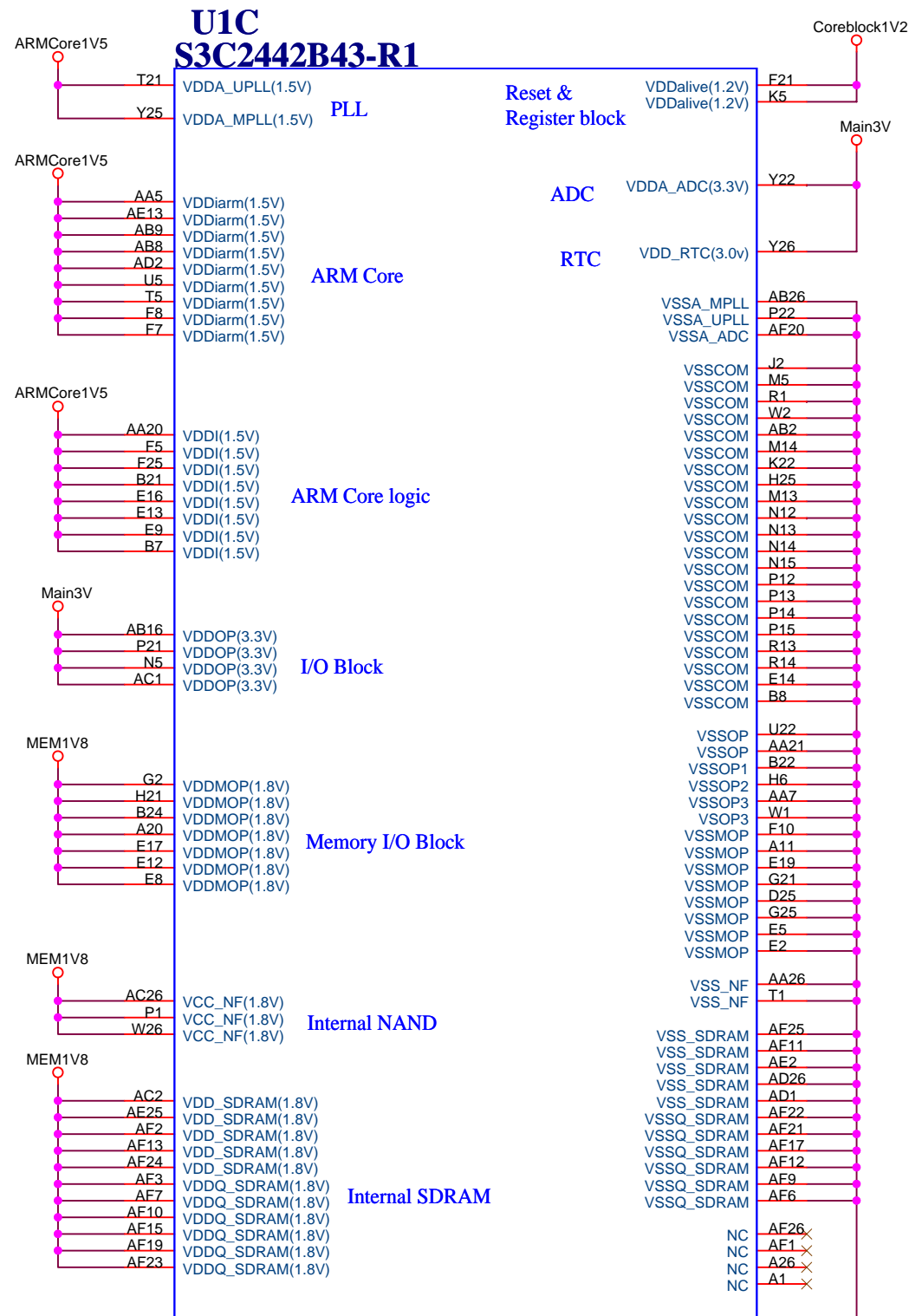


Remote Solution

Title : **5.5x MVT schematic x2.0**

Page Title : **Processor (ADDR/ATA/ADC/CLK/...)**

Size : A3	Drawing By : Ahn young tae	Drawing File Name : 5.5X MVT SCHEMATIC X2.0.DSN	Rev : x2.0
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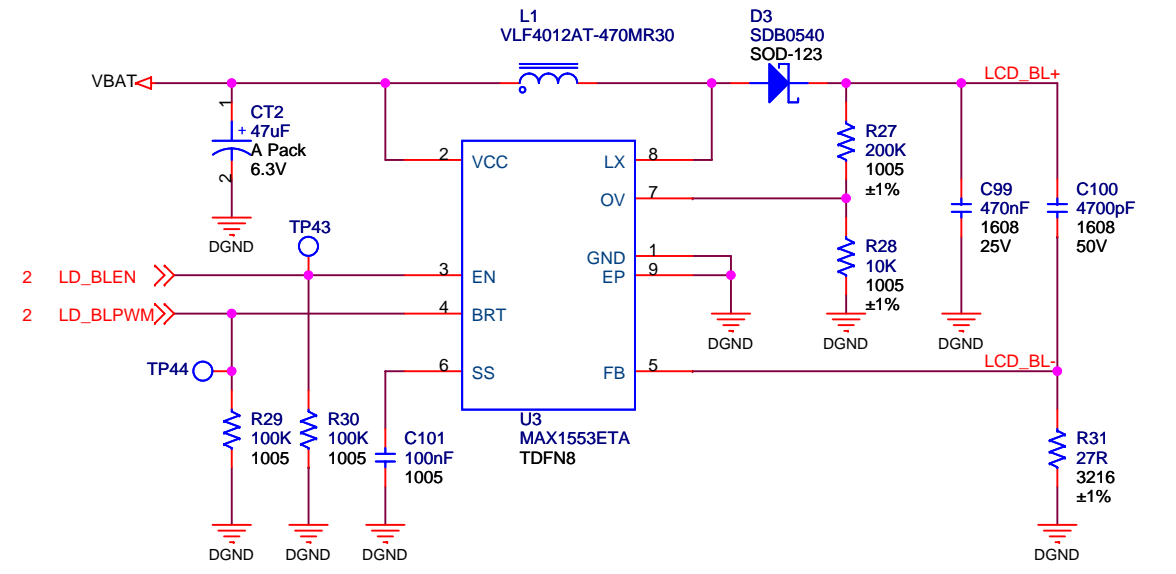
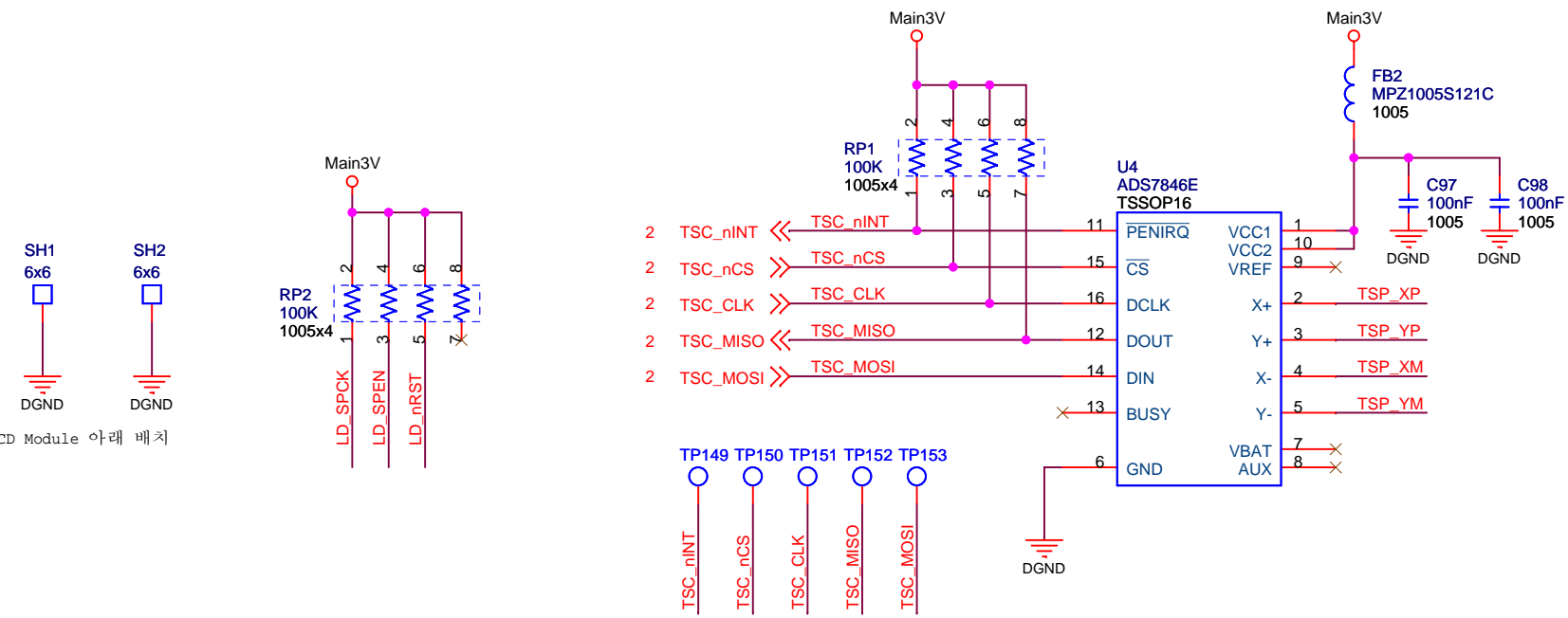
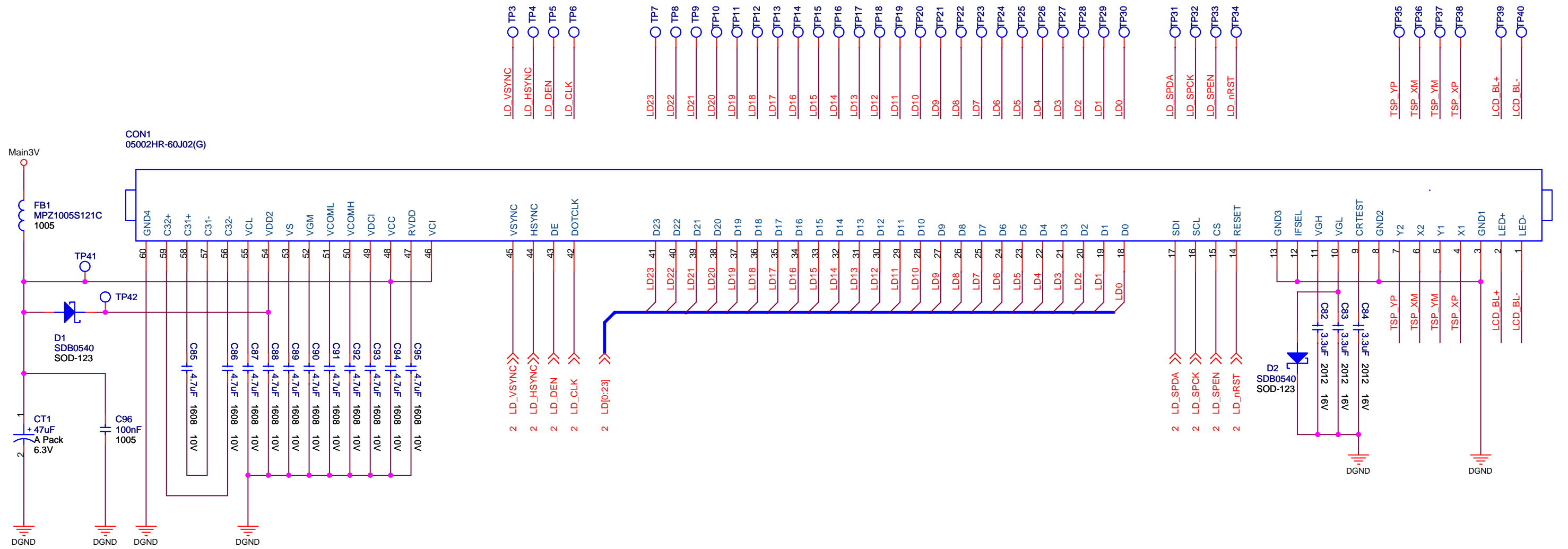


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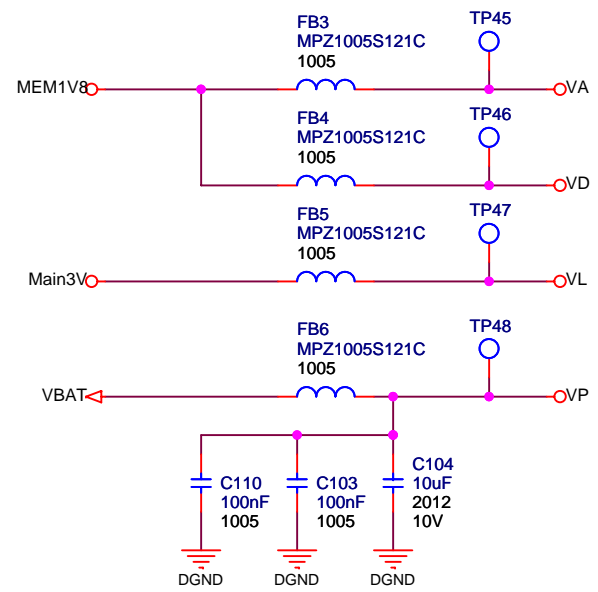
Title : **5.5x MVT schematic x2.0**

Page Title : **Processor (Power)**

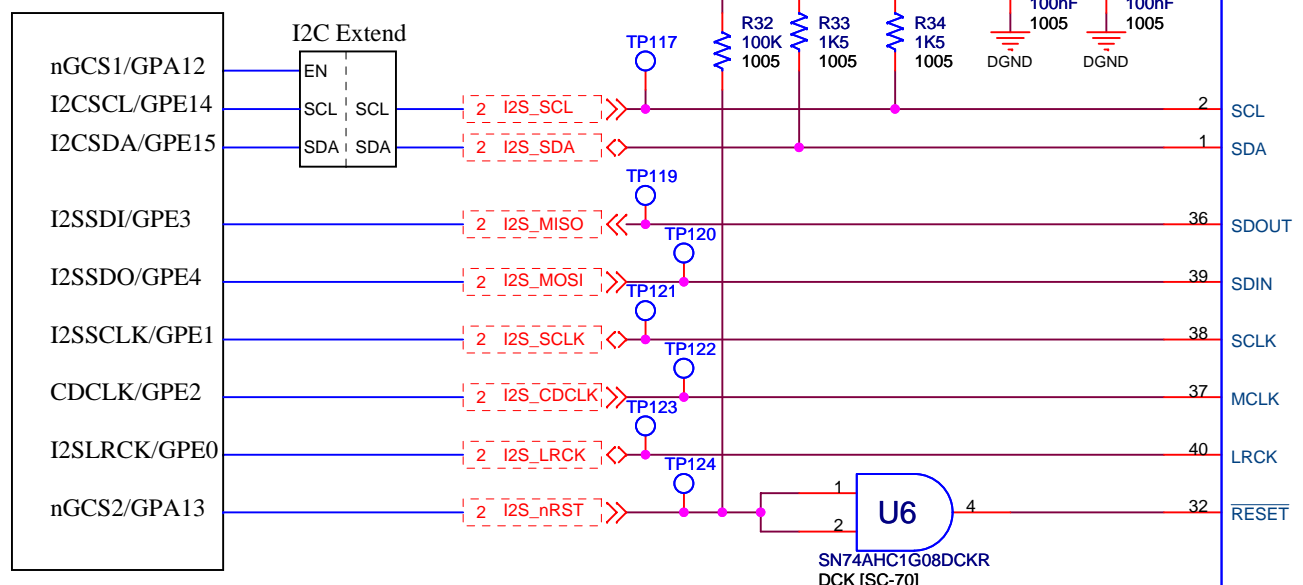
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Page Title : TFT LCD Interface			
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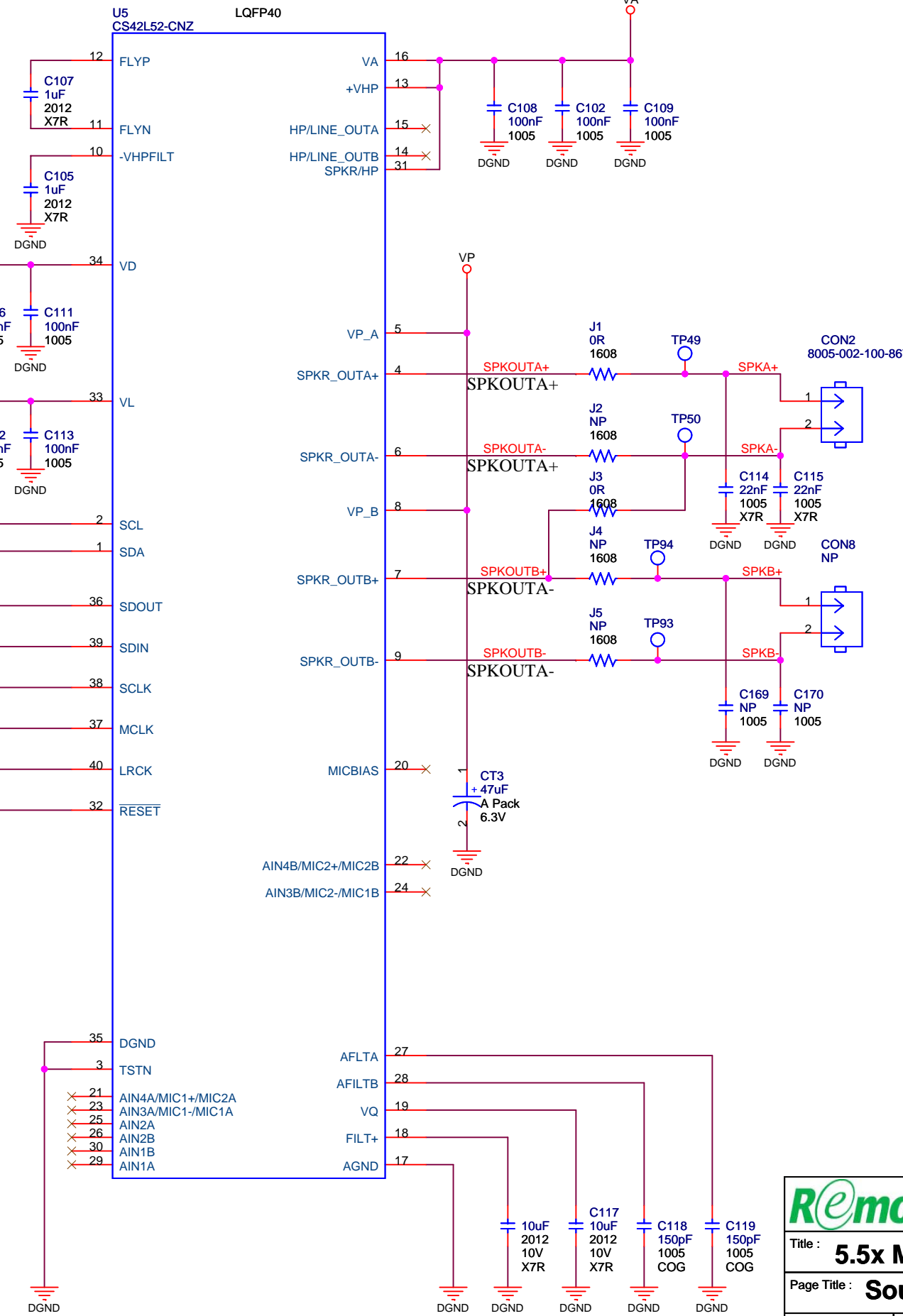


S3C2442A-43



Sound codec power information

- VL : Digital interface section power - 1.65V~3.47V
- VD : Internal digital section power - 1.65V~2.63V
- VA : Analog section power - 1.65V~2.63V
- +VHP : Headphone amplifier power - 1.65V~2.63V
- VP : Speaker PWM Driver power - 1.6V~5.25V



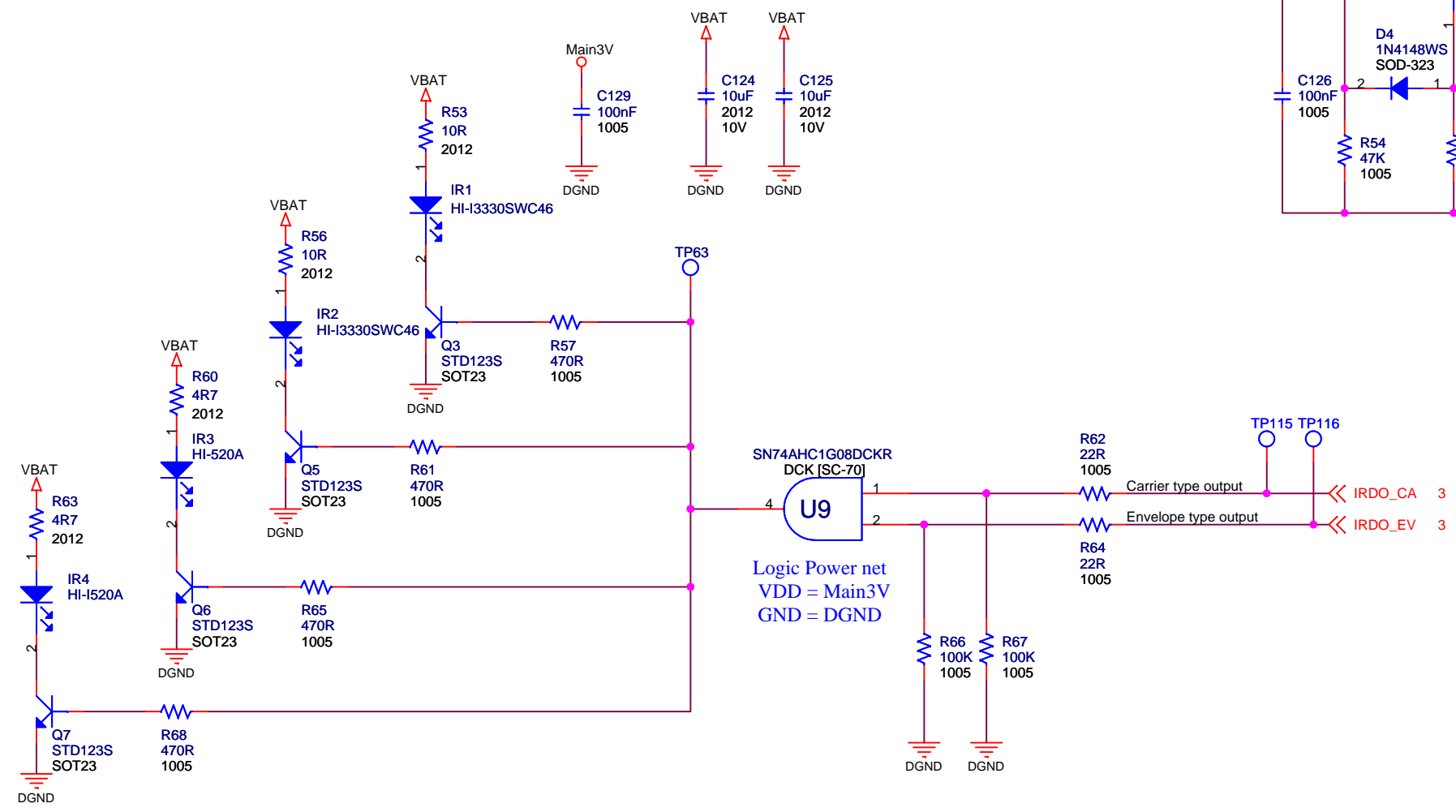
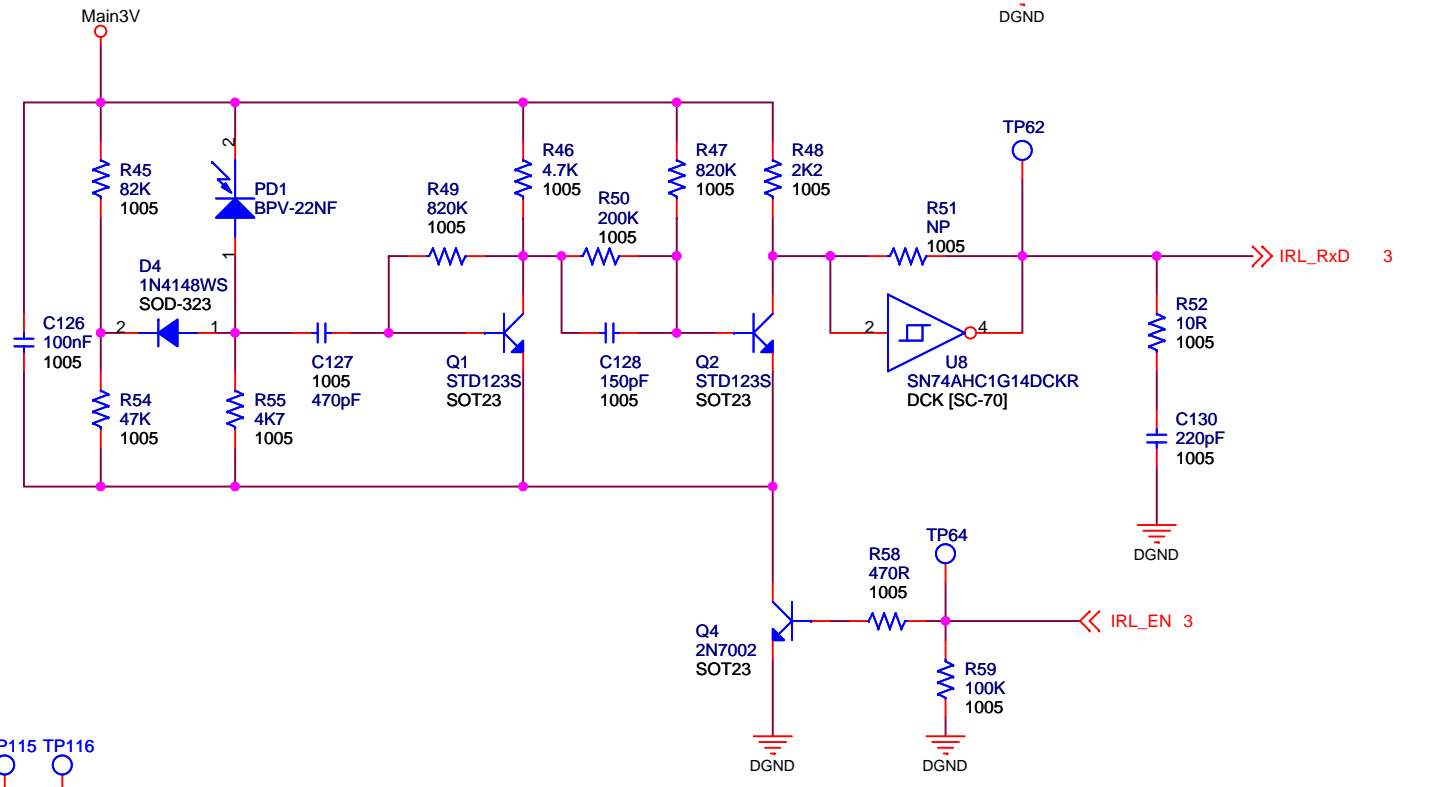
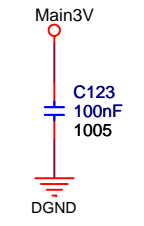
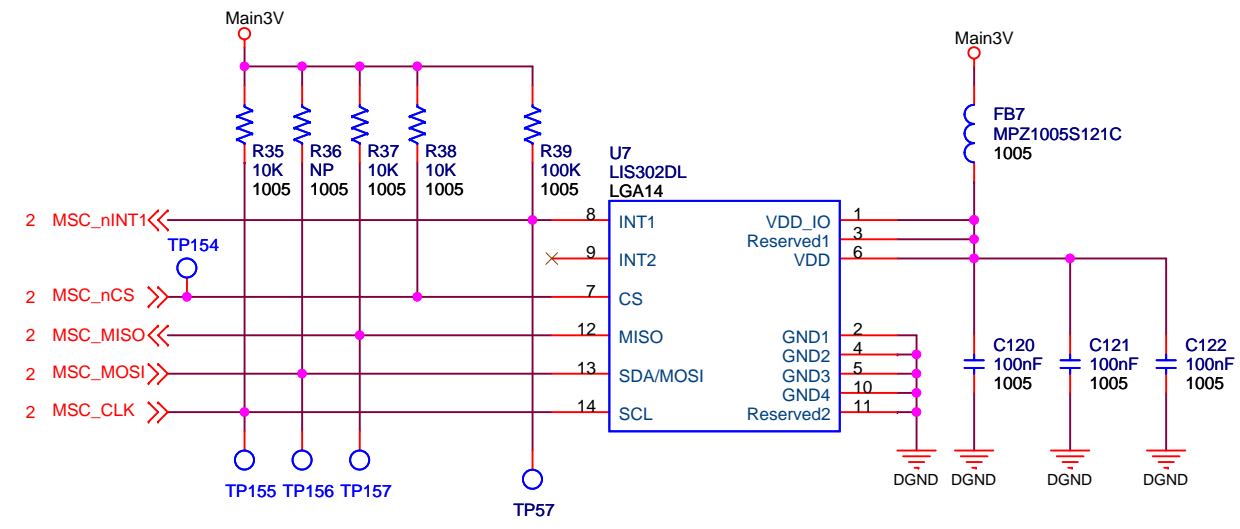
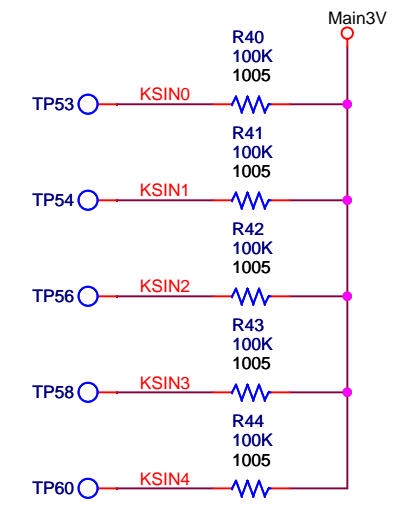
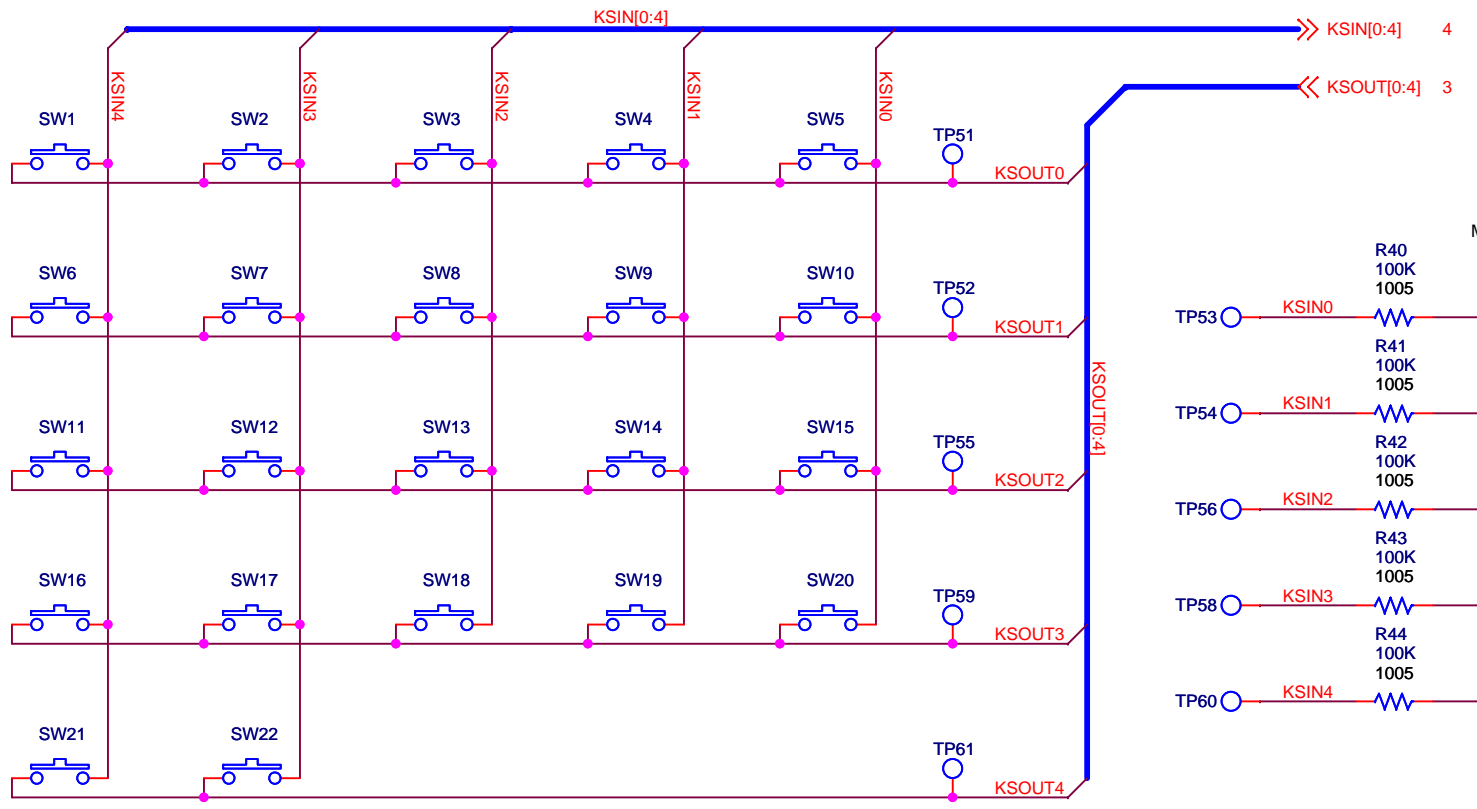
Information

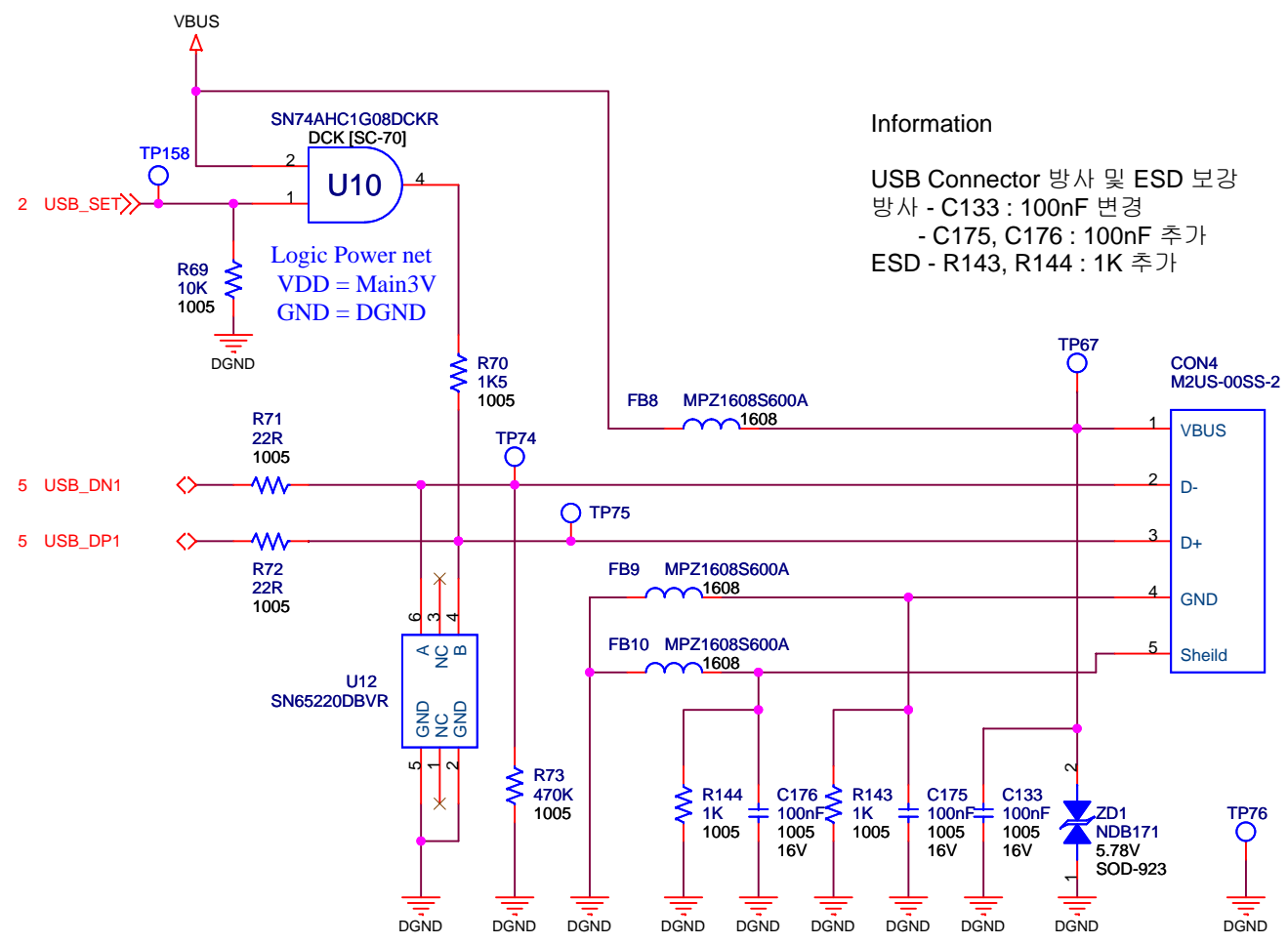
Speaker Output Signal's Overshoot & UnderShoot를 억제하기 위해 C114, C115를 22nF로 변경

Speaker jump option		
2-Ch. Stereo Speaker	J1,J2,J4,J5-ON / J3-OFF	
1-Ch. Mono Speaker	J1,J3-ON / J2,J4,J5-OFF	

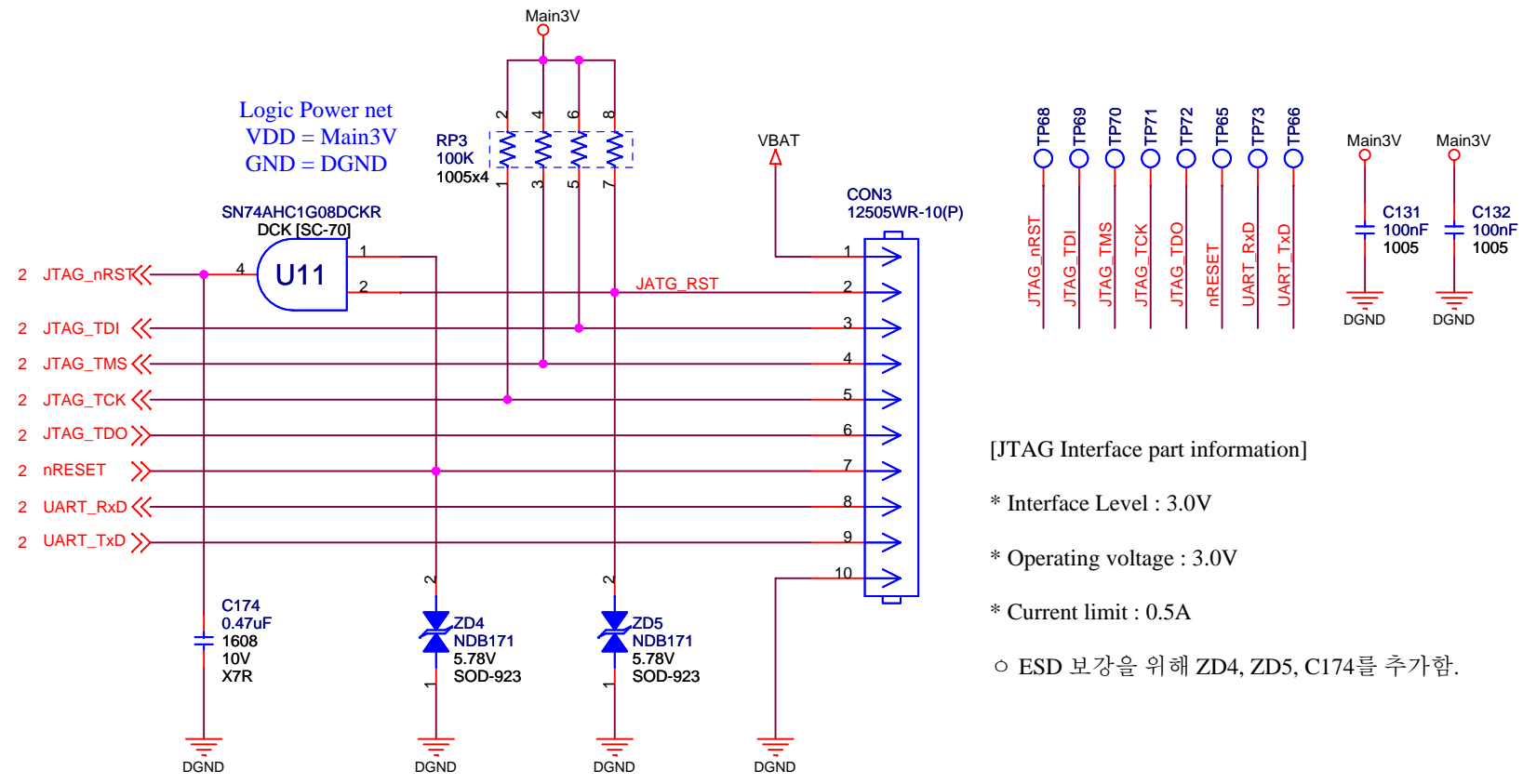
PIN	SPEAKER OUTPUT	
	SPKMONO=0	SPKMONO=1
4	SPKOUTA+	SPKOUTA+
6	SPKOUTA-	SPKOUTA+
7	SPKOUTB+	SPKOUTA-
9	SPKOUTB-	SPKOUTA-





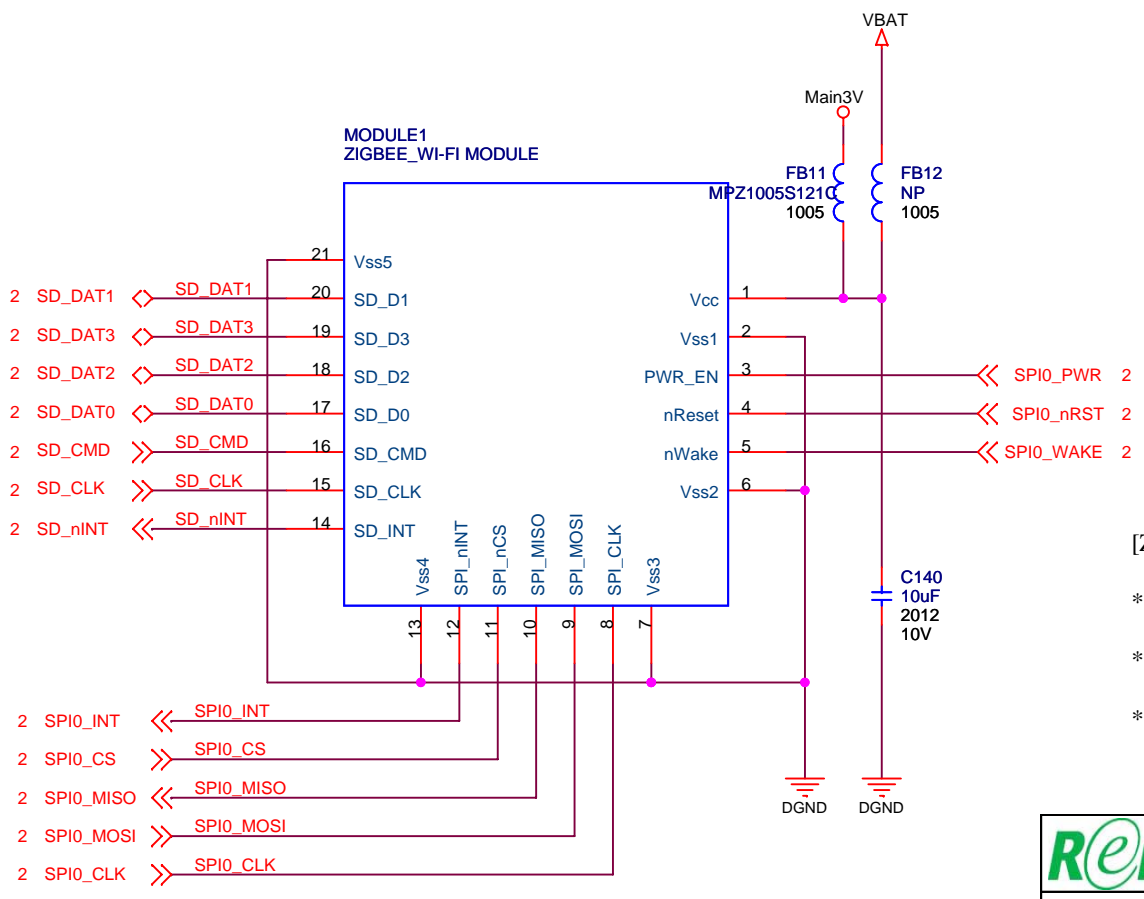
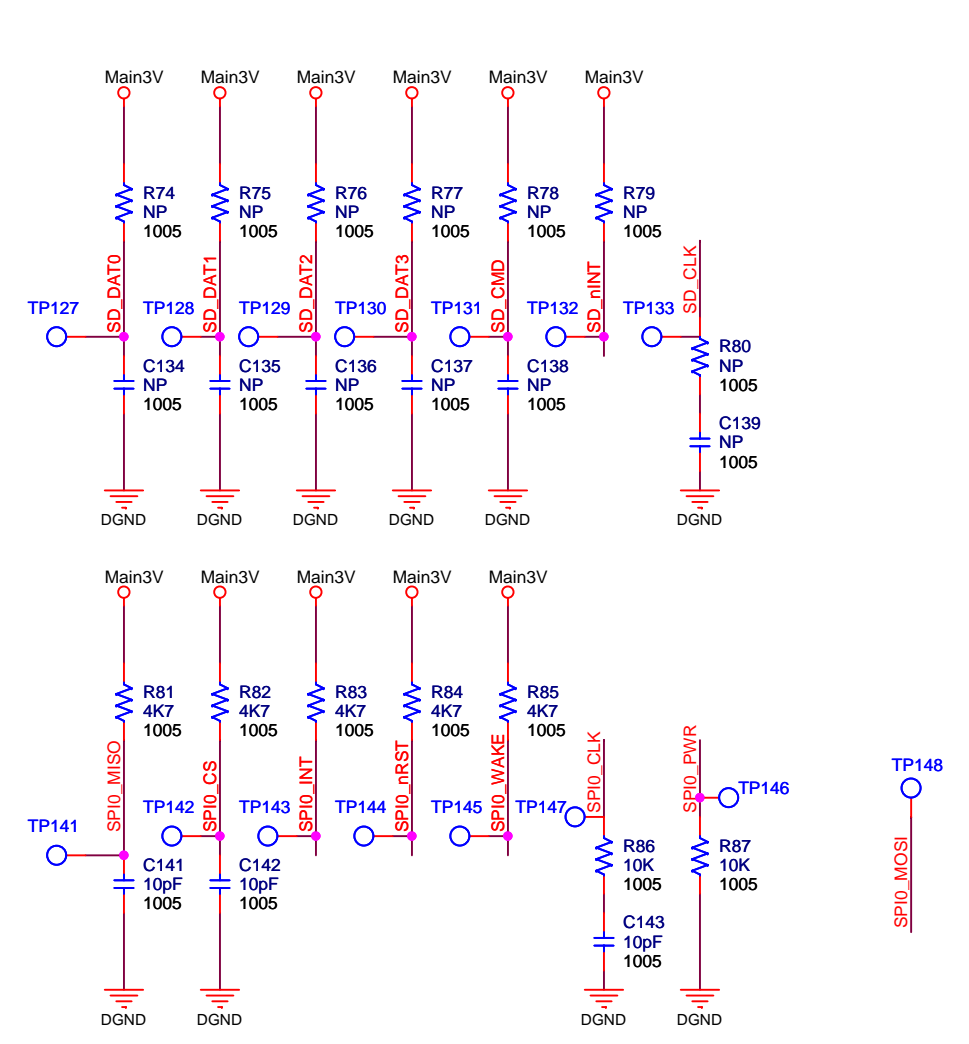


Information
 USB Connector 방사 및 ESD 보강
 방사 - C133 : 100nF 변경
 - C175, C176 : 100nF 추가
 ESD - R143, R144 : 1K 추가



[JTAG Interface part information]
 * Interface Level : 3.0V
 * Operating voltage : 3.0V
 * Current limit : 0.5A
 ○ ESD 보강을 위해 ZD4, ZD5, C174를 추가함.

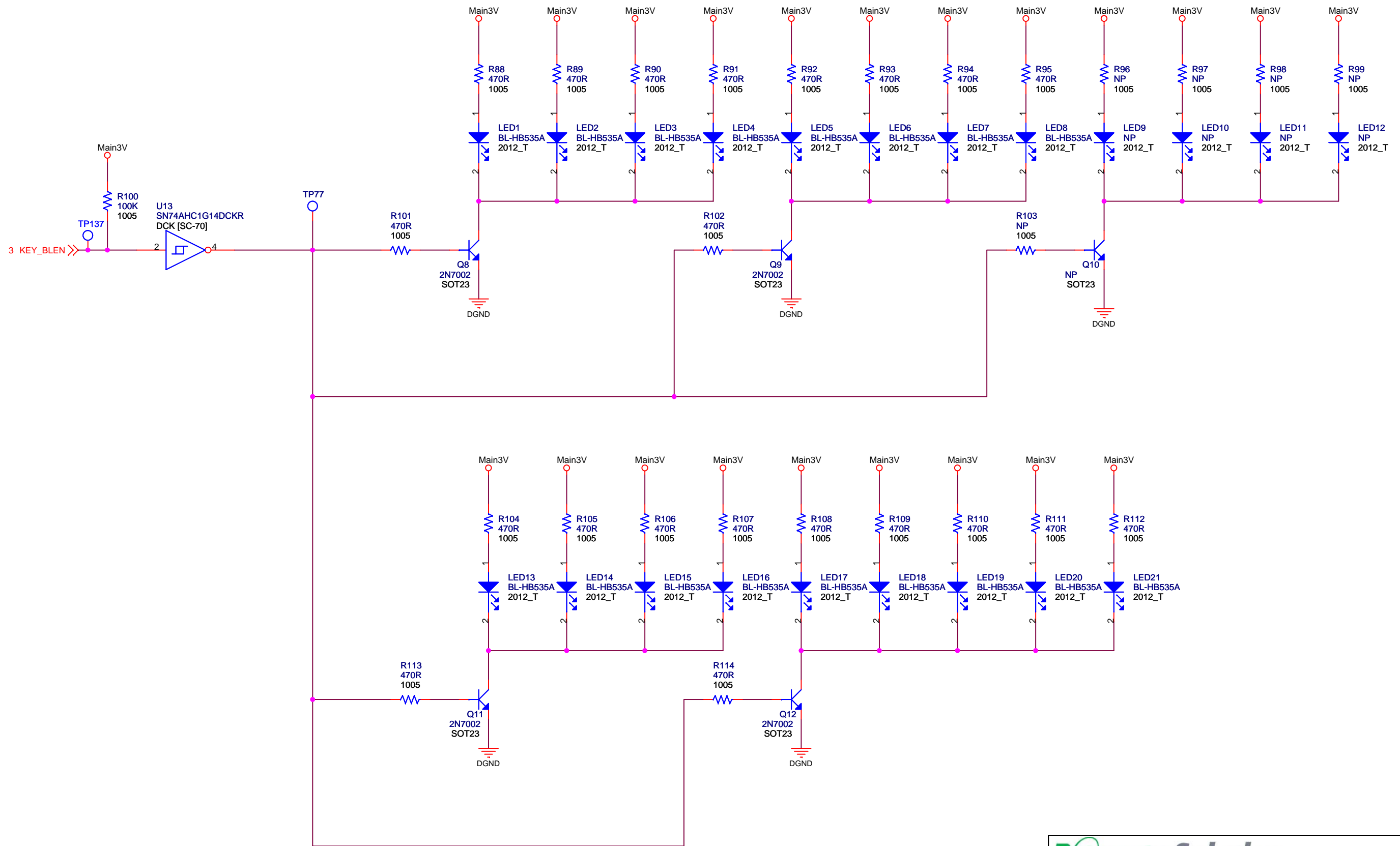
USB 1.1 Device Interface



[ZIGBEE Module Interface part information]
 * Interface Level : 3.0V or 3.3V
 * Operating voltage : 3.0V or 3.3V
 * Current limit : 0.5A



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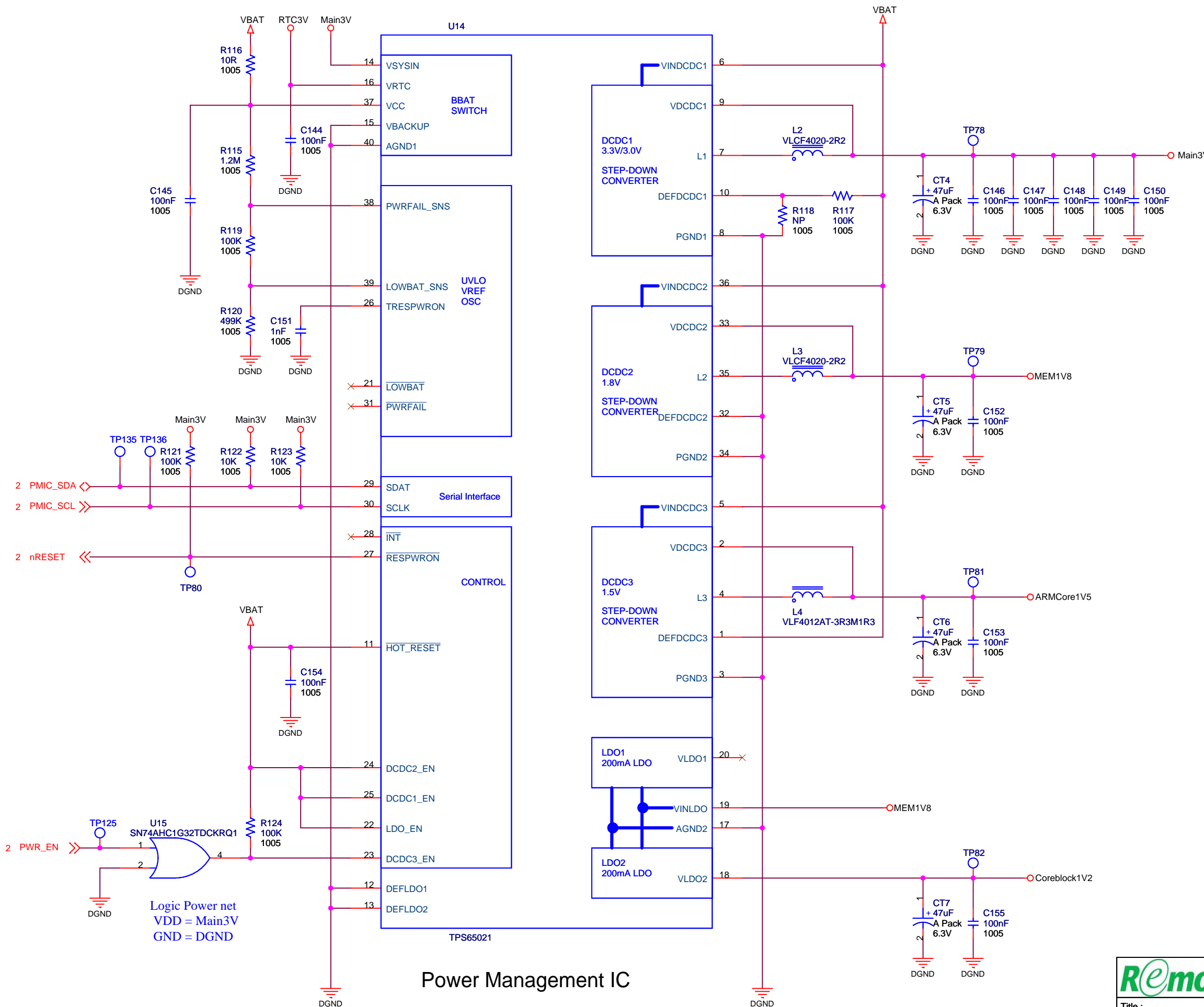


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Page Title : **Key Basklight**

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DCDC1 Step-down converter
 Input voltage range : 2.5V~6V
 Maximum out current : 1.2A
 Current limit :1.55A
 Output voltage : 3.0V/3.3V

DCDC2 Step-down converter
 Input voltage range : 2.5V~6V
 Maximum out current : 1A
 Current limit :1.4A
 Output voltage : 1.8V

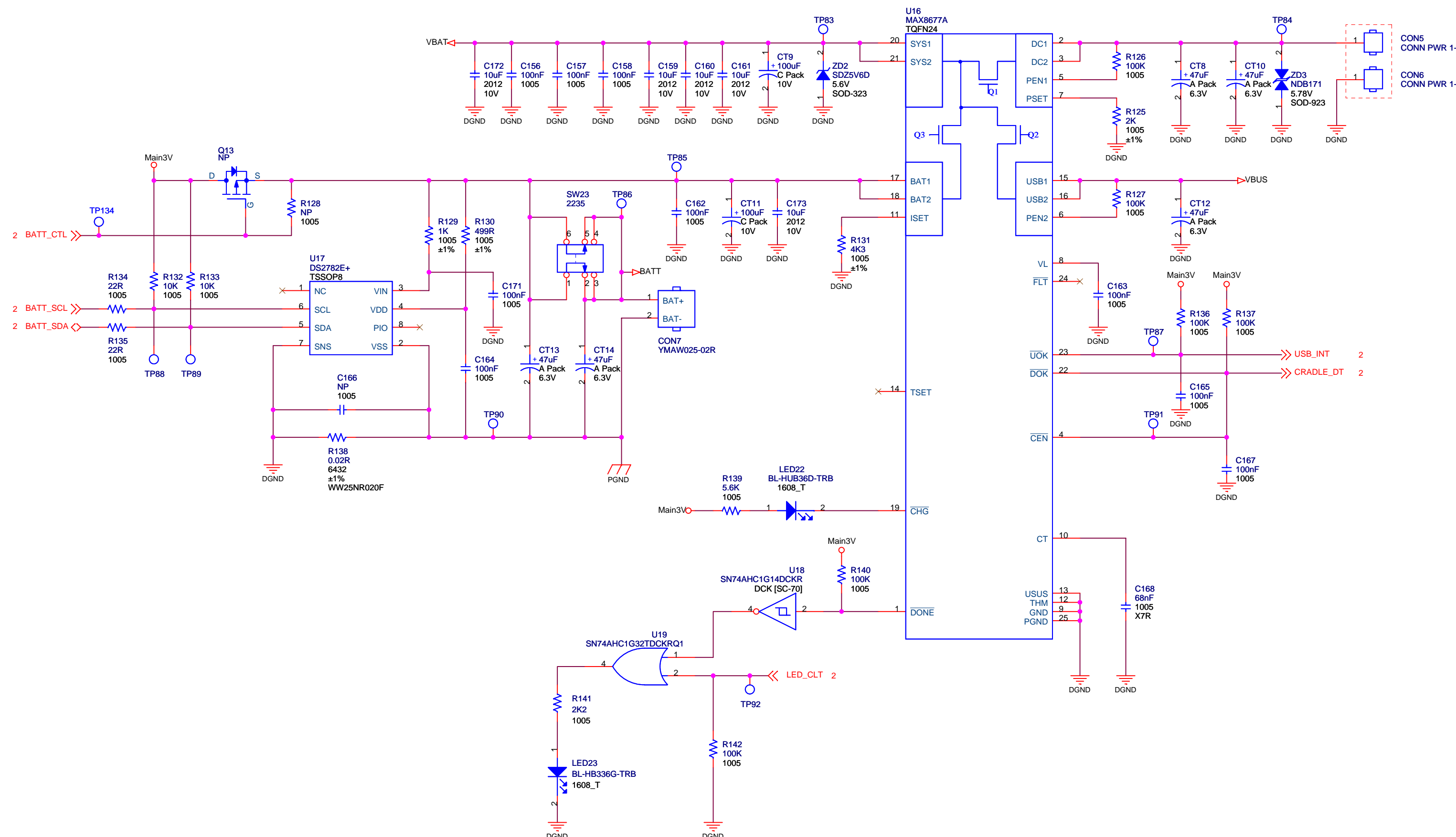
DCDC3 Step-down converter
 Input voltage range : 2.5V~6V
 Maximum out current : 0.9A
 Current limit :1.1A
 Output voltage : 0.8V~1.6V
 Voltage scaling : I2C Interface

LDO1, 2
 Input voltage range : 1.5V~6.5V
 Maximum out current : 0.2A
 Current limit :0.4A
 Maximum voltage drop : 300mV
 Output voltage : 1V~3.3V
 Voltage scaling : I2C Interface

LDO3 (RTC LDO)
 Input voltage range : 2.73V~3.75V
 Maximum out current : 30mA
 Current limit : 100mA
 Output voltage : 3.0V ± 1%

Power Management IC

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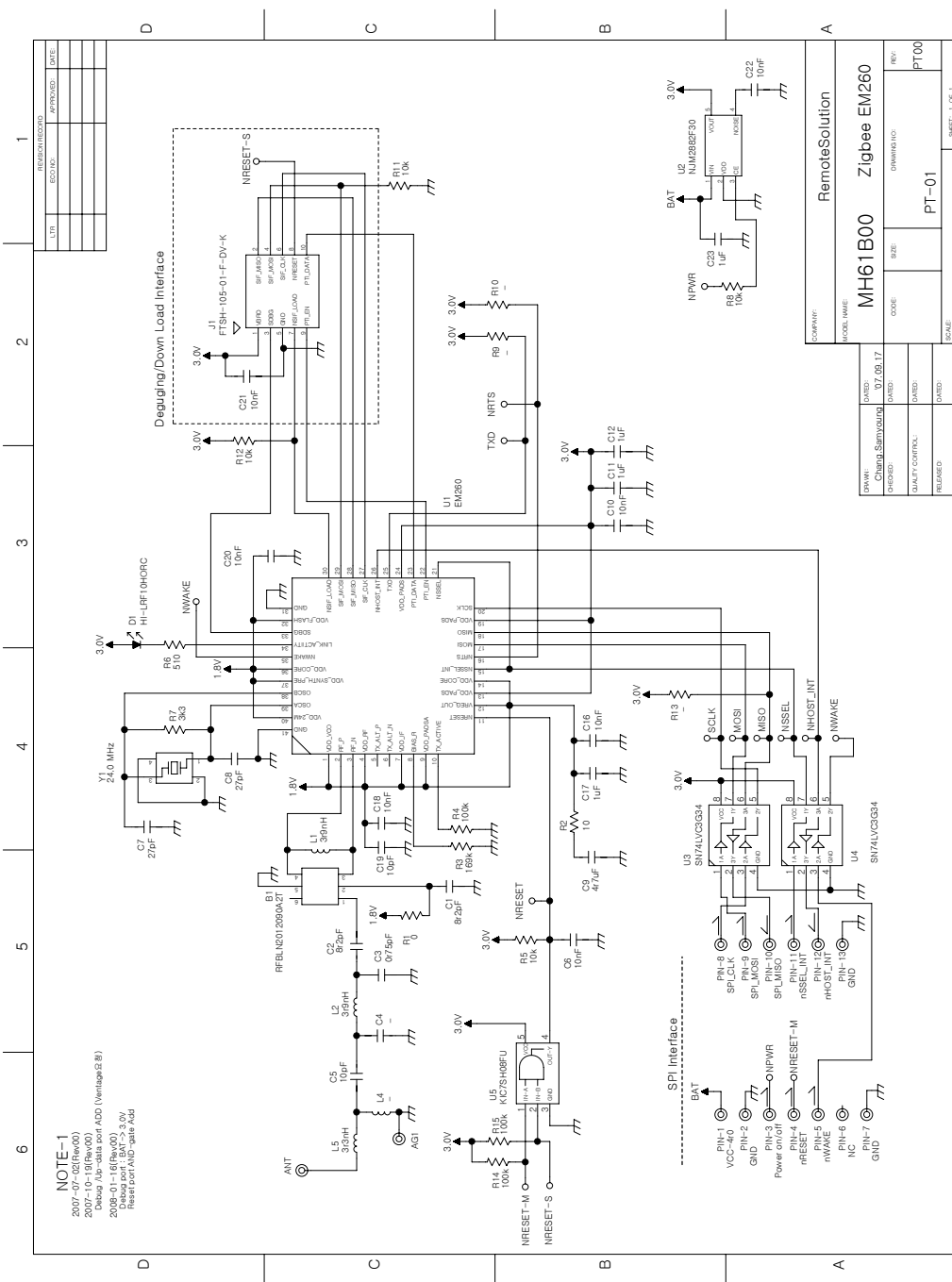


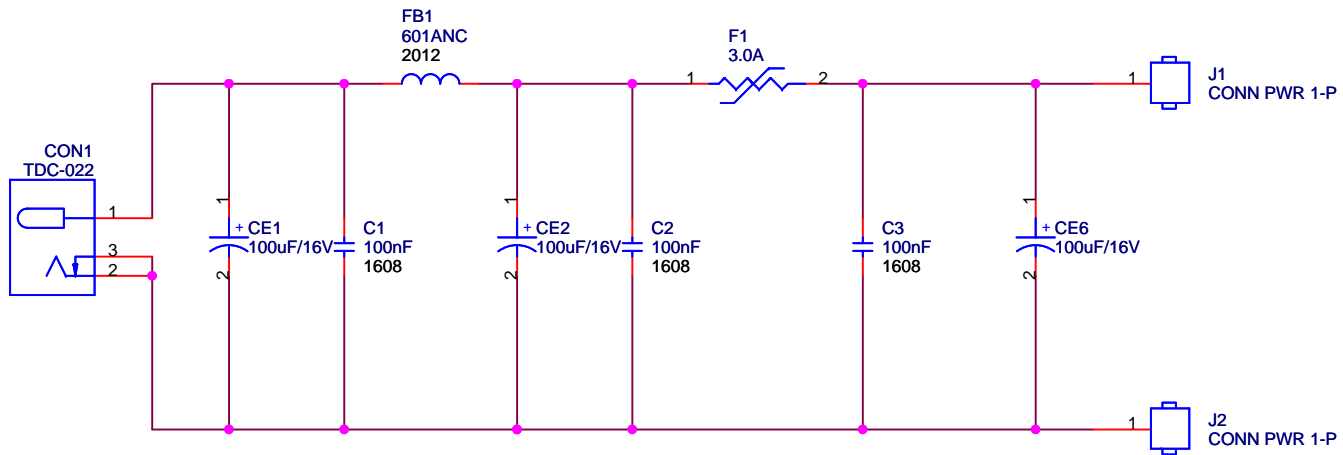
Remote Solution

Title : **5.5x MVT schematic x2.0**

Page Title : **Battery / Charger / monitoring / MUX**

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Size :
A4

Drawing By :
Ahn young tae

Drawing File Name :
6x Reference schematic x2.0.DSN

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