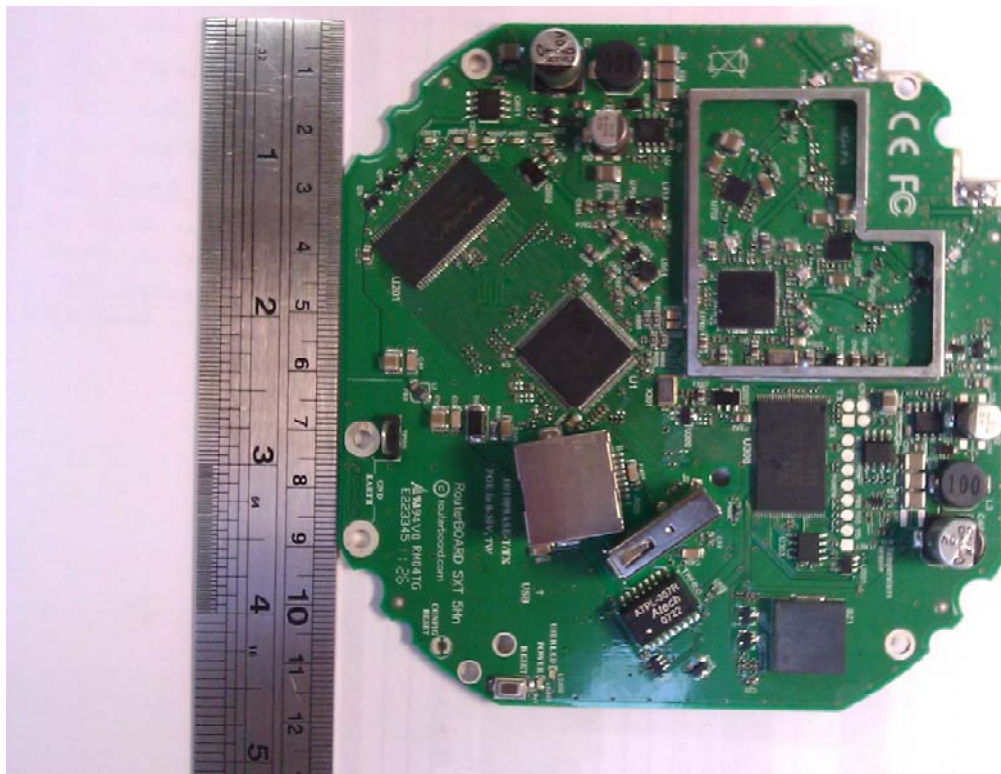


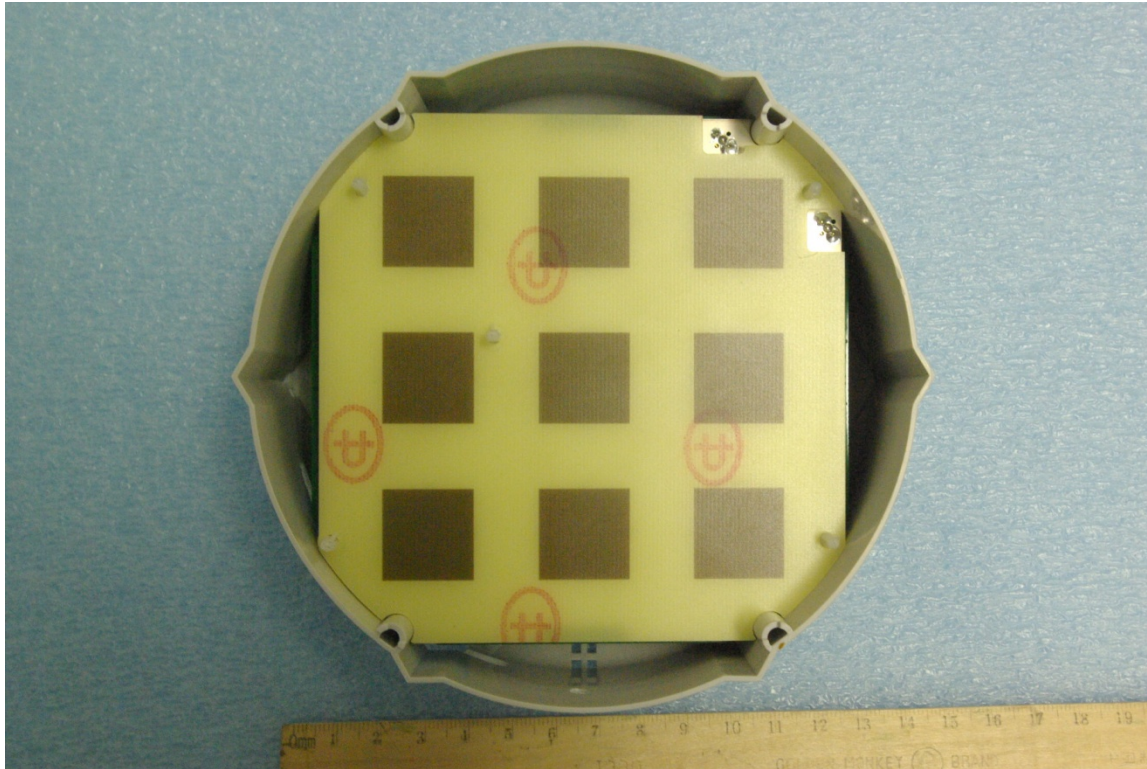
Internal photographs of equipment under test  
PCB without shield



Back side of PCB



EUT placement in enclosure



PCB with shield

