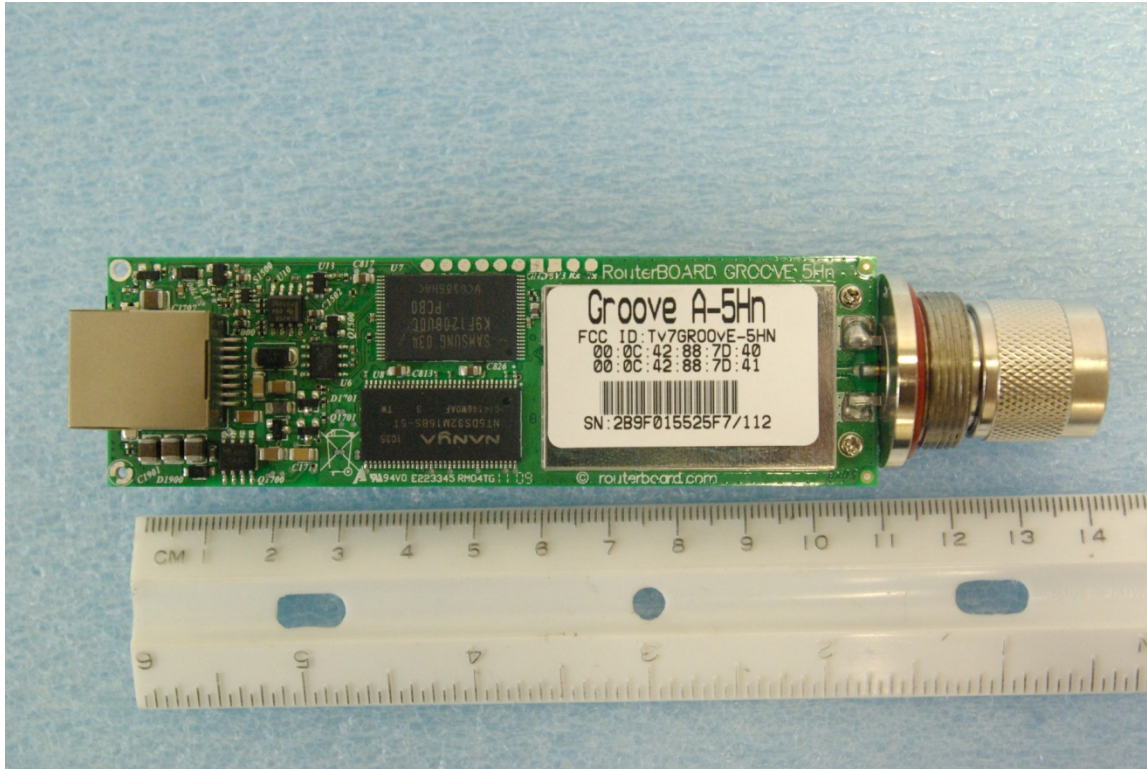
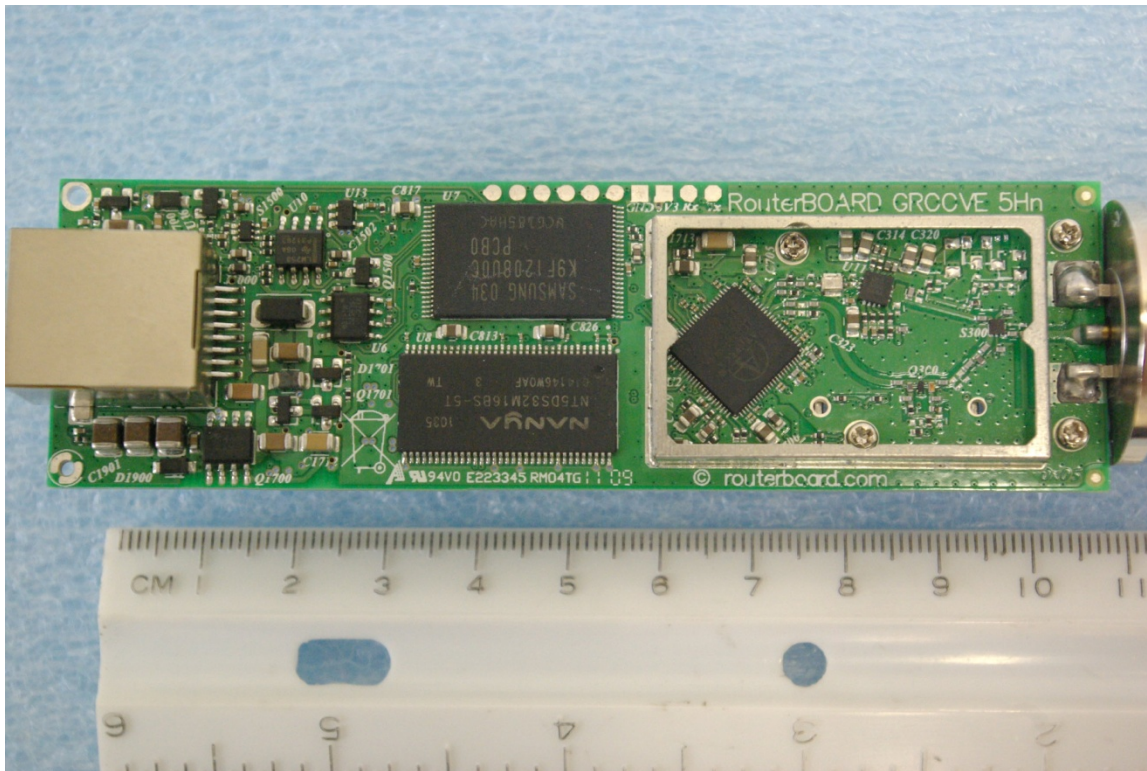


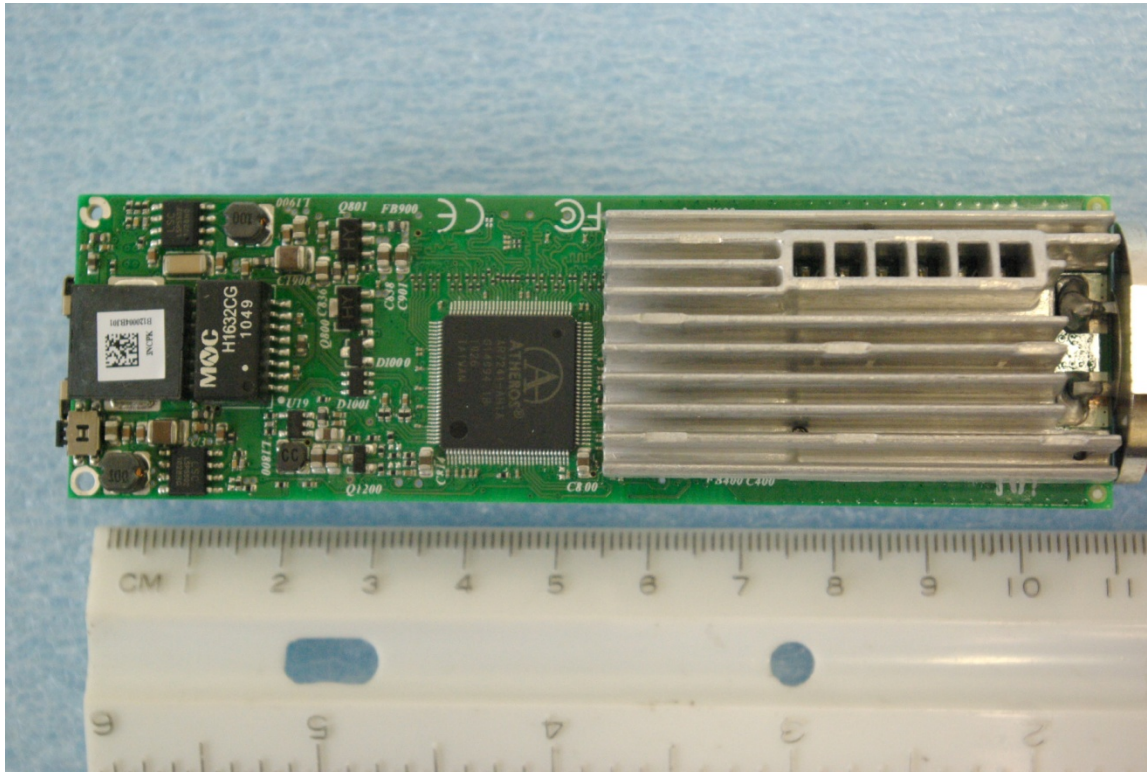
Internal photographs of equipment under test  
PCB with shield



PCB without shield



Back side of EUT PCB



EUT placement in enclosure

