

ET-JP-3702 Circuit description

ET-JP-3702 emitter is composed of single chip U1, surrounding memory IC U2 and power IC U3.

U3 , L2 and D1 are composed to a power supply's boost part, output voltage is steady 3V voltage, it supply power to the keyboard.

U1 is master circuit,

Internal circuit and X2 surrounding circuit are composed to clock oscillator, frequency is 32.768KHZ, it supply clock pulses to keyboard. X1 is 12MHZ surge circuit, it supply time series to system's RF part.

While press key of the keyboard, MB1 keyboard matrix port were activated, U1 will make special treatment included RF modulation, RF enlargement, PIN62 output, then transfer to antenna by C16~C19 matching circuit, then launch to free space.

U2 is ID code memory, when the mouse and the receivers code connected successfully, U2 will record this code and spare to next time.