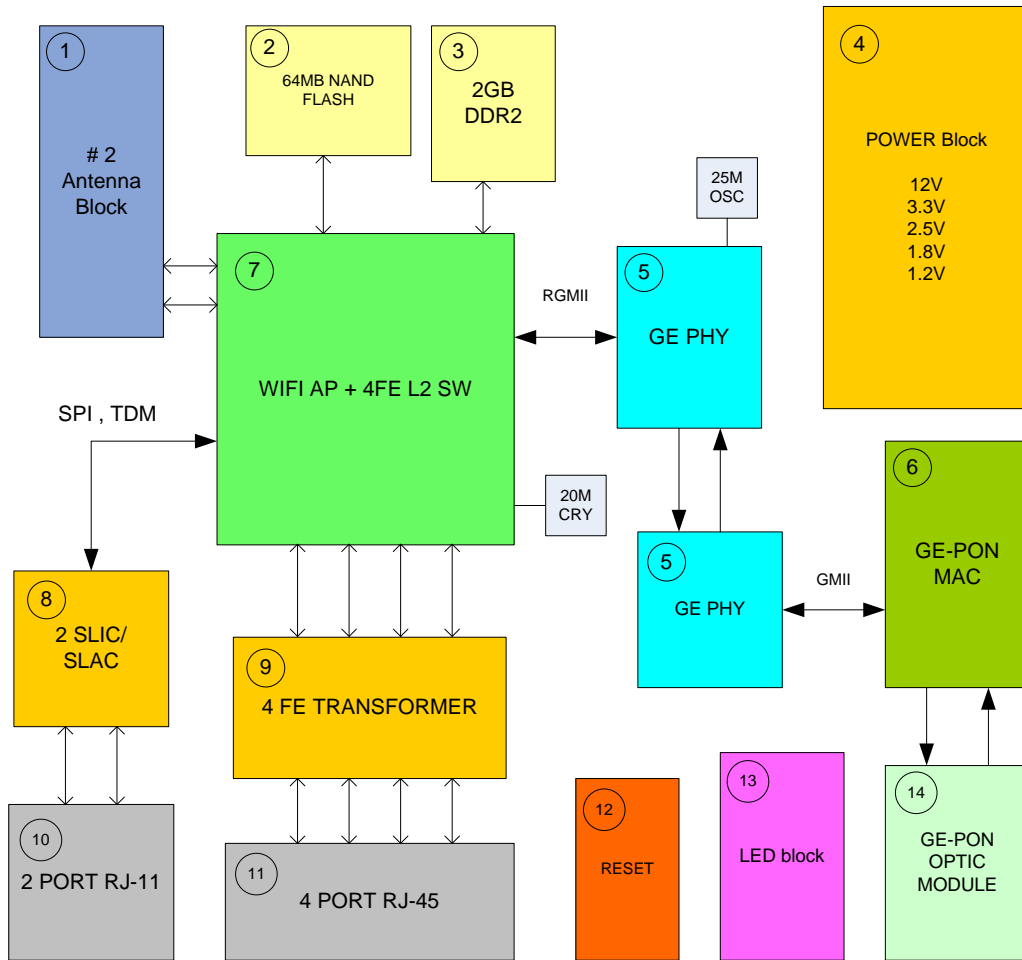


**Appendix 4.**

# Block Diagram

# BOS-ONU-1-3-105-D BLCOK DIAGRAM



- ① Antenna Block : is connected to two Dipole antennas. 2T/2R 802.11b/g/n
- ② 64MB NAND Flash : is programed O/S , F/W , S/W.
- ③ 2GB DDR2 Memory : is running Programed S/W .
- ④ POWER Block : is power block on board. 12v,3.3v,2.5v, 1.8v,1.2v
- ⑤ GE PHY : is Gigabit Ethernet PHY. 25MHz OSC is running Frequency
- ⑥ GE-PON MAC : is GE-PON MAC IC. Service for GEAPON
- ⑦ WIFI AP + 4FE L2 SW : is MAIN WIFI AP + L2 SW IC. 20MHz Crystal is running Frequency.
- ⑧ 2 SLIC / SLAC : is 2 ch SLIC / SLAC IC for VOIP Service.
- ⑨ 4 FE TRANSFORMER : is 4 ports FE Transformer For Ethernet Service.
- ⑩ 2 PORT RJ-11 : is 2 ports RJ-11 JACK For FXS.
- ⑪ 4 PORT RJ-45 : is 4 ports RJ-45 JACK For FE (Fast Ethernet)
- ⑫ RESET : is RESET Switch/IC for system reset.
- ⑬ LED BLOCK : is LED block For status indication.
- ⑭ GE-PON OPTIC MODULE : is GE-PON OPTIC MODULE.