sch_051700236_sht6.pdf   sheet 6 of the schematics contains the FPGA which is used to control the UART interface between the DS5250 Secure Microcontroller to the PN532. The FPGA controls reset signals and programming signals as well as multiplexing the transmit and receive on the UARTs.

sch_051700236_sht9.pdf   sheet 9 of the schematics contains the PN532. This device generates all the signals for the 13.56 MHz Near Field Communication interface. The PN532 drives the base of transistors which provide power amplification. The current and power to the antenna is determined by the emitter resistors (24 Ohms). Inductors and capacitors between the Power Amplifier and antenna provide filtering and impedance matching to the antenna.

sch_051700236_sht10.pdf   sheet 10 of the schematics contains the Wi2Wi device. This device is classified as a System in Package and contains all the circuitry to support the 802.11 b/g with the exception of an antenna.