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To Whom It May Concern,

The following are the only difference between 4354A1 (externally called BCM4354 for Windows/Android SDIO designs and externally called BCM43540 for ChromeOS SDIO designs) and 4354A2 (externally called BCM4356 for PCIe designs, and BCM4356S for single-antenna PCIe designs).

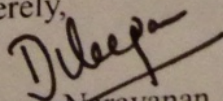
No changes that impact emissions were made to the WLAN and BT radio circuits.

These changes do not necessitate 2 separate certification IDs when the same board design uses BCM4354, BCM43540, BCM4356 and BCM4356S.

Metal Layer Changes/Fixes Only - 4354A2(BCM4356)	4354A2	HW/SW
1. Pick up dot11acphy pktpreemption fixes from 4350C2.	Y	SW
2. Using iPA GNDMESH to improve leakage	Y	HW - no impact to emissions
3. PCIe OTP fix	Y	SW
4. BT_GPIO 2,4,5 - default pull resistor change to down, up, up & the same in sleep mode	Y	SW
5. Audio DAC leakage	Y	HW - no impact to emissions
6. I2S_DO muxing	Y	HW - no impact to emissions
7. Remove SFlash interface	Y	HW - no impact to emissions
8. Add BT 4.1, and ANT+	Y	SW
9. Add A4WP	Y	SW
12. WL_Debug_Uart mux to interface with sensor hub	Y	HW - no impact to emissions

If you have any questions regarding this, please feel free to contact us.

Sincerely,


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