

Operational descriptions

WLM200N5-26ESD integrates an Atheros AR9220 chipset to create RF energy. The Atheros AR9220 is an all CMOS, single chip solution for dual-band, multi-mode, IEEE 802.11a/b/g/n WLANs. It integrates a 5GHz radio, analog-to-digital and digital-to-analog (ADC/DAC) converters, a baseband processor and multi-protocol media access control. For the AR9220's transmitter, it combines baseband in-phase(I) and quadrature (Q) signals, converts them to the desired frequency, and drives the RF signal off-chip. This RF signal is again filtered by a low pass filter, before sending into a power amplifier chipset, which amplifies the RF signal. For the AR9220's receiver, it uses an integrated dual conversion architecture and requires no off-chip intermediate frequency (IF) filters

Modulation techniques :

OFDM :BPSK,QPSK,16 QAM,64QAM

Frequency Range (MHz) :5745 - 5825MHz for 802.11a/n(HT20)

5755 - 5795MHz for 802.11n(HT40)

Operational Block Diagram

