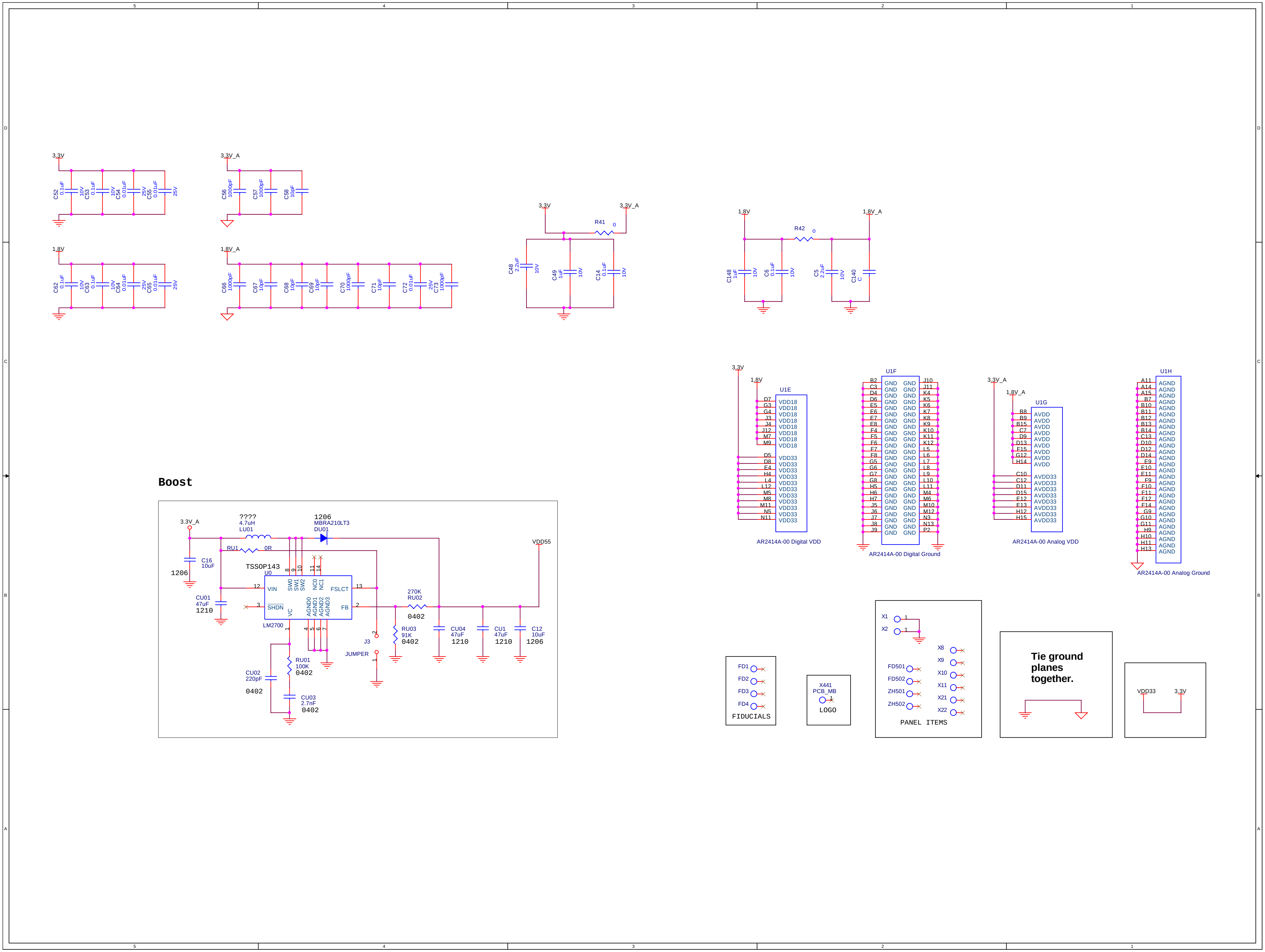
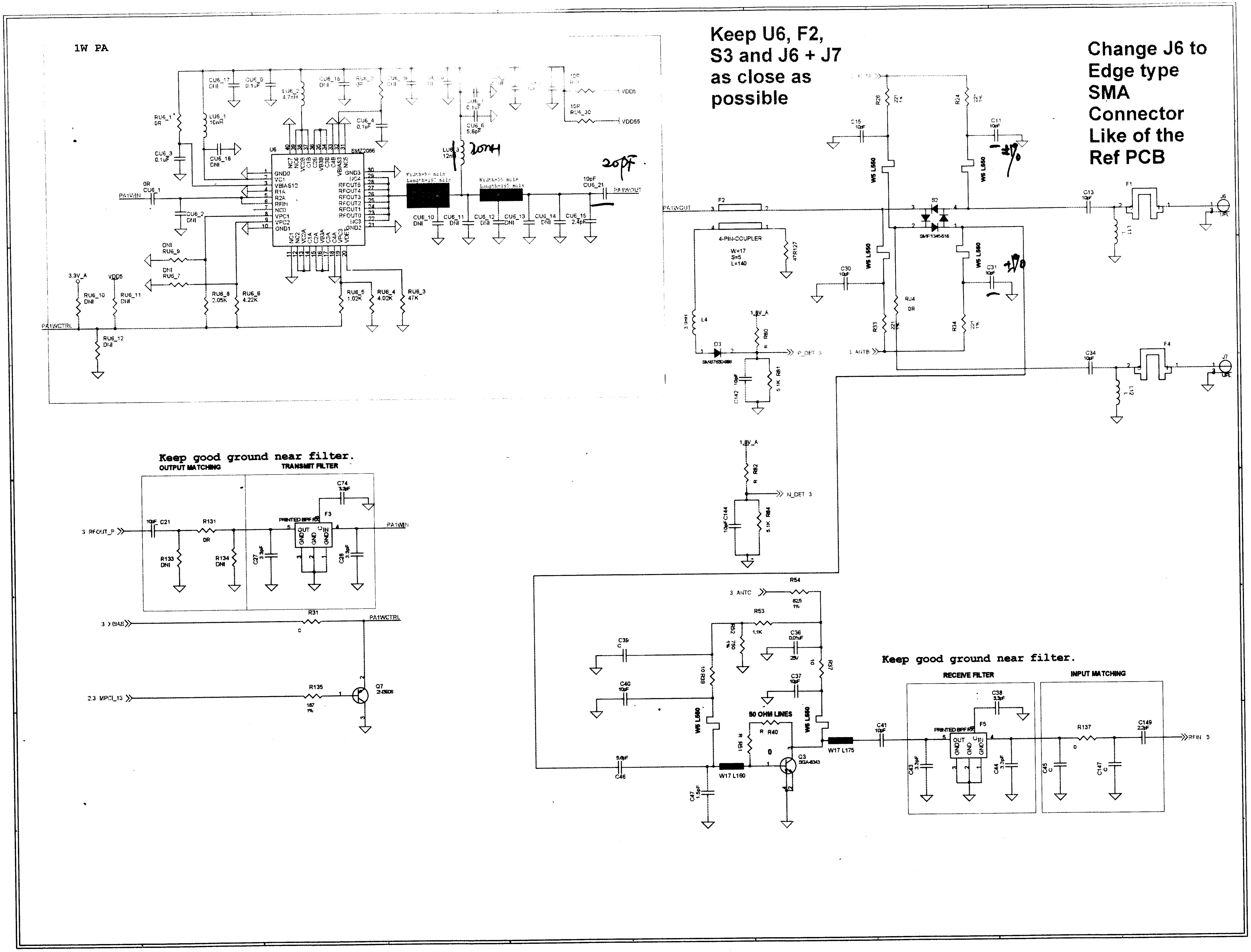
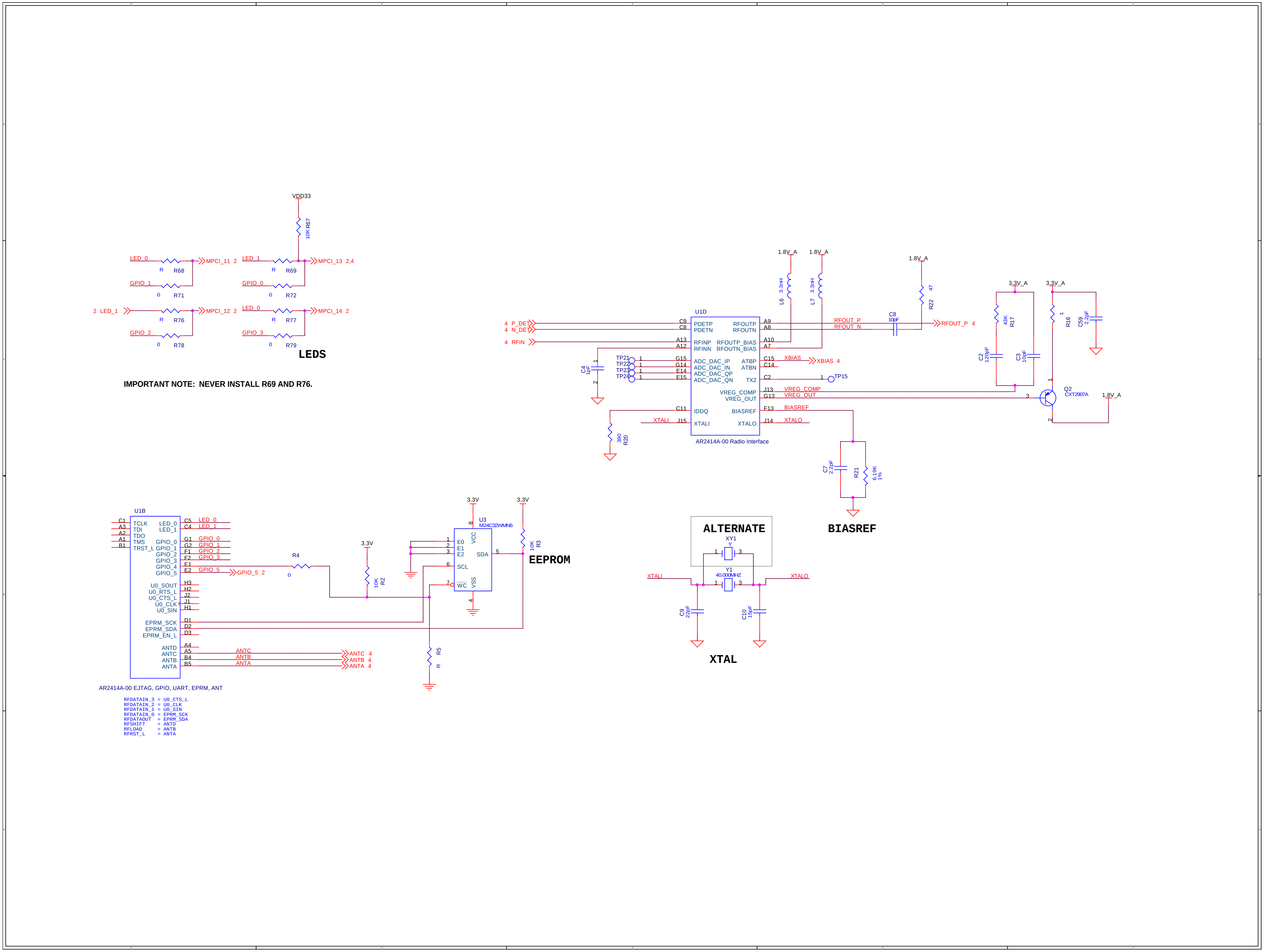


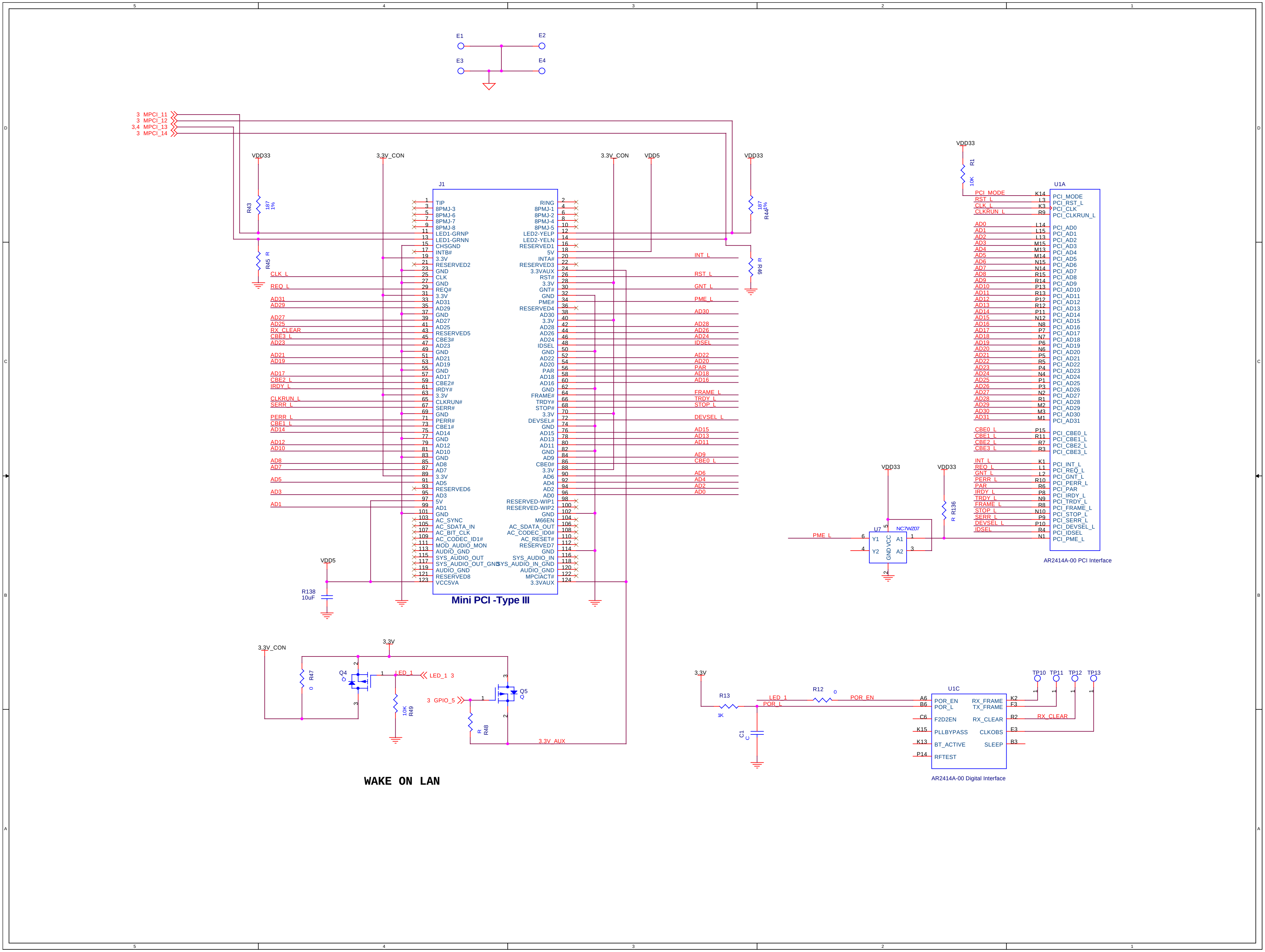
Boost
3.3V_A
???? 4.7uH LU01
1206 MBRA210LT3 DU01
RU1 0R
C16 10uF 1206
TSSOP143 U0
LM2700
VIN SW0 SW1 SW2 NC0 NC1 FSLCT
SHDN VC AGND0 AGND1 AGND2 AGND3 FB
CU01 47uF 1210
270K RU02 0402
RU03 91K 0402
CU04 47uF 1210
CU1 47uF 1210
C12 10uF 1206
VDD55
J3 JUMPER
RU01 100K 0402
CU02 220pF 0402
CU03 2.7nF 0402
3.3V
C52 0.1uF 10V
C53 0.1uF 10V
C54 0.01uF 25V
C55 0.01uF 25V
C56 1000pF
C57 1000pF
C58 10pF
1.8V
C62 0.1uF 10V
C63 0.1uF 10V
C64 0.01uF 25V
C65 0.01uF 25V
1.8V_A
C66 1000pF
C67 10pF
C68 10pF
C69 10pF
C70 1000pF
C71 10pF
C72 0.01uF 25V
C73 1000pF
R41 0
C48 2.2uF 10V
C49 1uF 10V
C14 0.1uF 10V
R42 0
C148 1uF 10V
C6 0.1uF 10V
C5 2.2uF 10V
C140 C
U1E
VDD18
VDD33
AR2414A-00 Digital VDD
U1F
GND
AR2414A-00 Digital Ground
U1G
AVDD
AVDD33
AR2414A-00 Analog VDD
U1H
AGND
AR2414A-00 Analog Ground
FD1 FD2 FD3 FD4
FIDUCIALS
X441 PCB_MB
LOGO
X1 X2 X8 X9 X10 X11 X21 X22
FD501 FD502 ZH501 ZH502
PANEL ITEMS
Tie ground planes together.
VDD33 3.3V



1W PA
Keep U6, F2, S3 and J6 + J7 as close as possible
Change J6 to Edge type SMA Connector Like of the Ref PCB
Keep good ground near filter.
OUTPUT MATCHING
TRANSMIT FILTER
Keep good ground near filter.
RECEIVE FILTER
INPUT MATCHING
50 OHM LINES
4-PIN-COUPLER



VDD33
10K R67
LED_0
R R68
MPCI_11 2
GPIO_1
0 R71
LED_1
R R69
MPCI_13 2,4
GPIO_0
0 R72
2 LED_1
R R76
MPCI_12 2
GPIO_2
0 R78
LED_0
R R77
MPCI_14 2
GPIO_3
0 R79
LEDS
IMPORTANT NOTE: NEVER INSTALL R69 AND R76.
1.8V_A
L6 3.3nH
1.8V_A
L7 3.3nH
1.8V_A
47 R22
U1D
4 P_DET
4 N_DET
4 RFIN
PDETP
PDETN
RFINP
RFINN
ADC_DAC_IP
ADC_DAC_IN
ADC_DAC_QP
ADC_DAC_QN
IDDQ
XTALI
RFOUTP
RFOUTN
RFOUTP_BIAS
RFOUTN_BIAS
ATBP
ATBN
TX2
VREG_COMP
VREG_OUT
BIASREF
XTALO
AR2414A-00 Radio Interface
C8 10pF
RFOUT_P
RFOUT_N
RFOUT_P 4
XBIAS
XBIAS 4
TP15
TP21
TP22
TP23
TP24
C4 1pF
390 R20
XTALI
XTALO
VREG_COMP
VREG_OUT
BIASREF
3.3V_A
3.3V_A
43K R17
1 R16
C59 2.2pF
C2 120pF
C3 10pF
Q2 CXT2907A
1.8V_A
C7 2.2pF
R21 6.19K 1%
BIASREF
U1B
TCLK
TDI
TDO
TMS
TRST_L
LED_0
LED_1
GPIO_0
GPIO_1
GPIO_2
GPIO_3
GPIO_4
GPIO_5
GPIO_5 2
U0_SOUT
U0_RTS_L
U0_CTS_L
U0_CLK
U0_SIN
EPRM_SCK
EPRM_SDA
EPRM_EN_L
ANTD
ANTC
ANTB
ANTA
ANTC 4
ANTB 4
ANTA 4
AR2414A-00 EJTAG, GPIO, UART, EPRM, ANT
RFDATAIN_3 = U0_CTS_L
RFDATAIN_2 = U0_CLK
RFDATAIN_1 = U0_SIN
RFDATAIN_0 = EPRM_SCK
RFDATAOUT = EPRM_SDA
RFSHIFT = ANTD
RFLOAD = ANTB
RFRST_L = ANTA
R4 0
3.3V
10K R2
3.3V
U3 M24C32WMN6
E0
E1
E2
SCL
WC
VCC
SDA
VSS
3.3V
10K R3
EEPROM
R5 R
ALTERNATE
XY1 Y
Y1 40.000MHZ
XTALI
XTALO
C9 22pF
C10 15pF
XTAL


E1
E2
E3
E4
3 MPCI_11
3 MPCI_12
3,4 MPCI_13
3 MPCI_14
VDD33
3.3V_CON
VDD5
J1
R43
187 1%
R44
R45
R46
TIP
8PMJ-3
8PMJ-6
8PMJ-7
8PMJ-8
LED1-GRNP
LED1-GRNN
CHSGND
INTB#
3.3V
RESERVED2
GND
CLK
REQ#
AD31
AD29
AD27
AD25
RESERVED5
CBE3#
AD23
AD21
AD19
AD17
CBE2#
IRDY#
CLKRUN#
SERR#
PERR#
CBE1#
AD14
AD12
AD10
AD8
AD7
AD5
RESERVED6
AD3
5V
AD1
AC_SYNC
AC_SDATA_IN
AC_BIT_CLK
AC_CODEC_ID1#
MOD_AUDIO_MON
AUDIO_GND
SYS_AUDIO_OUT
SYS_AUDIO_OUT_GND
RESERVED8
VCC5VA
RING
8PMJ-1
8PMJ-2
8PMJ-4
8PMJ-5
LED2-YELP
LED2-YELN
RESERVED1
INTA#
RESERVED3
3.3VAUX
RST#
GNT#
PME#
RESERVED4
AD30
AD28
AD26
AD24
IDSEL
AD22
AD20
PAR
AD18
AD16
FRAME#
TRDY#
STOP#
DEVSEL#
AD15
AD13
AD11
AD9
CBE0#
AD6
AD4
AD2
AD0
RESERVED-WIP1
RESERVED-WIP2
M66EN
AC_SDATA_OUT
AC_CODEC_ID0#
AC_RESET#
RESERVED7
SYS_AUDIO_IN
SYS_AUDIO_IN_GND
MPCIACT#
Mini PCI -Type III
CLK_L
REQ_L
RX_CLEAR
CBE3_L
CBE2_L
IRDY_L
CLKRUN_L
SERR_L
PERR_L
CBE1_L
INT_L
RST_L
GNT_L
PME_L
FRAME_L
TRDY_L
STOP_L
DEVSEL_L
CBE0_L
R138
10uF
R1
10K
U1A
PCI_MODE
PCI_RST_L
PCI_CLK
PCI_CLKRUN_L
PCI_AD0
PCI_AD31
PCI_CBE0_L
PCI_CBE1_L
PCI_CBE2_L
PCI_CBE3_L
PCI_INT_L
PCI_REQ_L
PCI_GNT_L
PCI_PERR_L
PCI_PAR
PCI_IRDY_L
PCI_TRDY_L
PCI_FRAME_L
PCI_STOP_L
PCI_SERR_L
PCI_DEVSEL_L
PCI_IDSEL
PCI_PME_L
AR2414A-00 PCI Interface
U7
NC7WZ07
R136
3.3V
R47
Q4
Q5
LED_1 3
3 GPIO_5
R48
R49
10K
3.3V_AUX
WAKE ON LAN
R13
1K
R12
0
C1
LED_1
POR_L
POR_EN
U1C
POR_EN
POR_L
F2D2EN
PLLBYPASS
BT_ACTIVE
RFTEST
RX_FRAME
TX_FRAME
RX_CLEAR
CLKOBS
SLEEP
TP10
TP11
TP12
TP13
AR2414A-00 Digital Interface

MB51 - AR5005GS Based 802.11b/g Type IIIb Mini PCI

DATE	REVISION NUMBER	INITIALS	DESCRIPTION
6/11/04	245-01268-480, Rev 000	CL	Initial release. Branched from MB51-245-01268-080. Edited title block, deleted U1, cleared cache and replaced U1 with AR2414A-00.
6/11/04	245-01268-481	CL	Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part number up-rev'd as well as header and footer information.
7/6/04	245-01268-482	CL	Changed R120 from 150 to 200.
7/12/04	245-01268-483	CL	Add OEpic as alternate XPA following regulatory certification and ATMEL as alternate EEPROM.