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Regarding Requirements of Paragraph 15.212(a)(1)

Shielding 15.212(a)(1)(i)

The oscillator/transmitter circuitry is housed under a metal shield soldered to the circuit board.

Buffered Modulation/Data Input 15.212(a)(1)(ii)

All signals are buffered as required.

Power Regulation 15.212(a)(1)(iii)

The RF components are powered by a locally regulated 3.3 V source.

Antenna Construction 15.212(a)(1)(iv)

The antenna is integral to the printed circuit board as an etched trace or patch antenna.

Test Configuration 15.212(a)(1)(v)

The device was tested stand-alone with no external shielding. No ferrites were used on the attached cable. The connecting cable measured in excess of 3 m of unshielded cable to a break-out box and with power further extended up to ~30 cm to the power supply. A commercial grade bench DC power supply provided 12 VDC power to the EUT. The power supply had no additional filtering for the mains.

Labeling 15.212(a)(1)(vi)

The module is labeled with the FCC ID using a permanently applied printed label as depicted in other supporting documentation. The user manual contains instructions for external labeling when the module is enclosed in a final configuration.

Applicable Requirements on Final Configuration 15.212(a)(1)(vii)

The user manual includes instructions that explain the requirements of marking and the instructions that need to be conveyed to the final configuration.

Compliance to RF Exposure Requirements in Final Configuration 15.212(a)(1)(viii)

The user manual includes instructions on the warnings required for the final configuration.

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