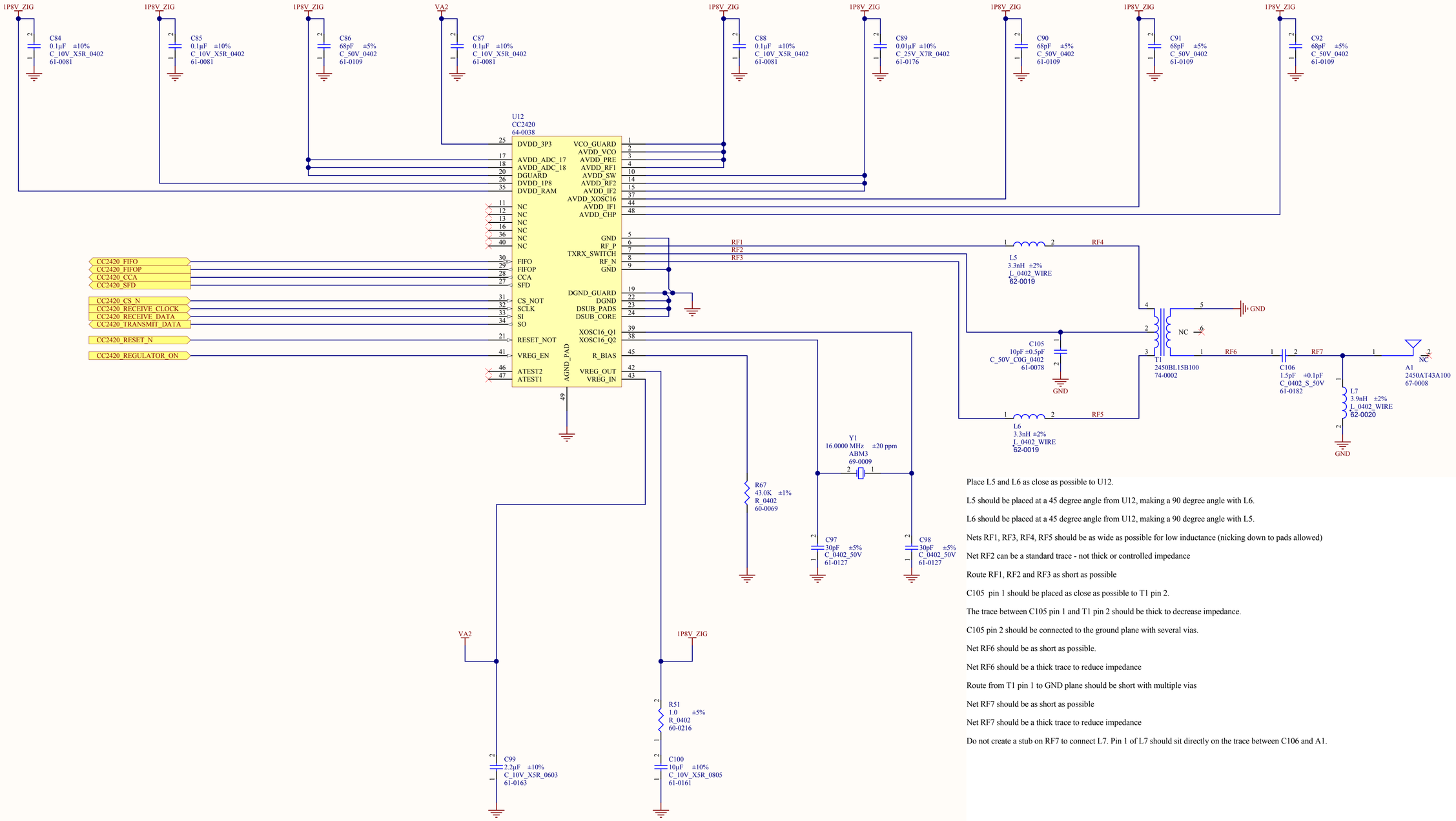

Place L5 and L6 as close as possible to U12.

L5 should be placed at a 45 degree angle from U12, making a 90 degree angle with L6.

L6 should be placed at a 45 degree angle from U12, making a 90 degree angle with L5.

Nets RF1, RF3, RF4, RF5 should be as wide as possible for low inductance (nicking down to pads allowed)

Net RF2 can be a standard trace - not thick or controlled impedance

Route RF1, RF2 and RF3 as short as possible

C105 pin 1 should be placed as close as possible to T1 pin 2.

The trace between C105 pin 1 and T1 pin 2 should be thick to decrease impedance.

C105 pin 2 should be connected to the ground plane with several vias.

Net RF6 should be as short as possible.

Net RF6 should be a thick trace to reduce impedance

Route from T1 pin 1 to GND plane should be short with multiple vias

Net RF7 should be as short as possible

Net RF7 should be a thick trace to reduce impedance

Do not create a stub on RF7 to connect L7. Pin 1 of L7 should sit directly on the trace between C106 and A1.

Assembly Variant: Variant name is not interpreted until output

WARNING!

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OWL (BCM5890 BASED BPID WITH CC2420)

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