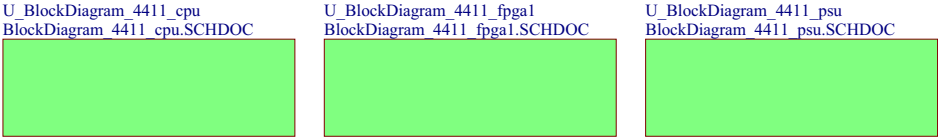


*These components are located in their own RF shielded compartment*

Bottom compartment of enclosure houses RFD4411

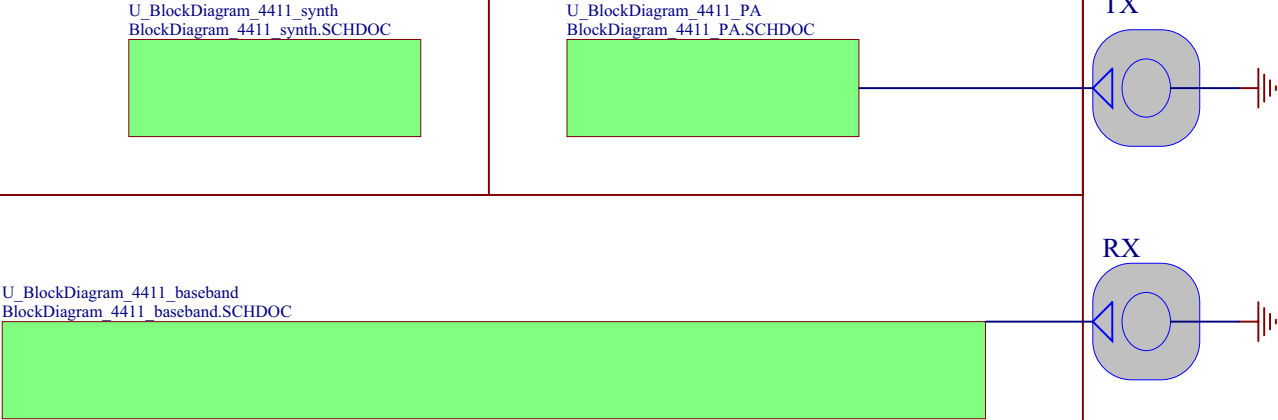



The MaxID RFID reader consist of the following PCBs:  
RFD4411 - Digital section, Power supply, Digital decoder (FPGA), User Interface  
RFM4410 - RF synthesizer, RF power amp. , Base Band signal conditioning

The Digital and RF PBCs are physically seperated from each other via an aluminium section

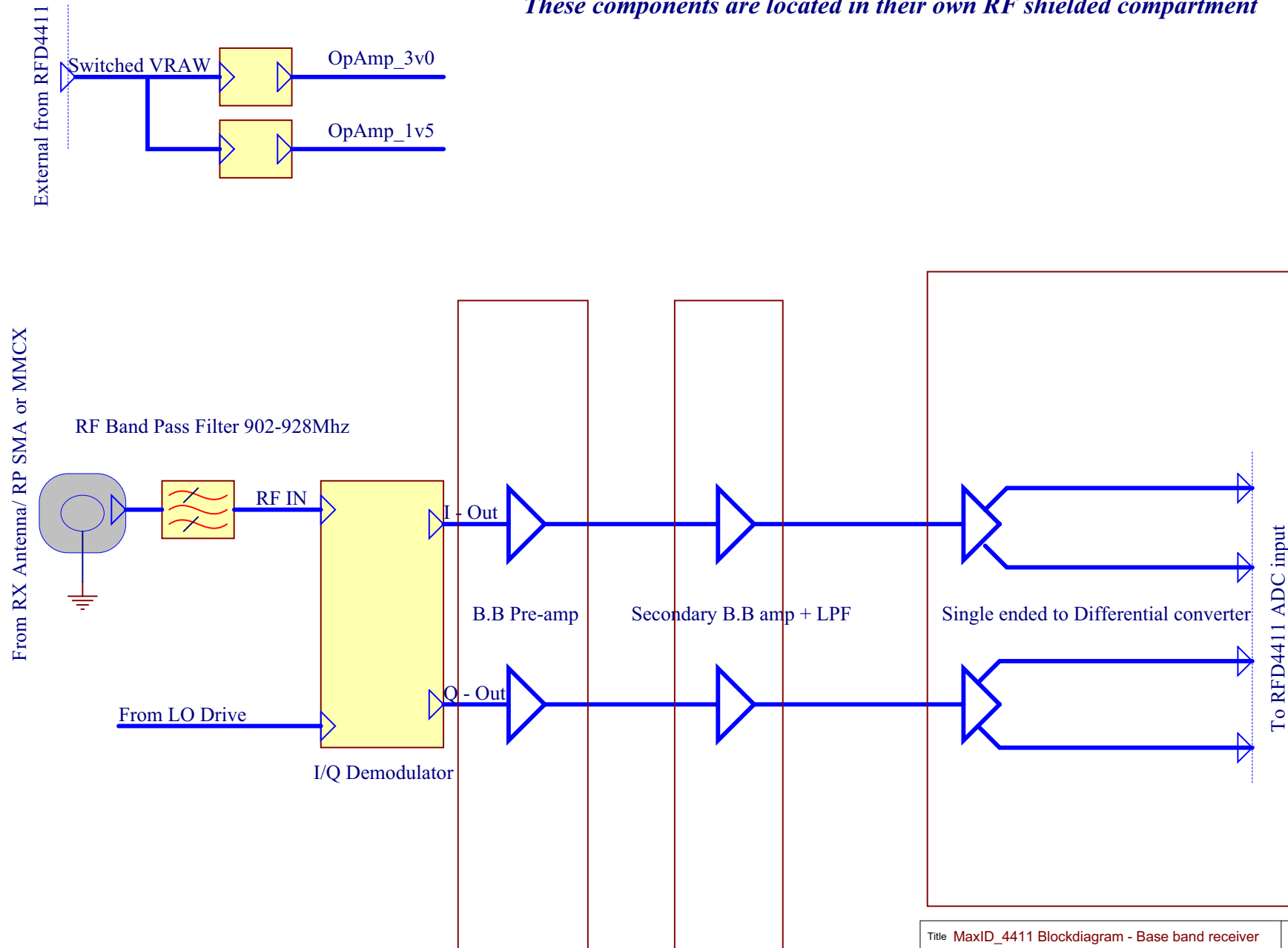
The RF section is also further seperated from each other via aluminium compartments.


Top compartment of enclosure houses RFM4410

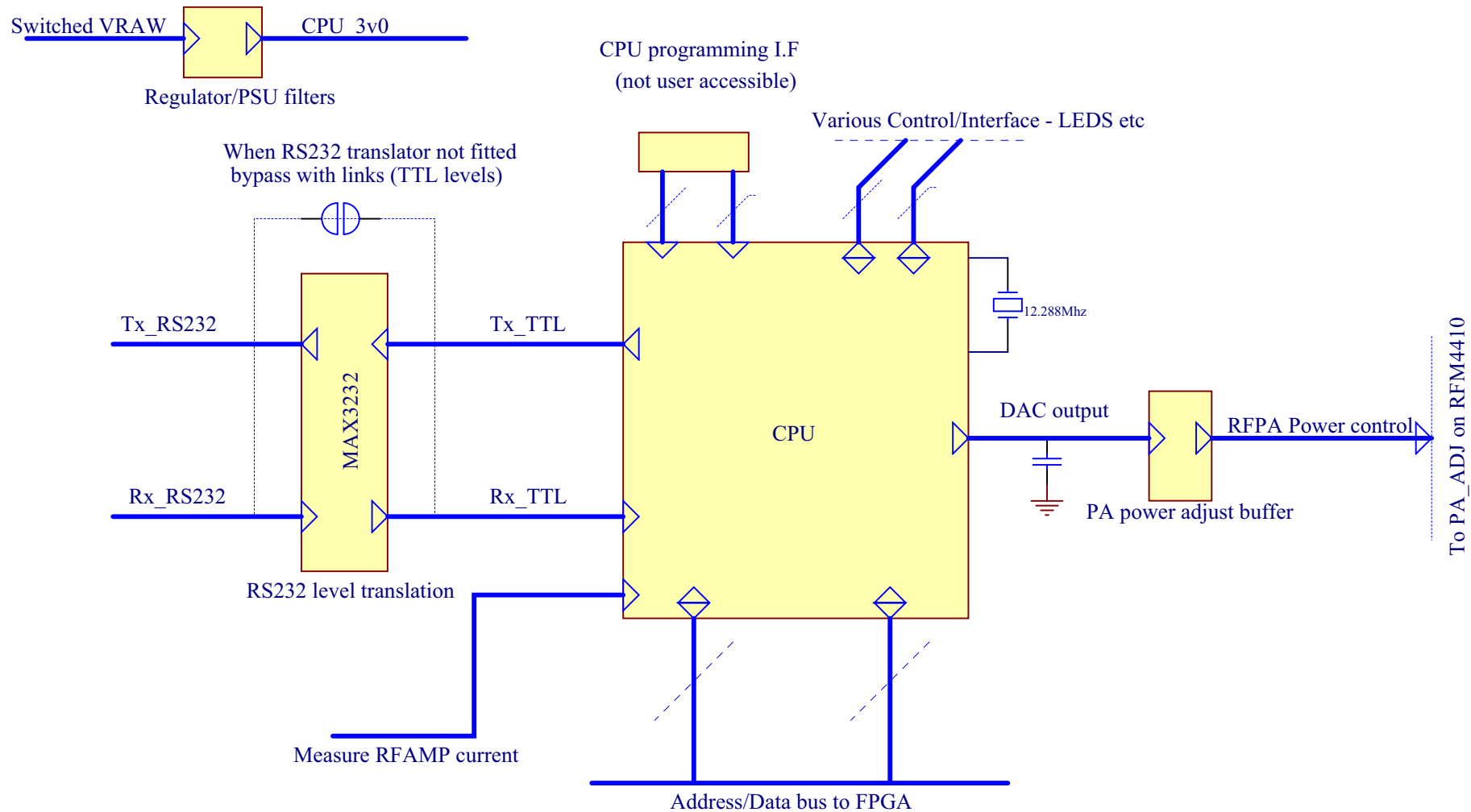


Title <b>MaxID_4411 Blockdiagram</b>			<div>MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom</div> <div></div>
Size: <b>A3</b>	Number: <b>RM100_4411</b>	Revision: <b>00</b>	
Date: <b>7/20/2005</b>	Time: <b>2:14:19 PM</b>	Sheet <b>1</b> of <b>7</b>	
File: <b>C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411.SCHDOC</b>			

*These components are located in their own RF shielded compartment*

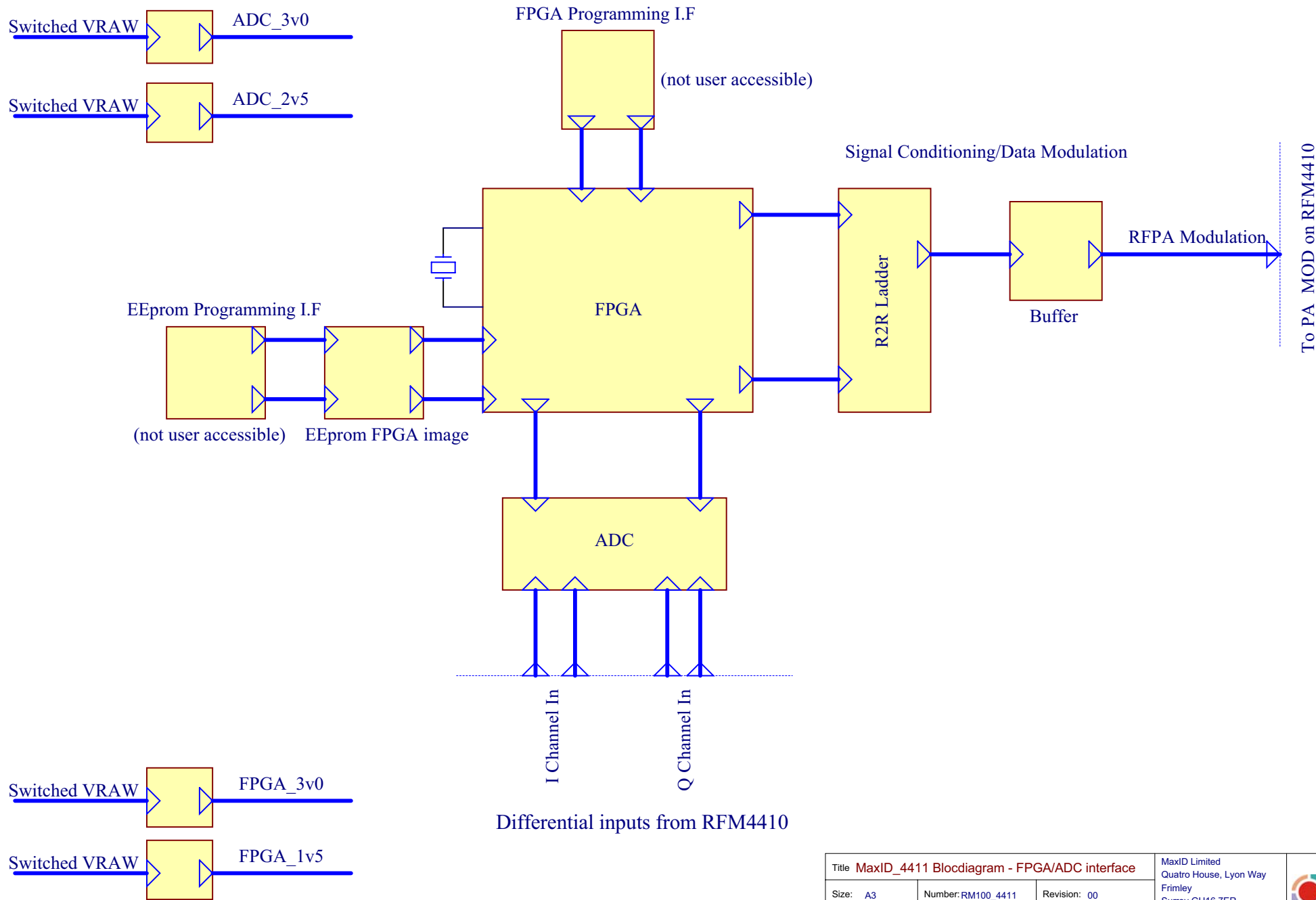



Title: <b>MaxID_4411 Blockdiagram - Base band receiver</b>			<div>MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom</div> 
Size: <b>A3</b>	Number: <b>RM100_4411</b>	Revision: <b>00</b>	
Date: <b>7/20/2005</b>	Time: <b>2:14:19 PM</b>	Sheet <b>6</b> of <b>7</b>	
File: <b>C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_baseband.SCHDOC</b>			



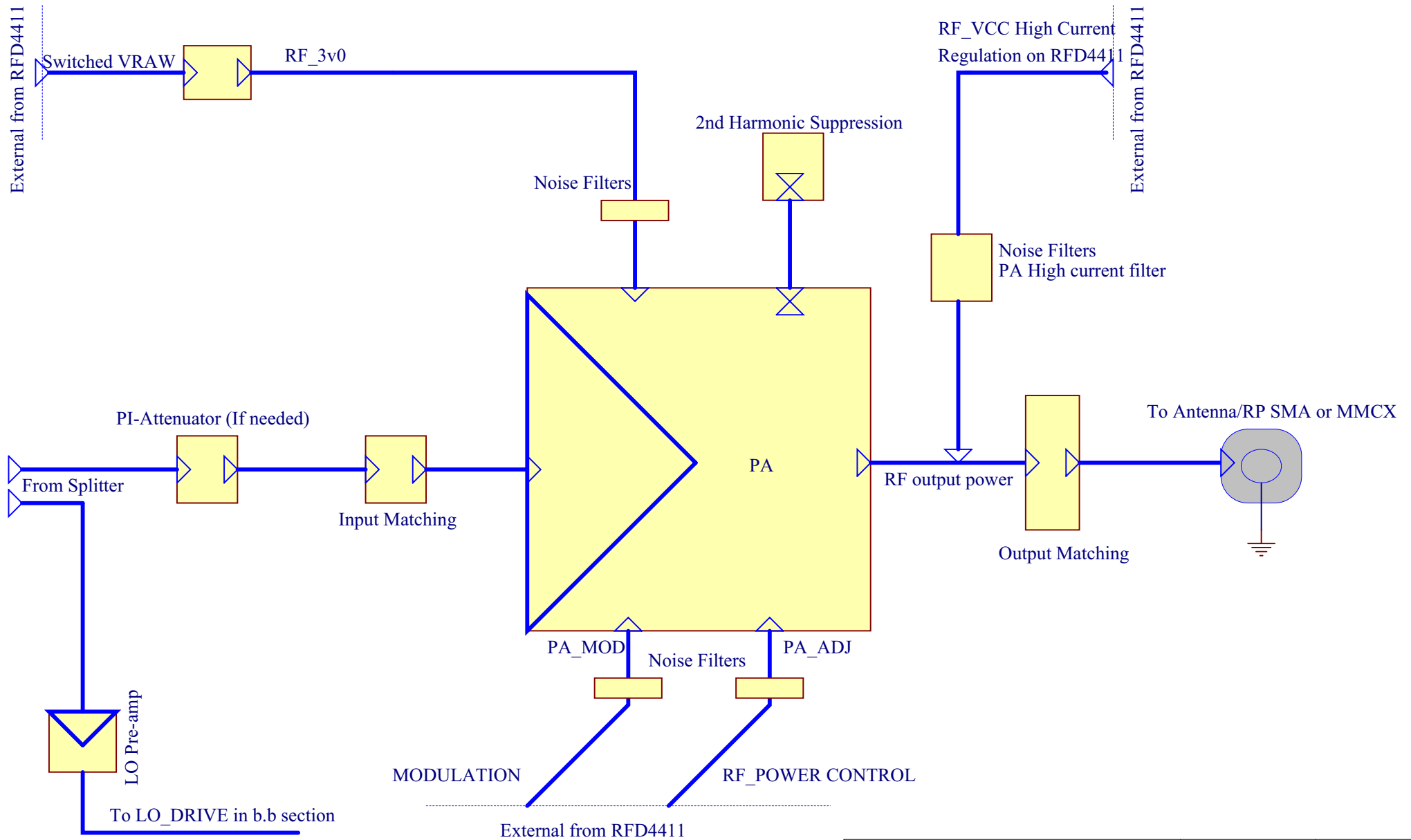
Title <b>MaxID_4411 Blockdiagram - CPU and Interface</b>			MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom
Size: <b>A3</b>	Number: <b>RM100_4411</b>	Revision: <b>00</b>	
Date: <b>7/20/2005</b>	Time: <b>2:14:20 PM</b>	Sheet <b>2</b> of <b>7</b>	
File: <b>C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\SCH\BlockDiagram_4411 cpu.SCHDOC</b>			






Title <b>MaxID_4411 Blocdiagram - FPGA/ADC interface</b>			MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom	
Size: <b>A3</b>	Number: <b>RM100_4411</b>	Revision: <b>00</b>		
Date: <b>7/20/2005</b>	Time: <b>2:14:20 PM</b>	Sheet <b>3</b> of <b>7</b>		
File: <b>C:\work\ProtelProjects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_fpga1.SCHDOC</b>				

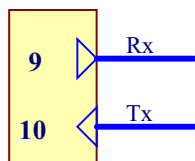
*These components are located in their own RF shielded compartment*



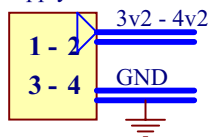
Title: <b>MaxID_4411 Blockdiagram - RF Power Amp</b>			<div>MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom</div> 
Size: <b>A3</b>	Number: <b>RM100_4411</b>	Revision: <b>00</b>	
Date: <b>7/20/2005</b>	Time: <b>2:14:20 PM</b>	Sheet <b>5</b> of <b>7</b>	
File: <b>C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_PA.SCHDOC</b>			

## External User interface

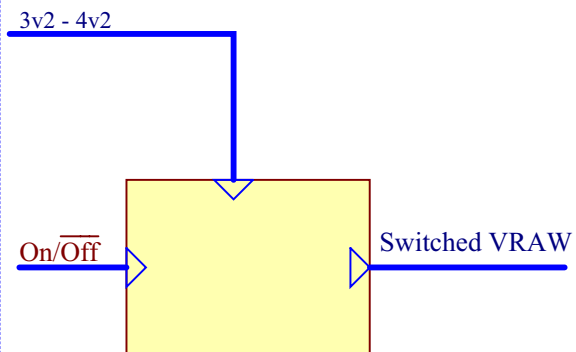
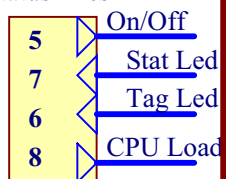
RS232 or TTL-3v0 level interface



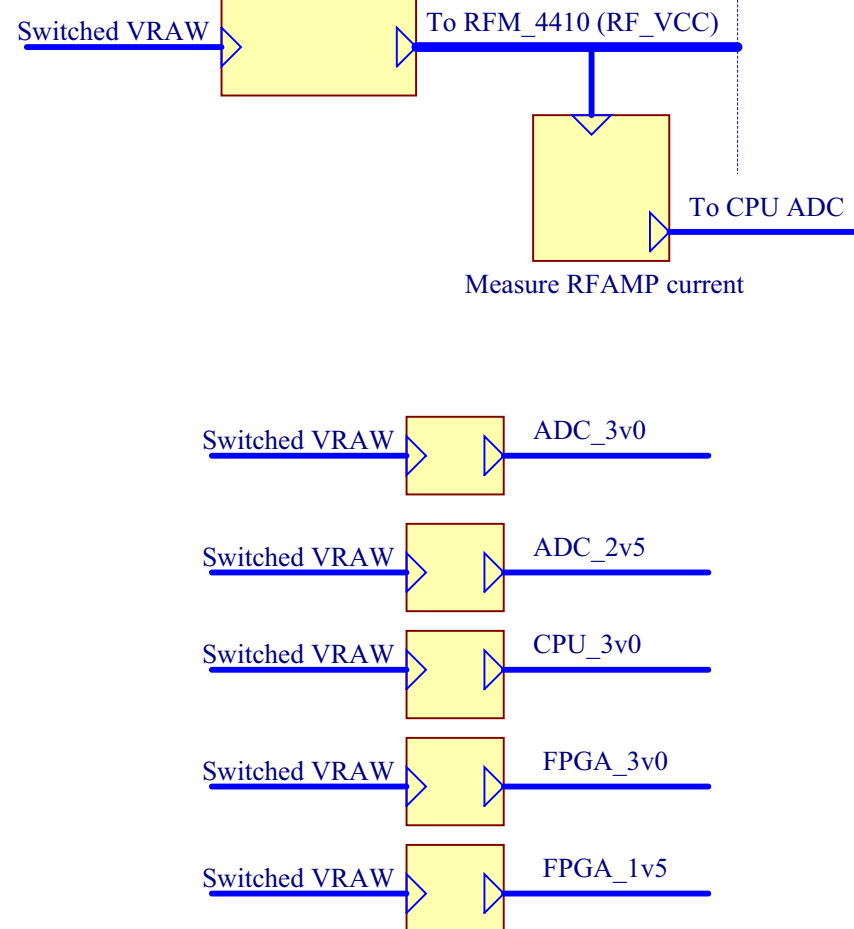
Ext. Power supply interface




Control/Status lines

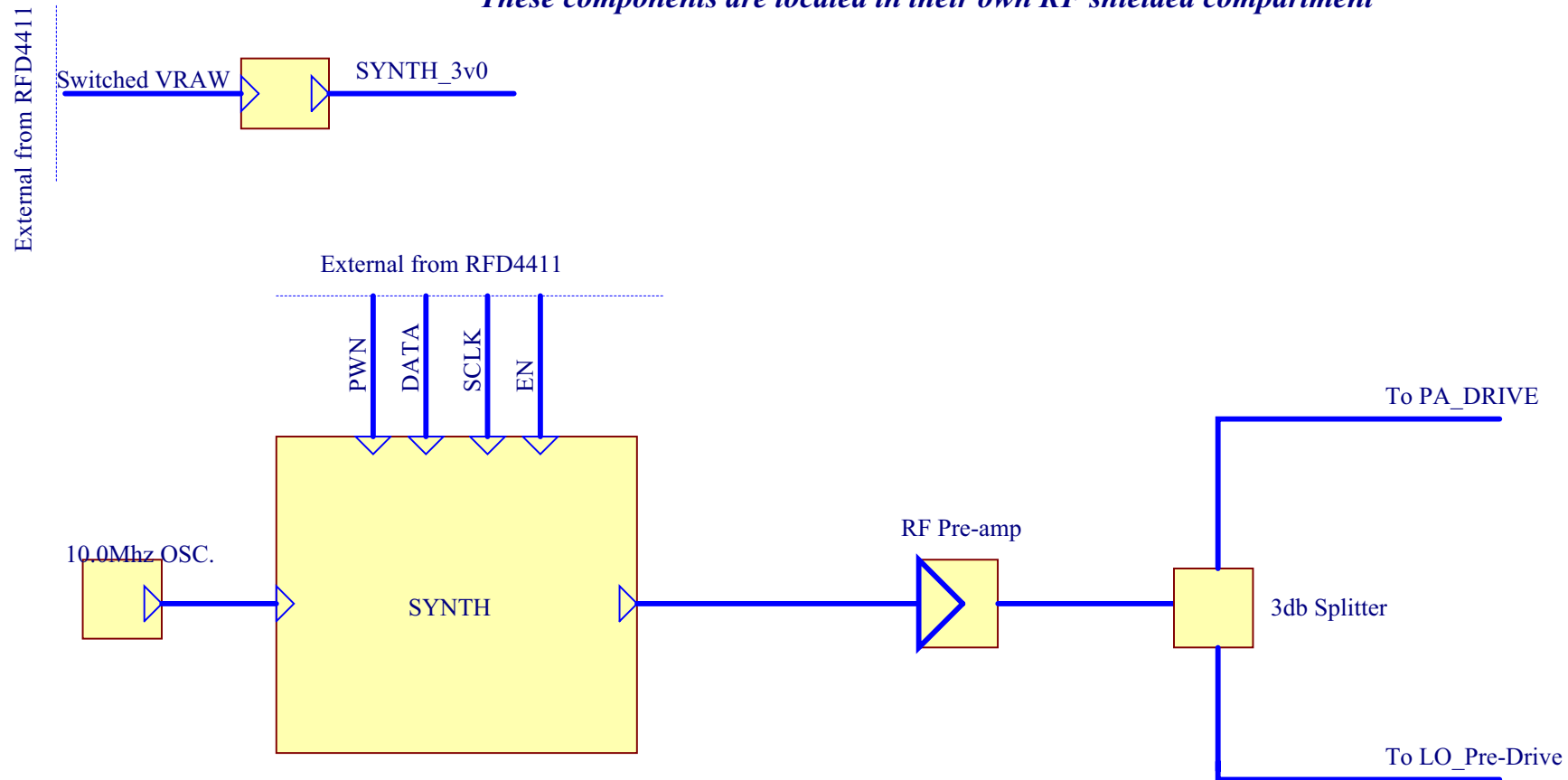



## Regulators/PSU filters



Title: <b>MaxID_4411 Blockdiagram - User interface/PSU</b>			 MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom
Size: <b>A3</b>	Number: <b>RM100_4411</b>	Revision: <b>00</b>	
Date: <b>7/20/2005</b>	Time: <b>2:14:21 PM</b>	Sheet <b>7</b> of <b>7</b>	
File: <b>C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_psu.SCHDOC</b>			

*These components are located in their own RF shielded compartment*



Title: MaxID_4411 Blockdiagram - RF Synth			 MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom
Size: A3	Number: RM100_4411	Revision: 00	
Date: 7/20/2005	Time: 2:14:21 PM	Sheet 4 of 7	
File: C:\work\ProtelProjects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_synth.SCHDOC			