

I have a few comments on this Application. Please **do not put confidential information** in your responses to these questions as the response letter will not be held confidential by the FCC. Depending on your answers there may be more questions.

1. The Block Diagram is not consistent with the requirements of 2.1033(b)(5). Please review.

2.1033(b) (5) A block diagram showing the frequency of all oscillators/crystals in the device. The signal path and frequency shall be indicated at each block. The tuning range(s) and intermediate frequency(ies) shall be indicated at each block.

A: Is the original block diagram not enough??

2. Please revise schematic diagram to list component values and upload to the schematic diagram exhibit.

A: Please find the "part list".

3. Please send photos of just the label drawing and upload to label exhibit.

A: Please find the "label" and "label location".