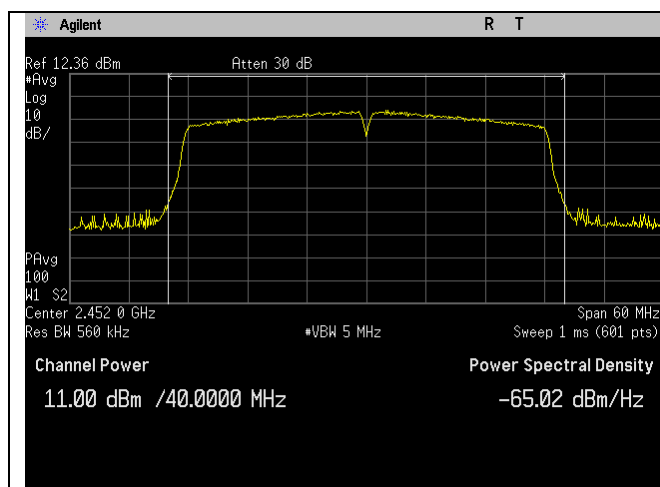


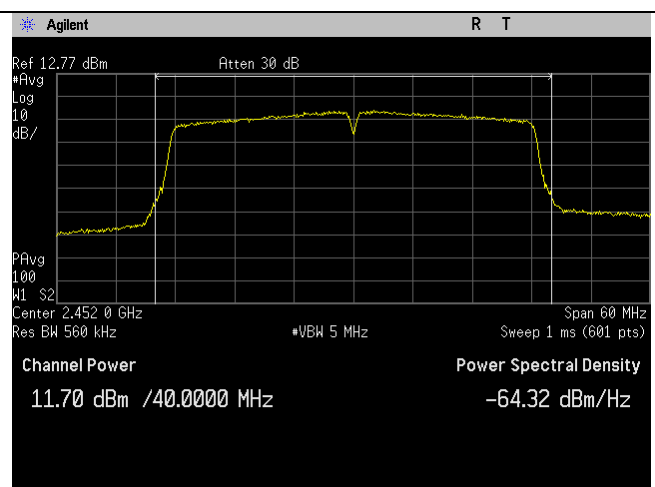
Annex B - Maximum Output Power

[Naming Convention]

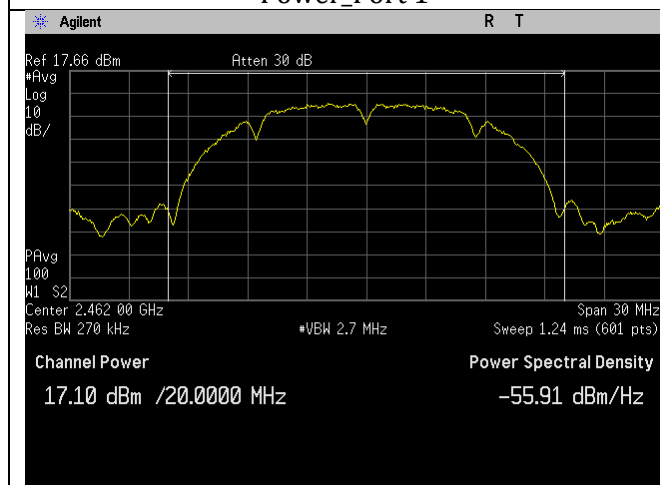
Channel (Low, Mid, High)_Frequency (MHz)_Bandwidth (MHz)_Modulation_(b-mode, g-mode, n-mode, ac-mode, ax-mode)_Measurement (Output Power)_Port (1, 2)



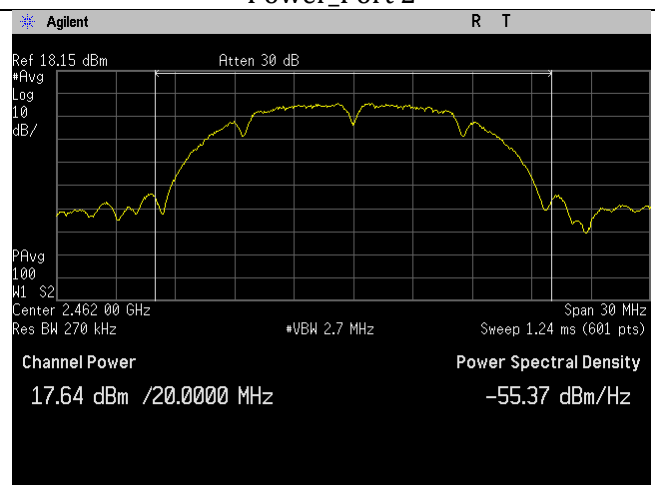
High Ch_2452MHz_40MHz BW_n-mode_Output
Power_Port 1



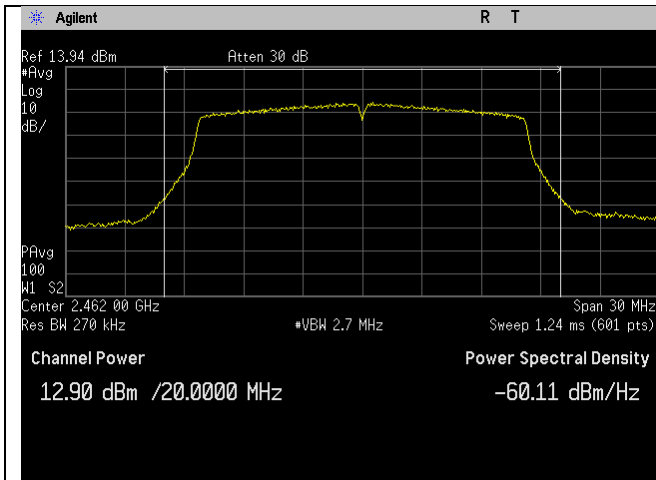
High Ch_2452MHz_40MHz BW_n-mode_Output
Power_Port 2



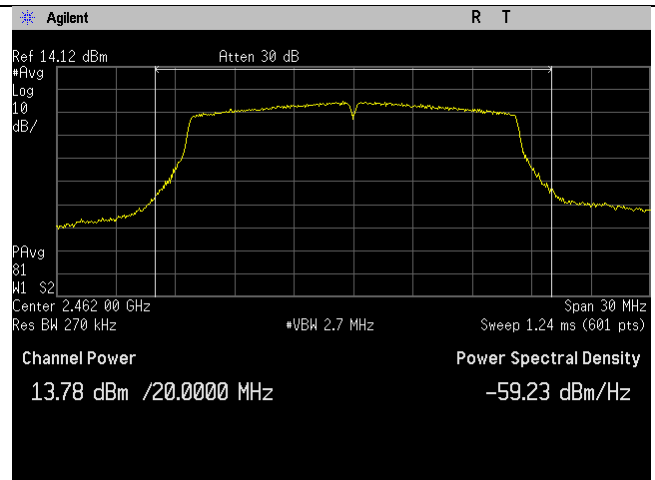
High Ch_2462MHz_20MHz BW_b-mode_Output
Power_Port 1



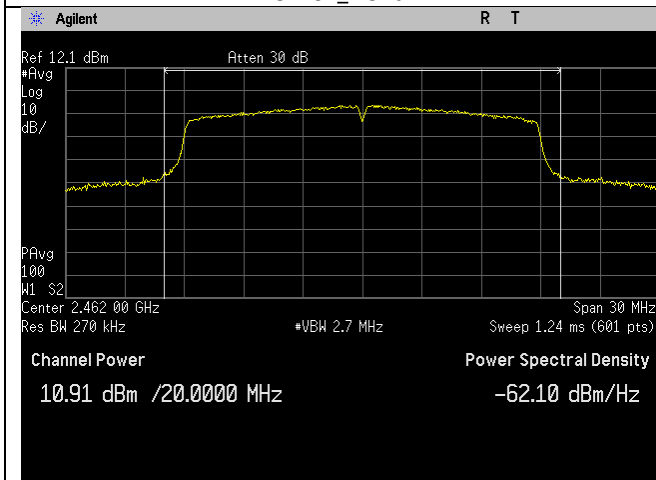
High Ch_2462MHz_20MHz BW_b-mode_Output
Power_Port 2



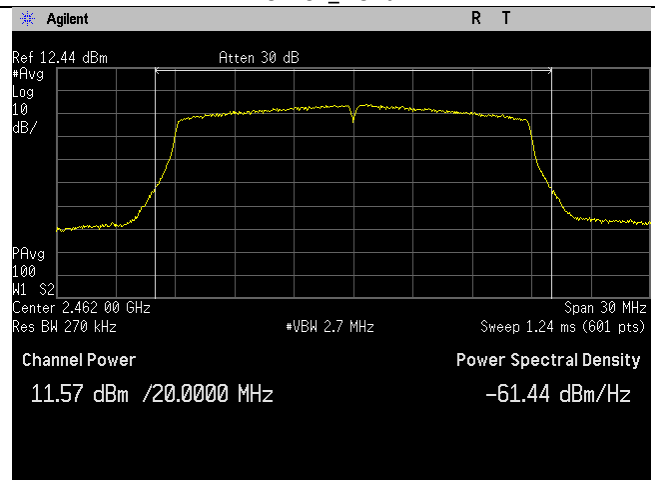
High Ch_2462MHz_20MHz BW_g-mode_Output
Power_Port 1



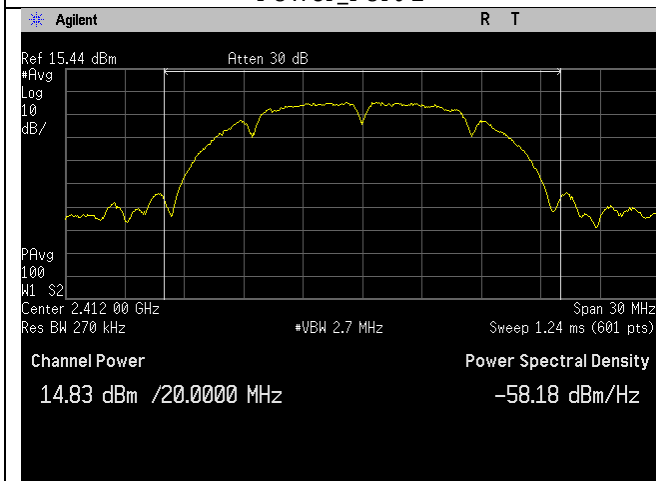
High Ch_2462MHz_20MHz BW_g-mode_Output
Power_Port 2



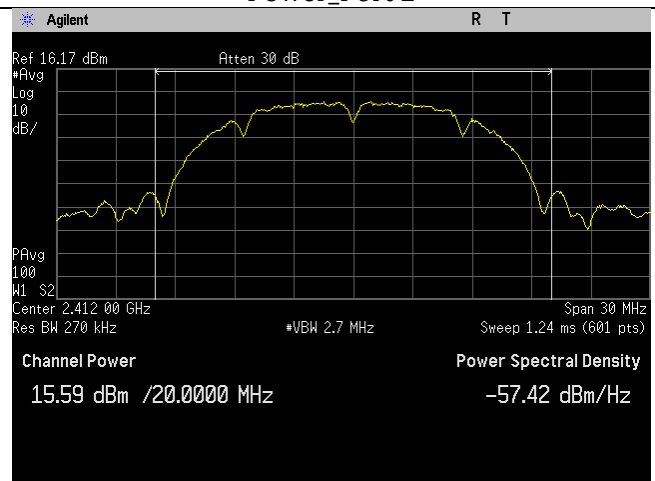
High Ch_2462MHz_20MHz BW_n-mode_Output
Power_Port 1



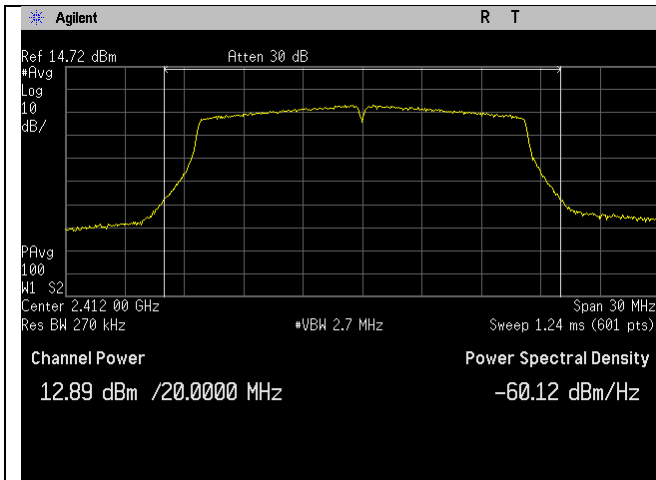
High Ch_2462MHz_20MHz BW_n-mode_Output
Power_Port 2



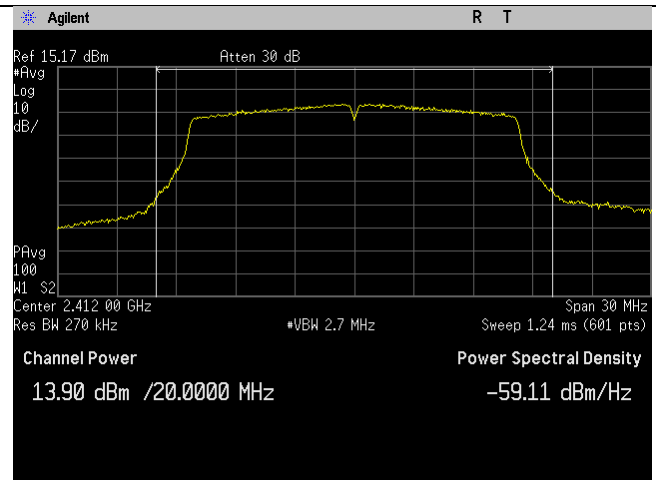
Low Ch_2412MHz_20MHz BW_b-mode_Output
Power_Port 1



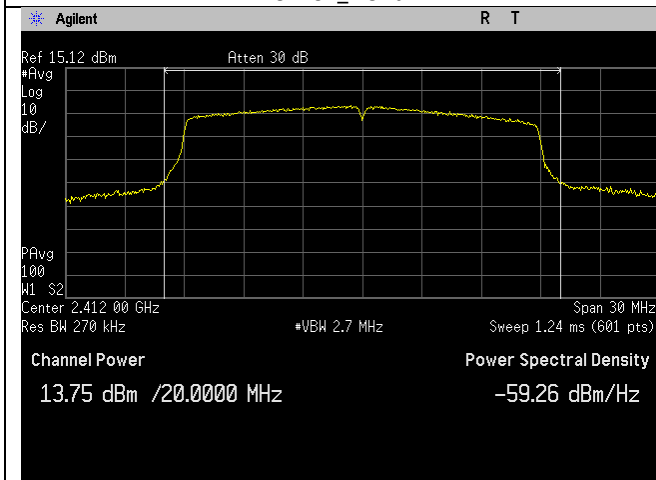
Low Ch_2412MHz_20MHz BW_b-mode_Output
Power_Port 2



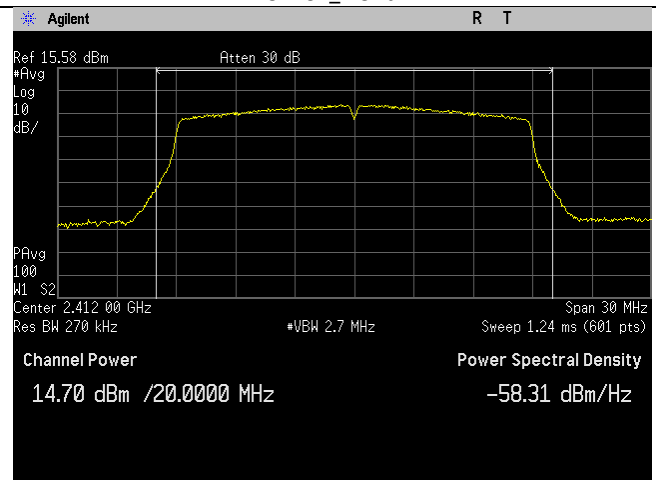
Low Ch_2412MHz_20MHz BW_g-mode_Output
Power_Port 1



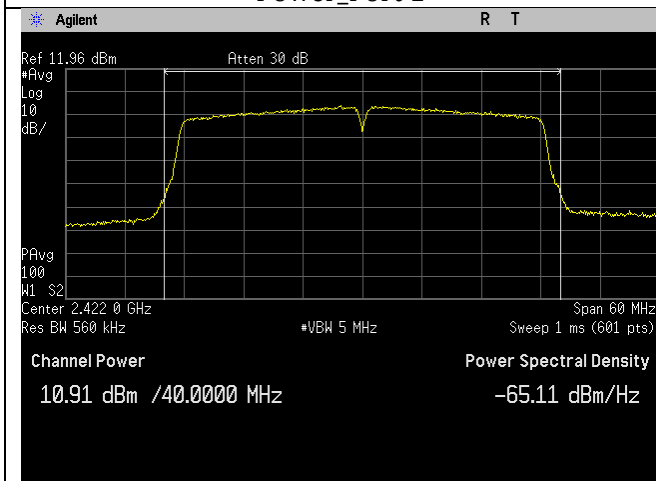
Low Ch_2412MHz_20MHz BW_g-mode_Output
Power_Port 2



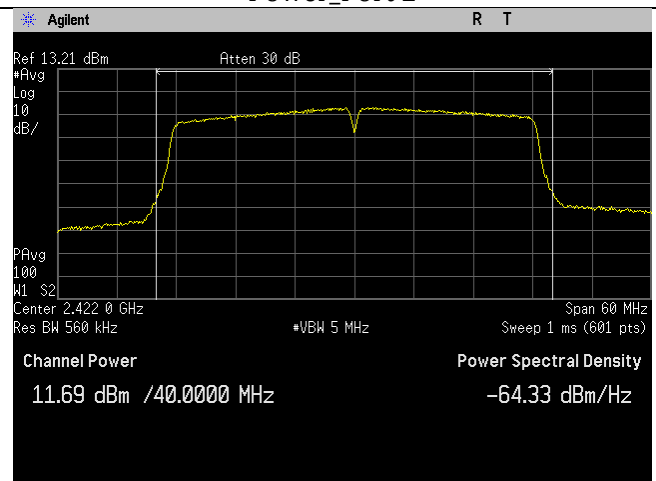
Low Ch_2412MHz_20MHz BW_n-mode_Output
Power_Port 1



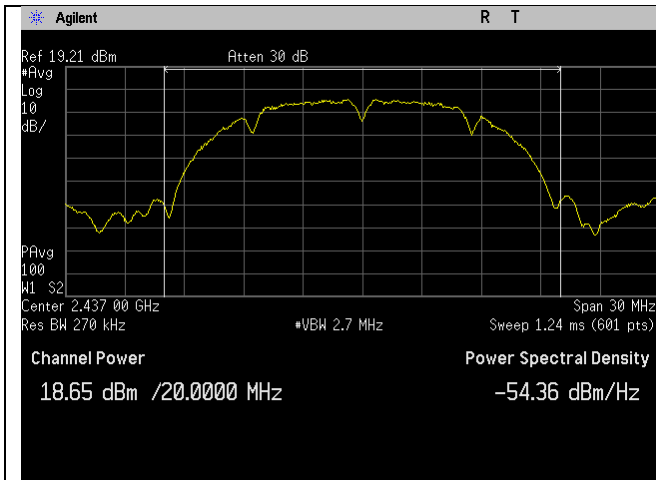
Low Ch_2412MHz_20MHz BW_n-mode_Output
Power_Port 2



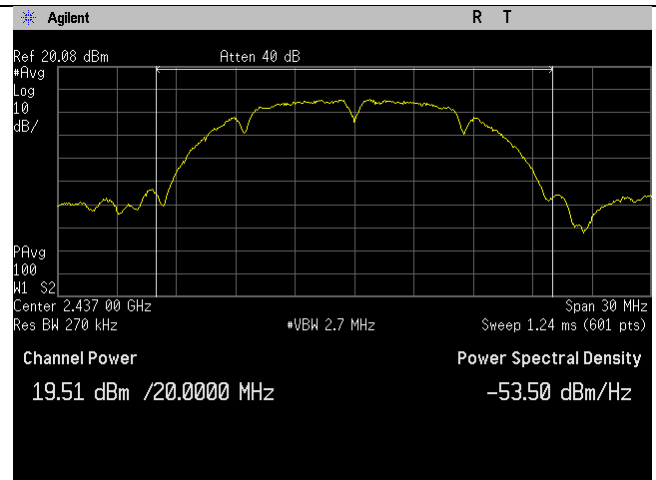
Low Ch_2422MHz_40MHz BW_n-mode_Output
Power_Port 1



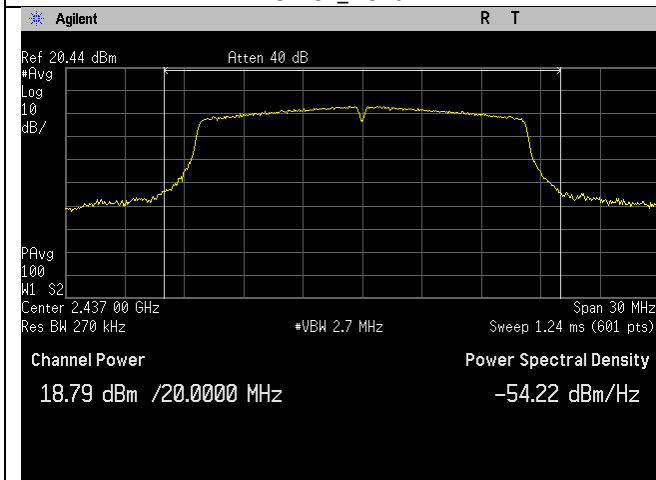
Low Ch_2422MHz_40MHz BW_n-mode_Output
Power_Port 2



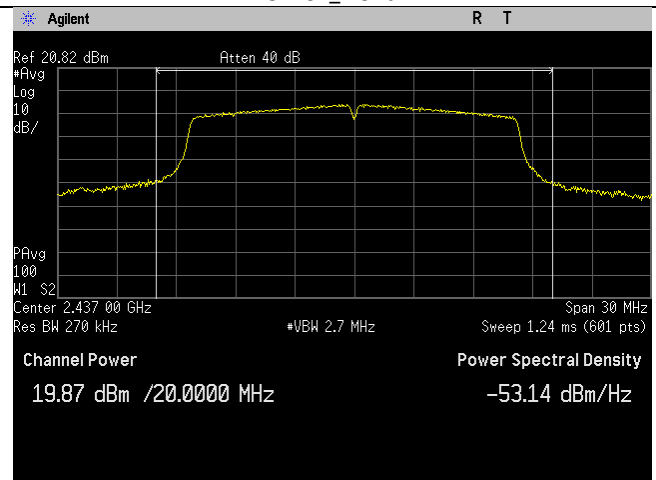
Mid Ch_2437MHz_20MHz BW_b-mode_Output
Power_Port 1



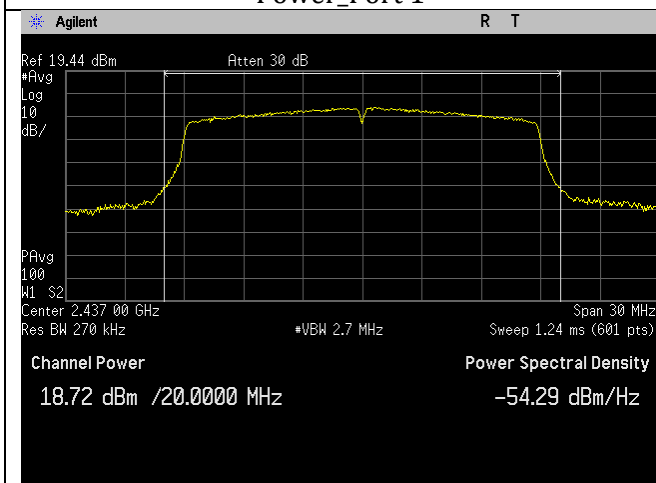
Mid Ch_2437MHz_20MHz BW_b-mode_Output
Power_Port 2



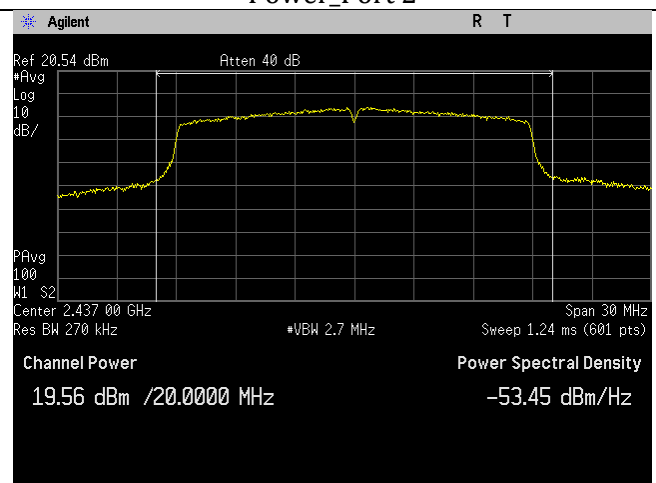
Mid Ch_2437MHz_20MHz BW_g-mode_Output
Power_Port 1



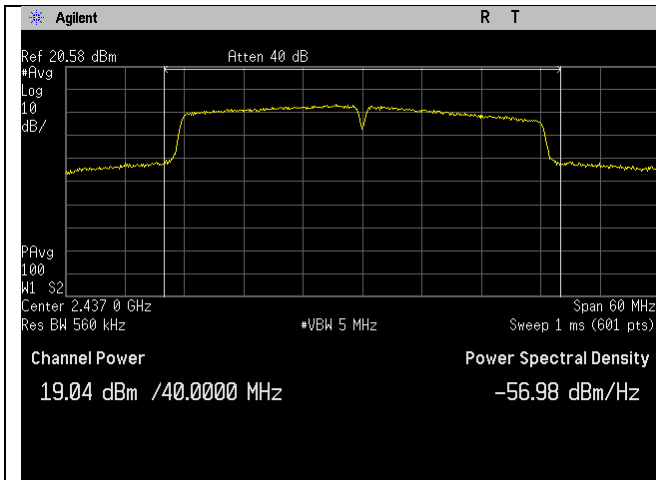
Mid Ch_2437MHz_20MHz BW_g-mode_Output
Power_Port 2



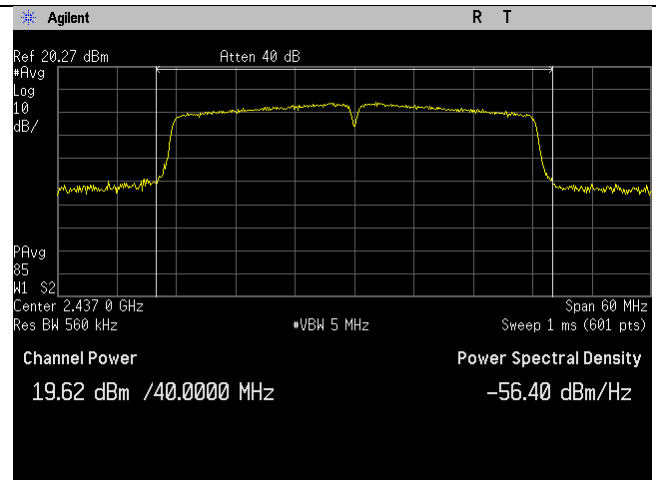
Mid Ch_2437MHz_20MHz BW_n-mode_Output
Power_Port 1



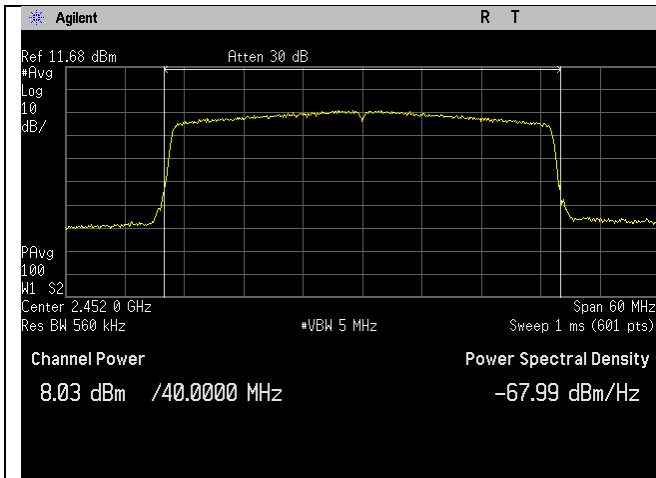
Mid Ch_2437MHz_20MHz BW_n-mode_Output
Power_Port 2



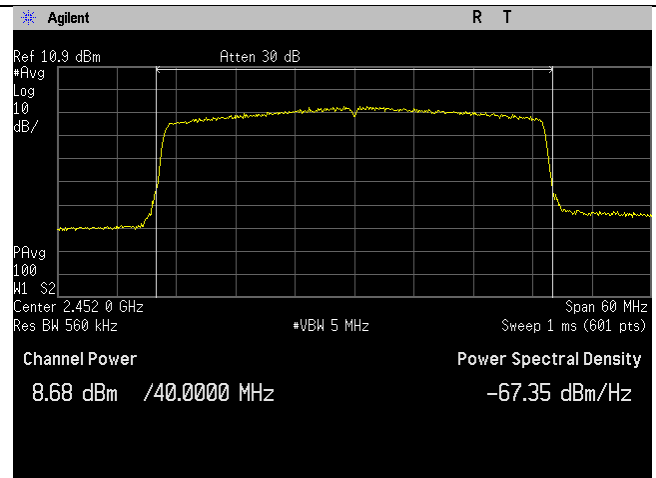
Mid Ch_2437MHz_40MHz BW_n-mode_Output
Power_Port 1



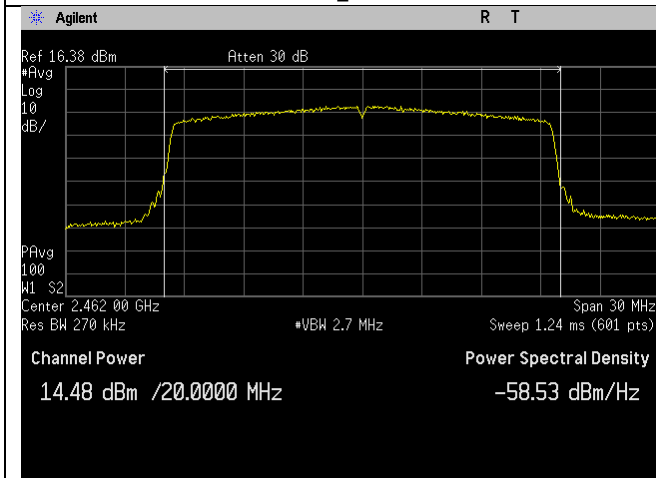
Mid Ch_2437MHz_40MHz BW_n-mode_Output
Power_Port 2



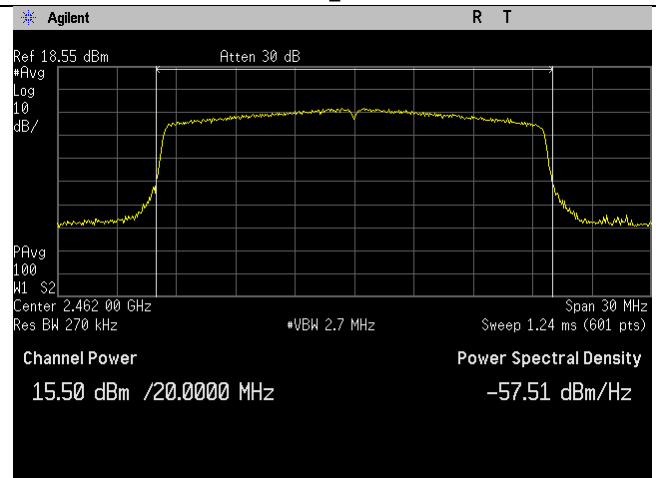
High Ch_2452MHz_40MHz BW_ax-mode_Output
Power_Port 1



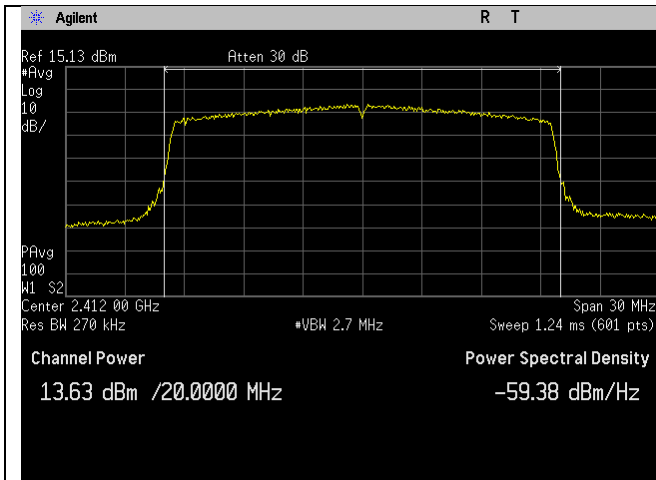
High Ch_2452MHz_40MHz BW_ax-mode_Output
Power_Port 2



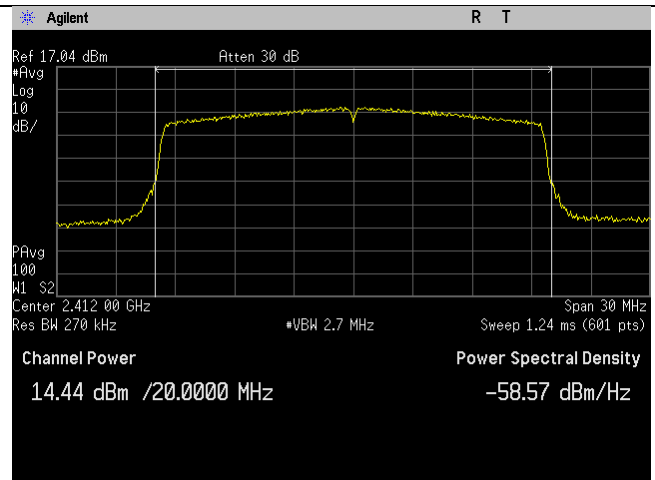
High Ch_2462MHz_20MHz BW_ax-mode_Output
Power_Port 1



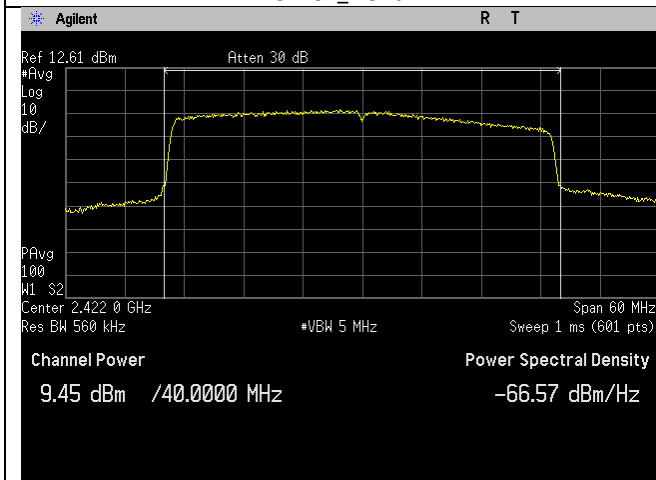
High Ch_2462MHz_20MHz BW_ax-mode_Output
Power_Port 2



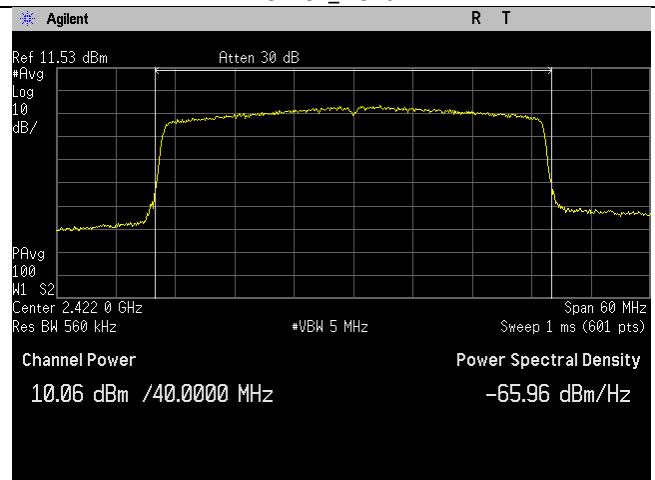
Low Ch_2412MHz_20MHz BW_ax-mode_Output
Power_Port 1



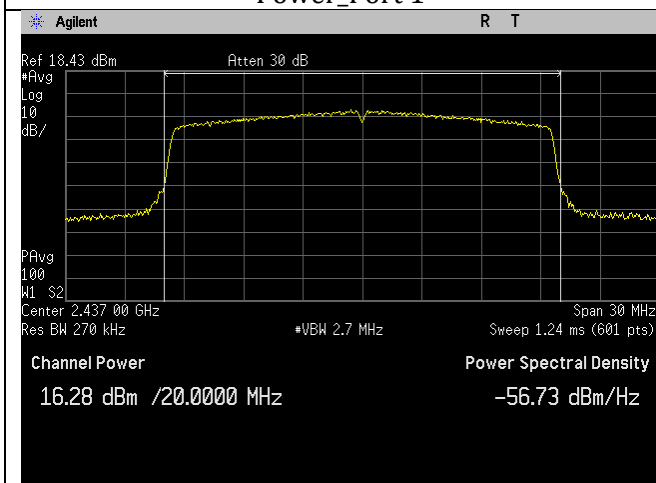
Low Ch_2412MHz_20MHz BW_ax-mode_Output
Power_Port 2



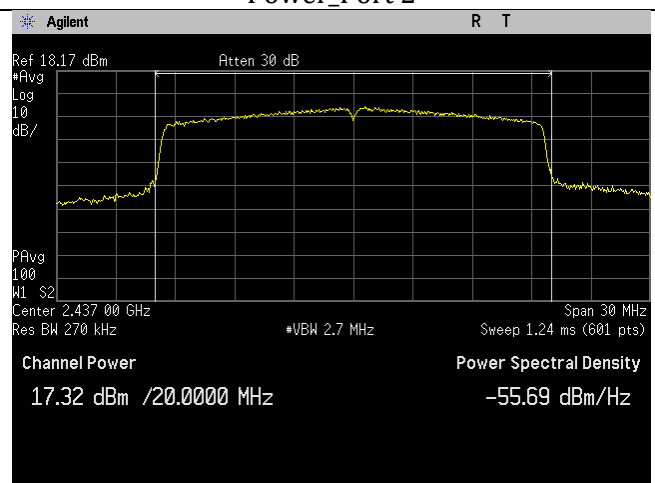
Low Ch_2422MHz_40MHz BW_ax-mode_Output
Power_Port 1



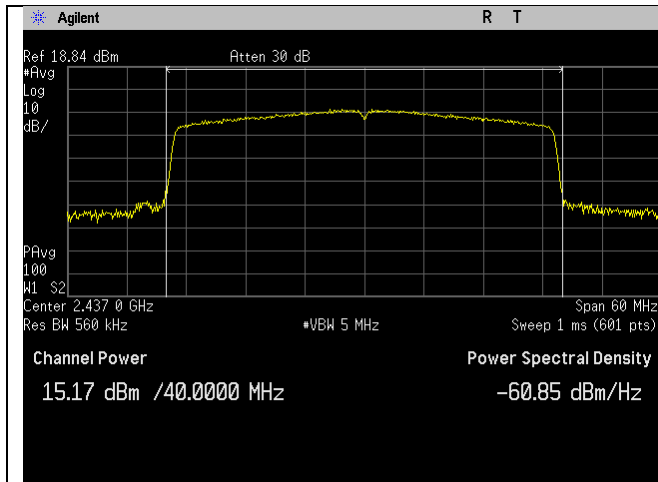
Low Ch_2422MHz_40MHz BW_ax-mode_Output
Power_Port 2



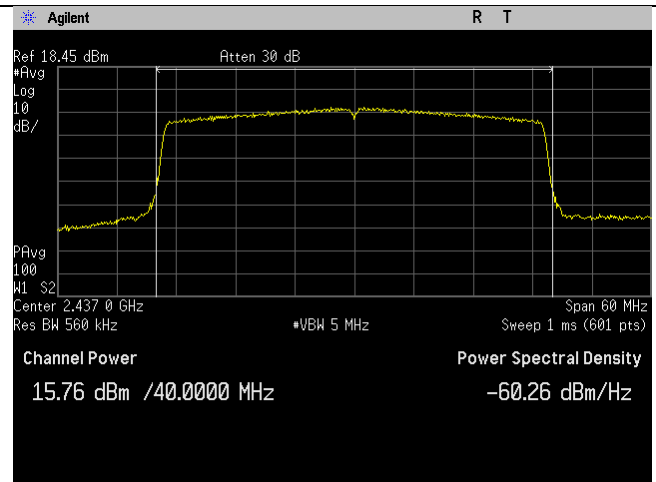
Mid Ch_2437MHz_20MHz BW_ax-mode_Output
Power_Port 1



Mid Ch_2437MHz_20MHz BW_ax-mode_Output
Power_Port 2



Mid Ch_2437MHz_40MHz BW_ax-mode_Output
Power_Port 1



Mid Ch_2437MHz_40MHz BW_ax-mode_Output
Power_Port 2