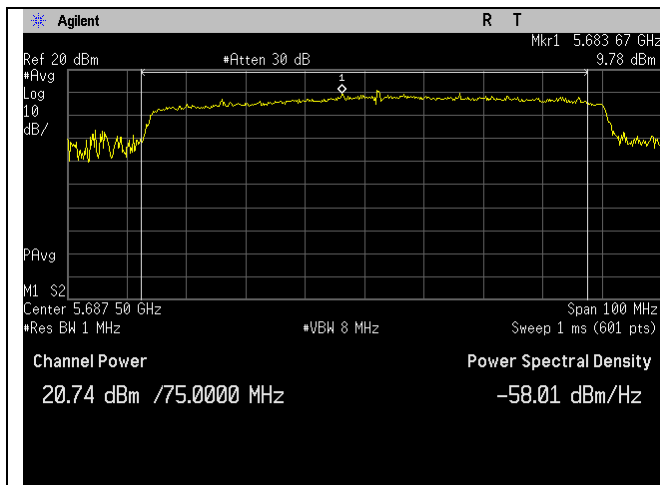


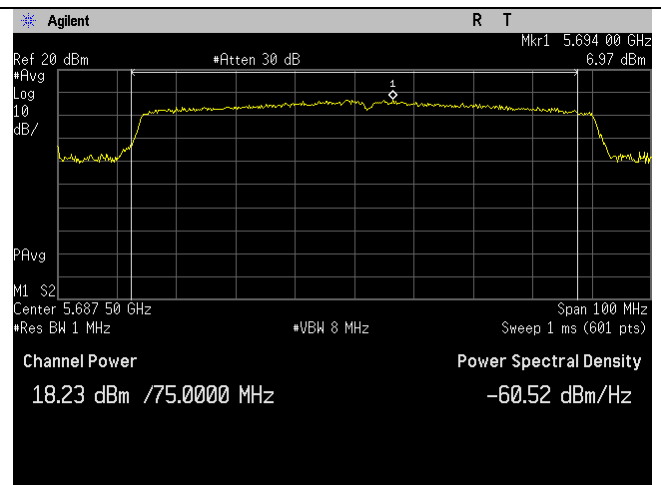
Annex J - Output Power Straddle

[Naming Convention]

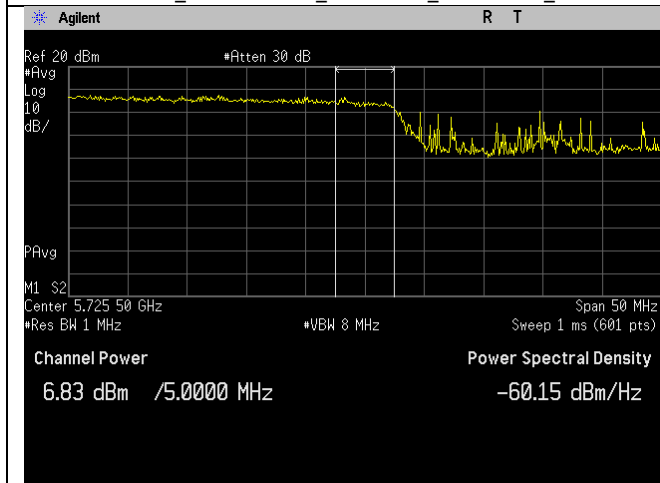
Frequency (MHz)_ Modulation (amode, n20mode, n40mode, ac80mode, ax20mode, ax40mode, ax80mdoe)_Band (UNII-2C, UNII-3)_Measurement (Output Power Straddle)_Port (1,2)



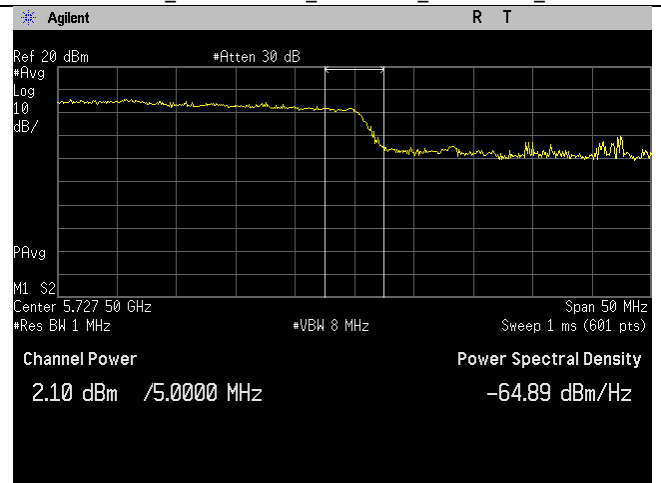
5690MHz_ac80mode_UNII-2C_Straddle_Port 1



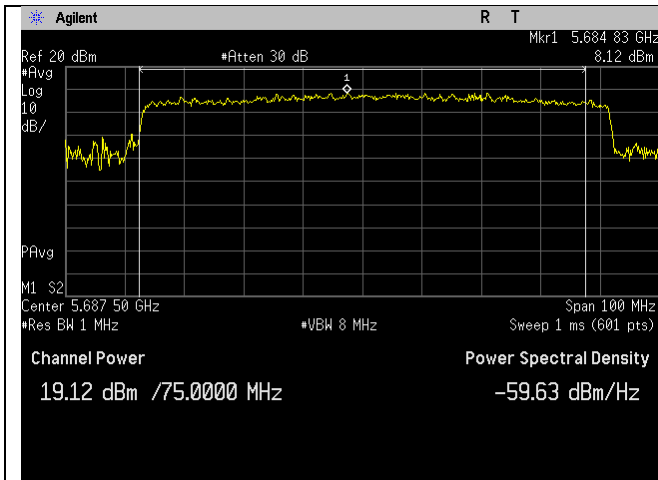
5690MHz_ac80mode_UNII-2C_Straddle_Port 2



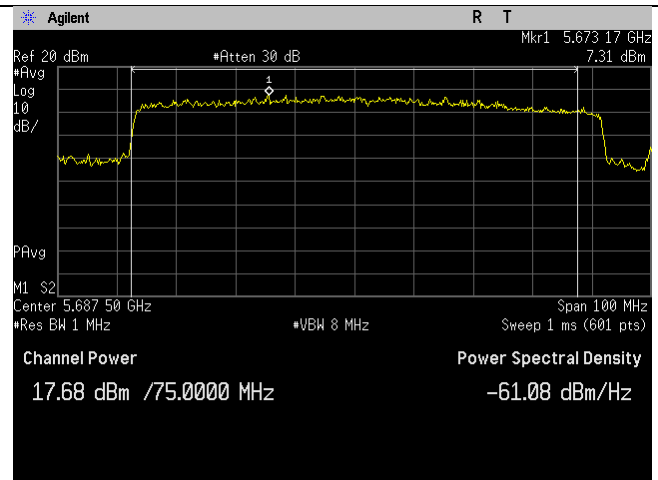
5690MHz_ac80mode_UNII-3_Straddle_Port 1



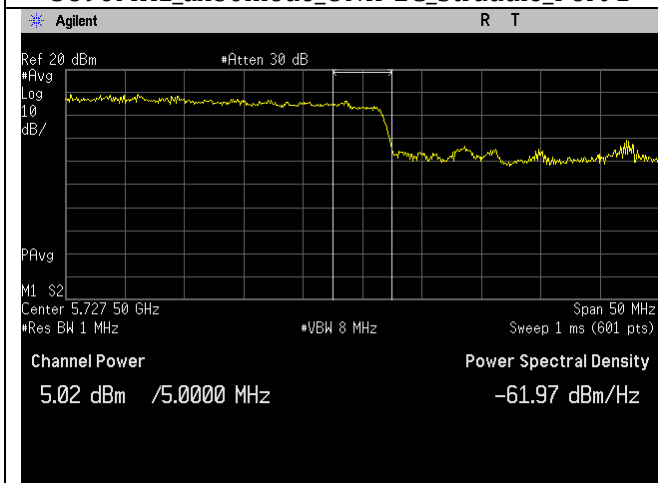
5690MHz_ac80mode_UNII-3_Straddle_Port 2



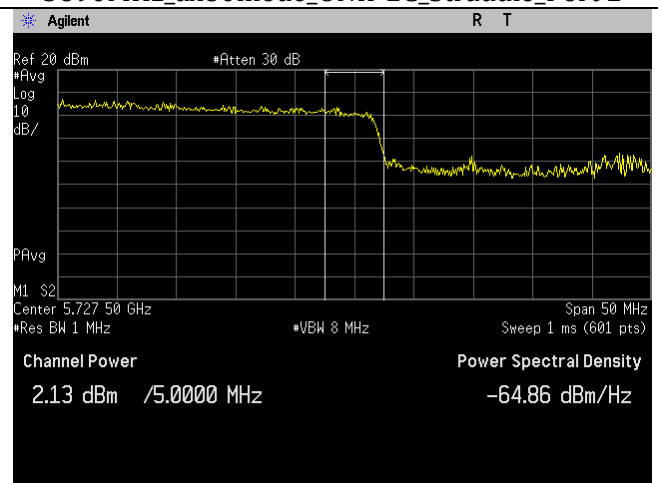
5690MHz_ax80mode_UNII-2C_Straddle_Port 1



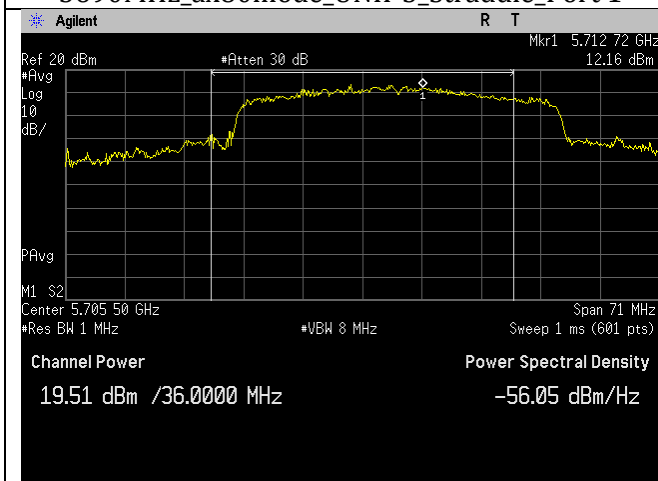
5690MHz_ax80mode_UNII-2C_Straddle_Port 2



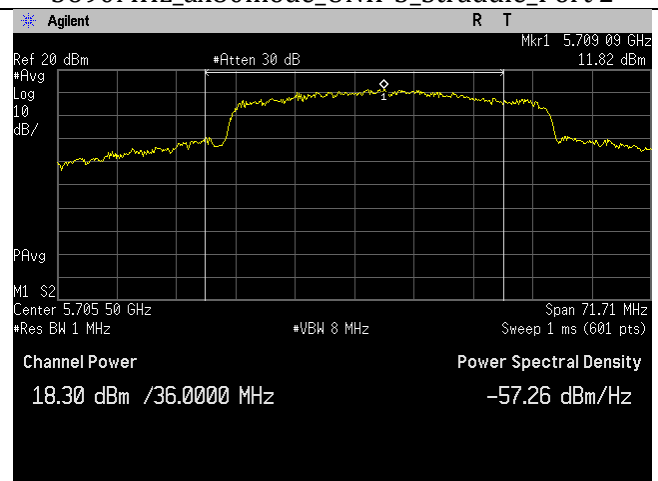
5690MHz_ax80mode_UNII-3_Straddle_Port 1



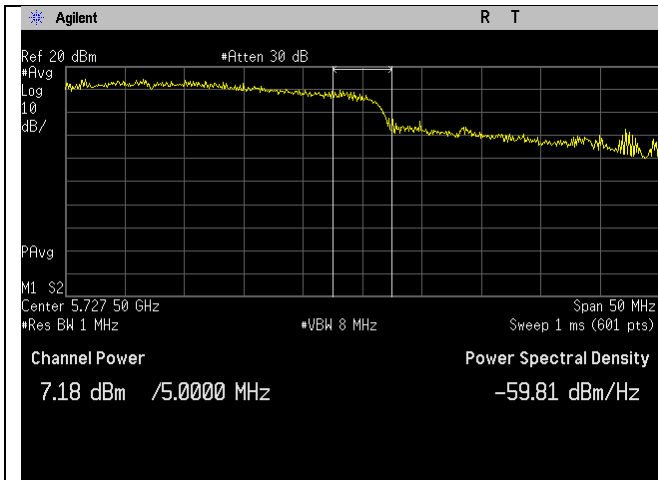
5690MHz_ax80mode_UNII-3_Straddle_Port 2



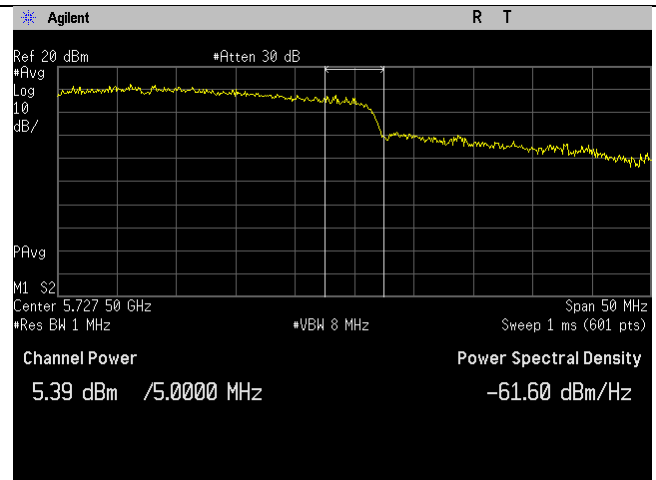
5710MHz_ax40mode_UNII-2C_Straddle_Port 1



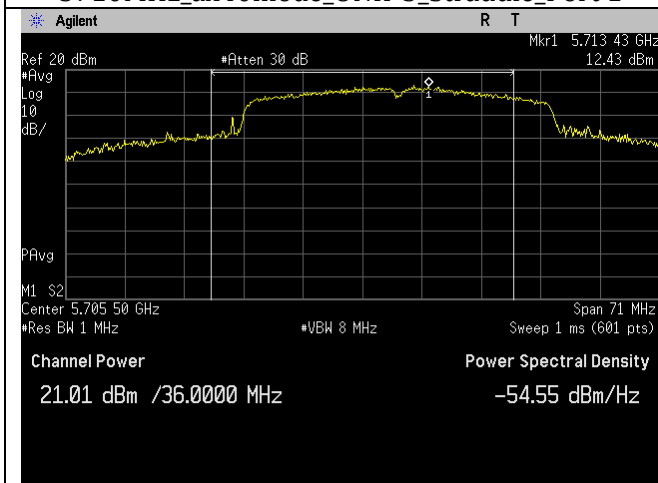
5710MHz_ax40mode_UNII-2C_Straddle_Port 2



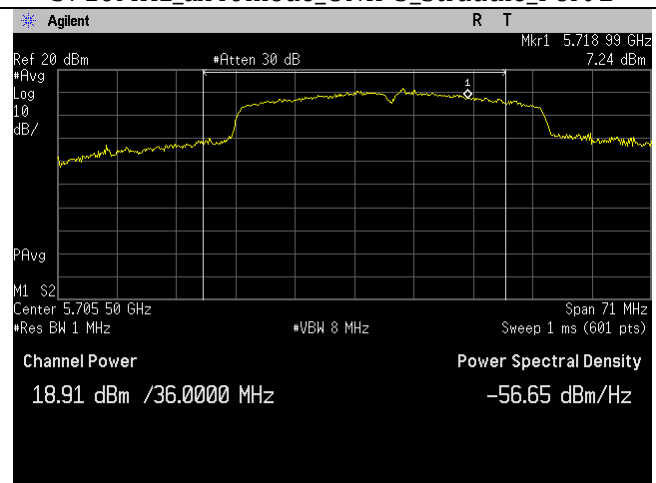
5710MHz_ax40mode_UNII-3_Straddle_Port 1



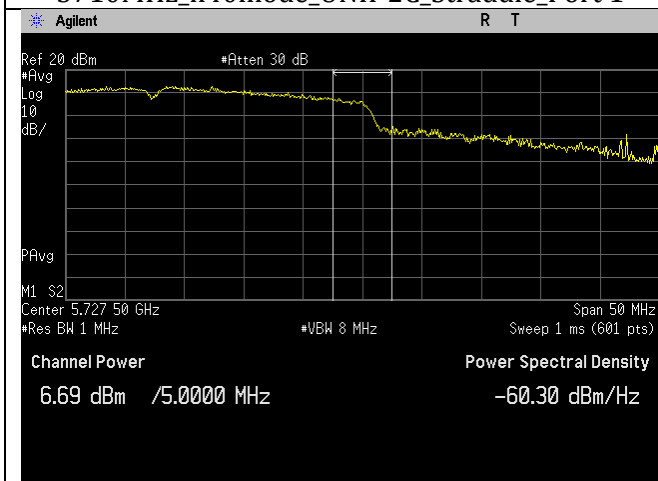
5710MHz_ax40mode_UNII-3_Straddle_Port 2



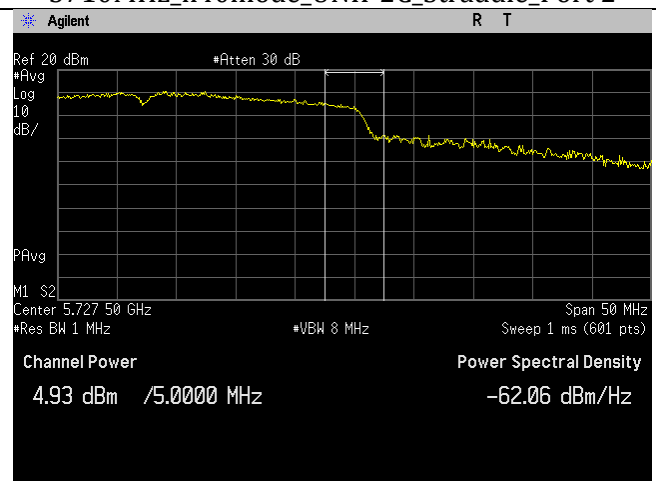
5710MHz_n40mode_UNII-2C_Straddle_Port 1



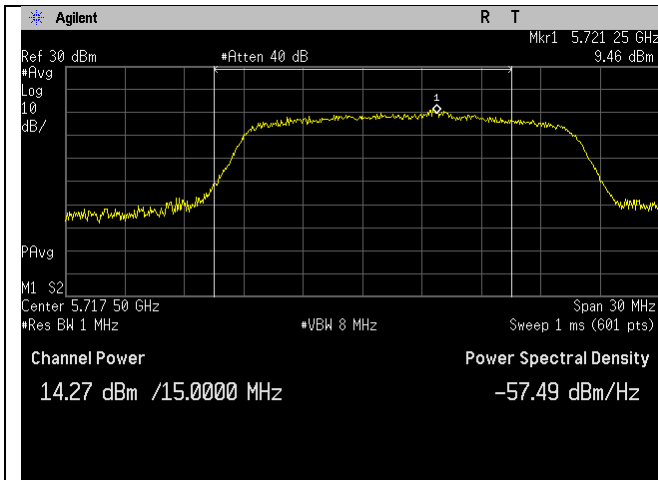
5710MHz_n40mode_UNII-2C_Straddle_Port 2



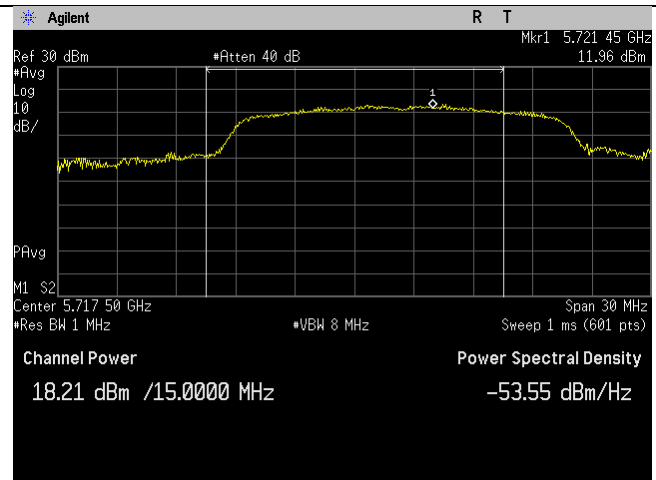
5710MHz_n40mode_UNII-3_Straddle_Port 1



5710MHz_n40mode_UNII-3_Straddle_Port 2



5720MHz_amode_UNII-2C_Straddle_Port 1



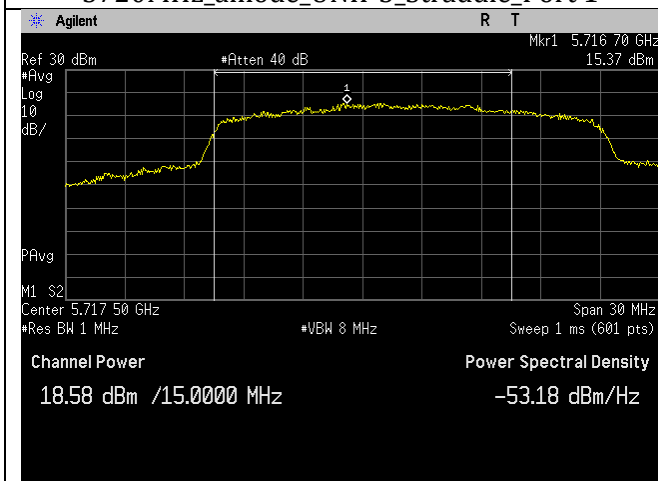
5720MHz_amode_UNII-2C_Straddle_Port 2



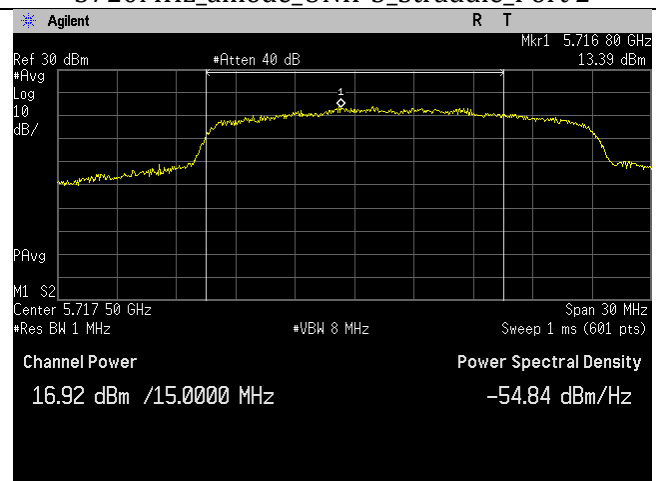
5720MHz_amode_UNII-3_Straddle_Port 1



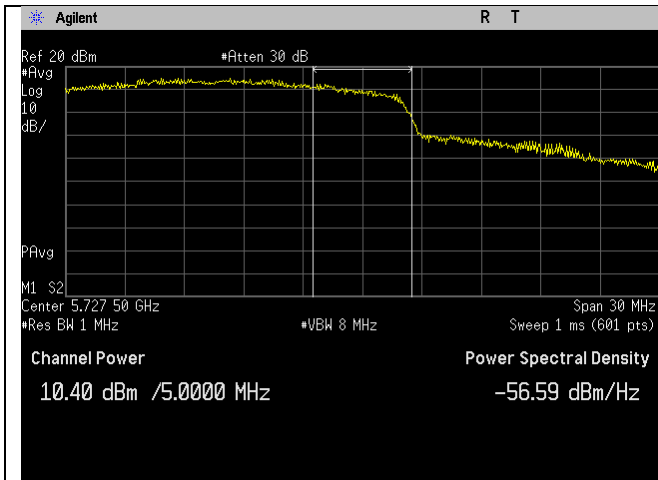
5720MHz_amode_UNII-3_Straddle_Port 2



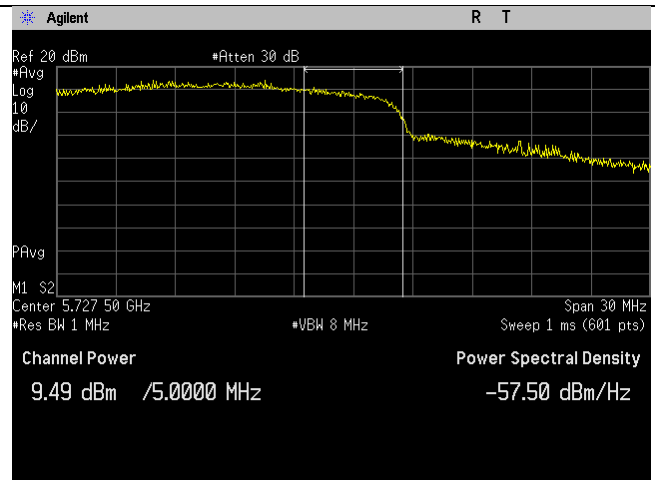
5720MHz_ax20mode_UNII-2C_Straddle_Port 1



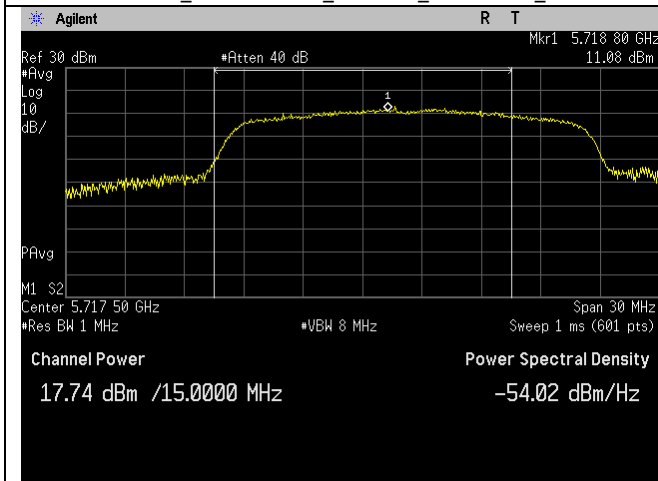
5720MHz_ax20mode_UNII-2C_Straddle_Port 2



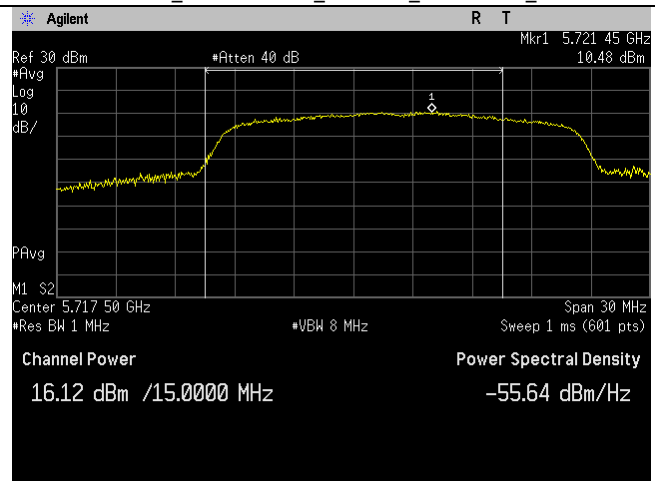
5720MHz_ax20mode_UNII-3_Straddle_Port 1



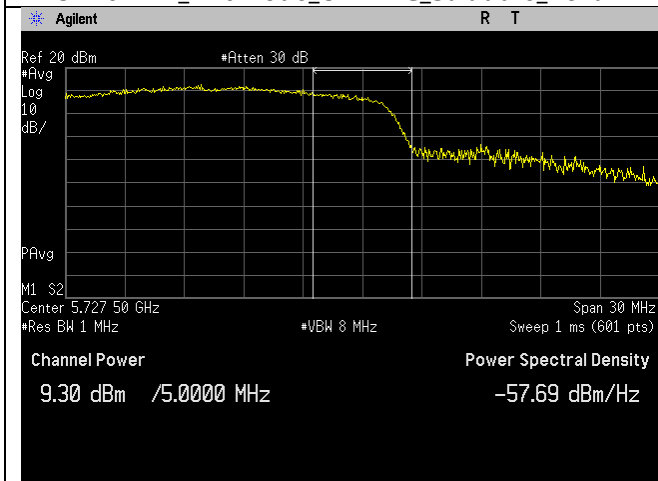
5720MHz_ax20mode_UNII-3_Straddle_Port 2



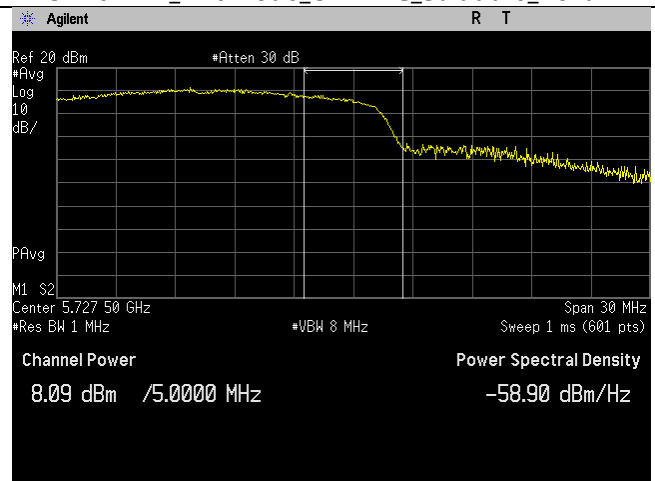
5720MHz_n20mode_UNII-2C_Straddle_Port 1



5720MHz_n20mode_UNII-2C_Straddle_Port 2



5720MHz_n20mode_UNII-3_Straddle_Port 1



5720MHz_n20mode_UNII-3_Straddle_Port 2