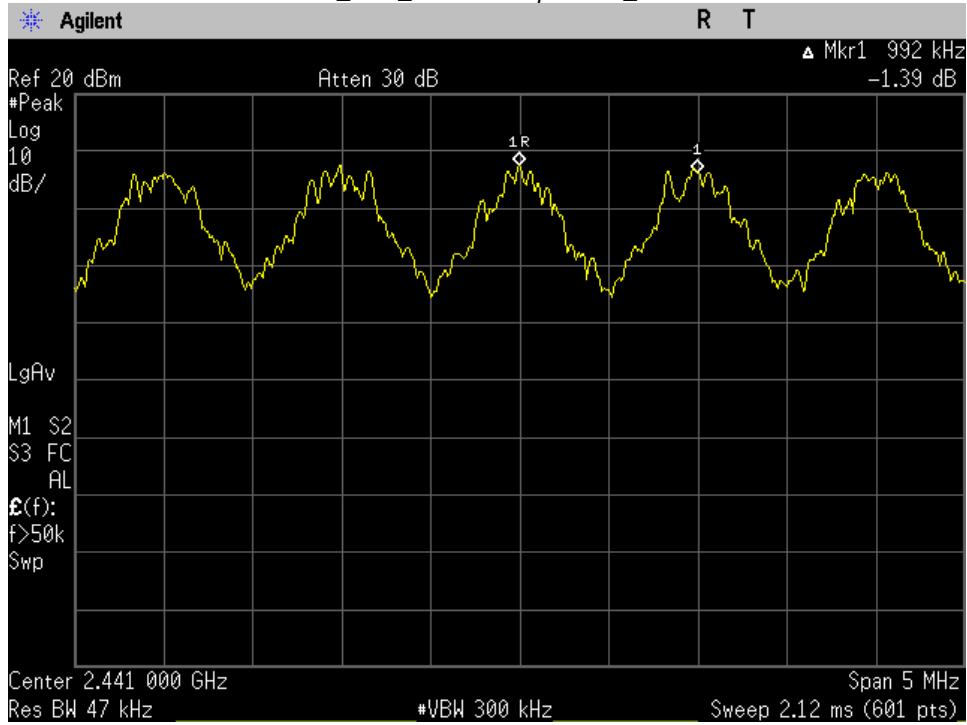


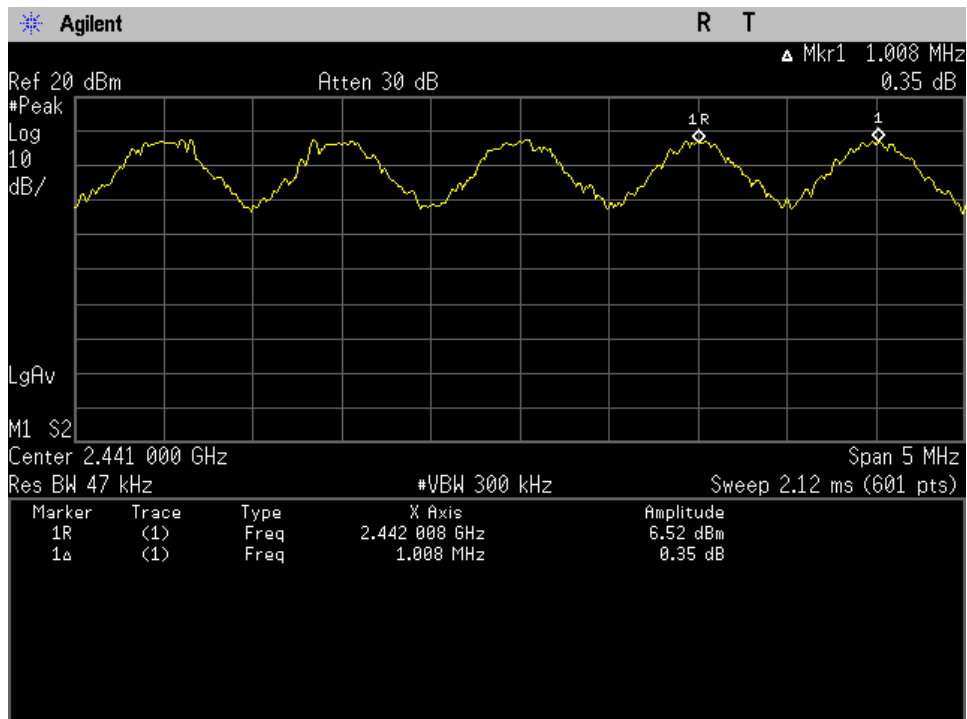
Annex G: Hopping Channel Separation

GFSK

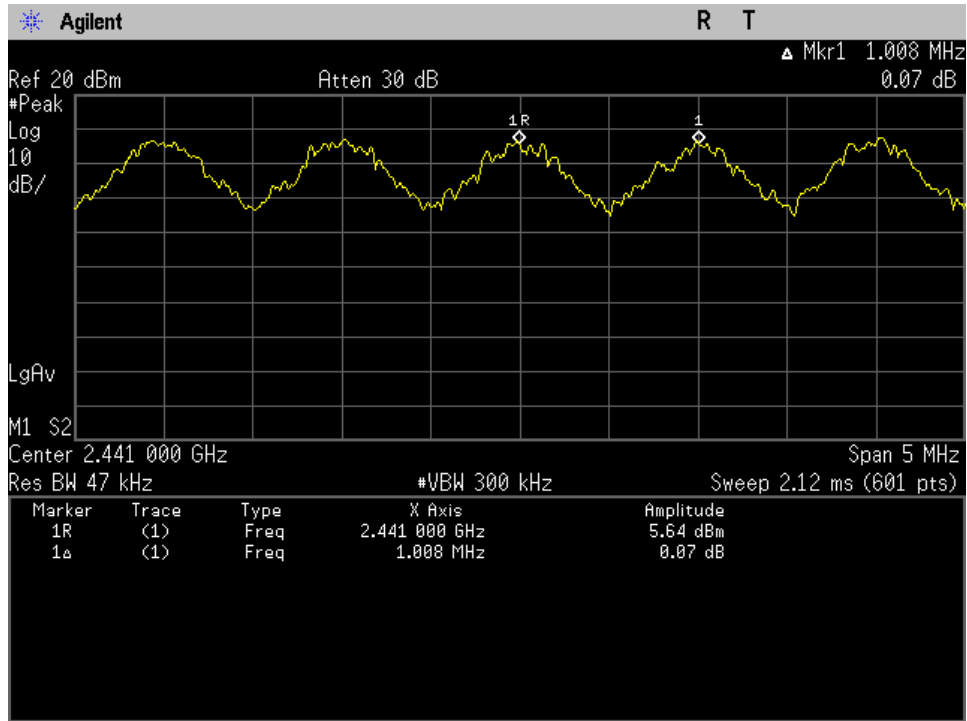
GFSK_DH1_Channel Separation_Port 1



GFSK_DH3_Channel Separation_Port 1

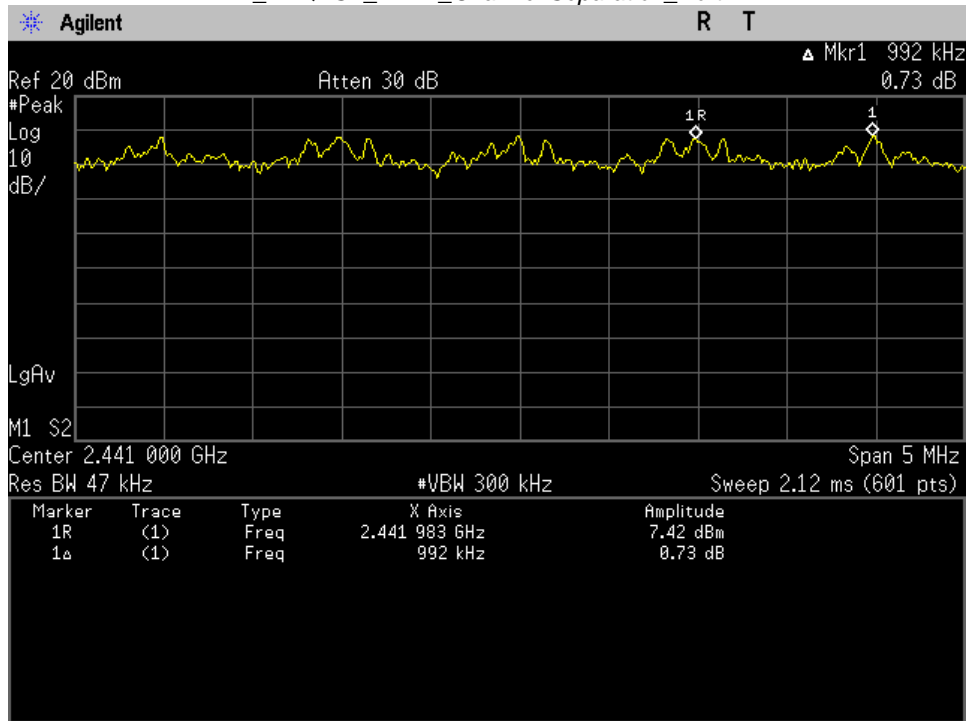


GFSK_DH5_Channel Separation_Port 1

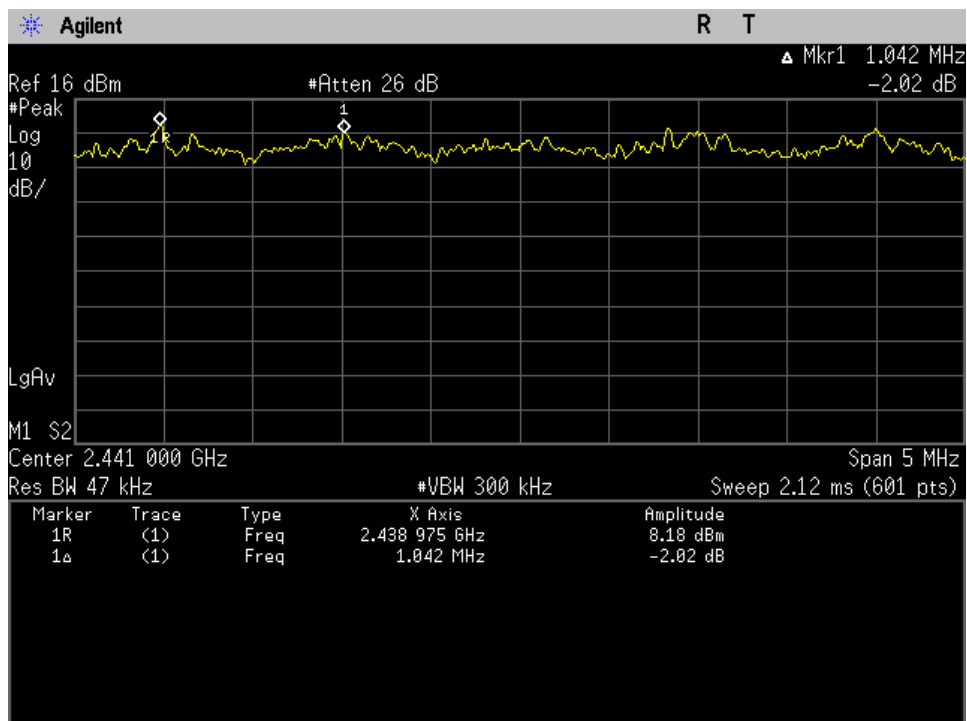


$\Pi/4$ DPSK

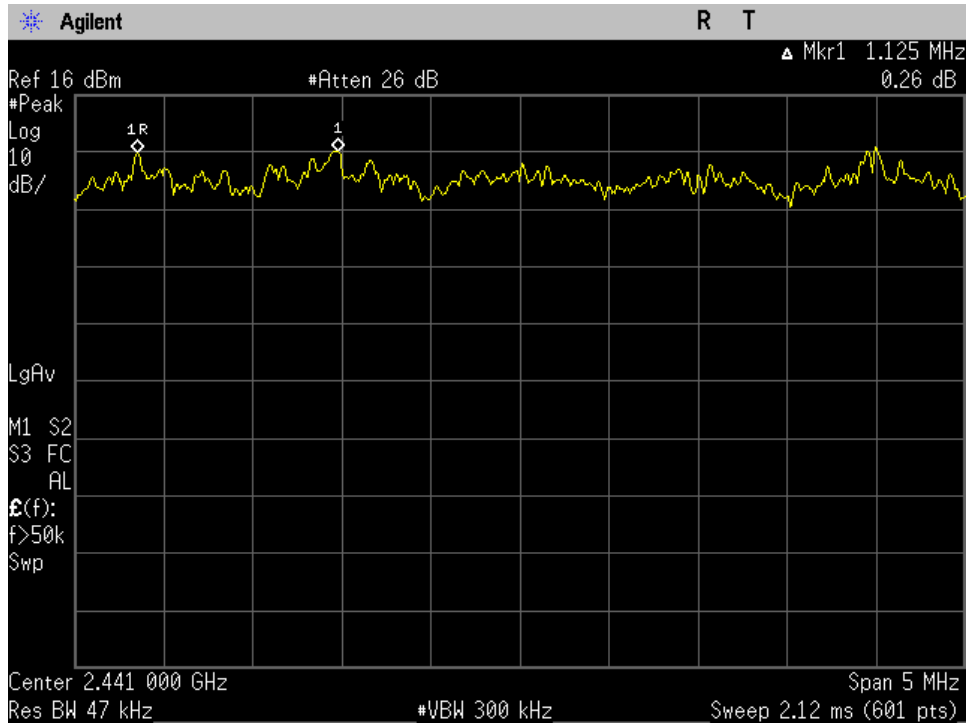
Pi_4DQPSK_2DH1_Channel Separation_Port 1



Pi_4DQPSK_2DH3_Channel Separation_Port 1

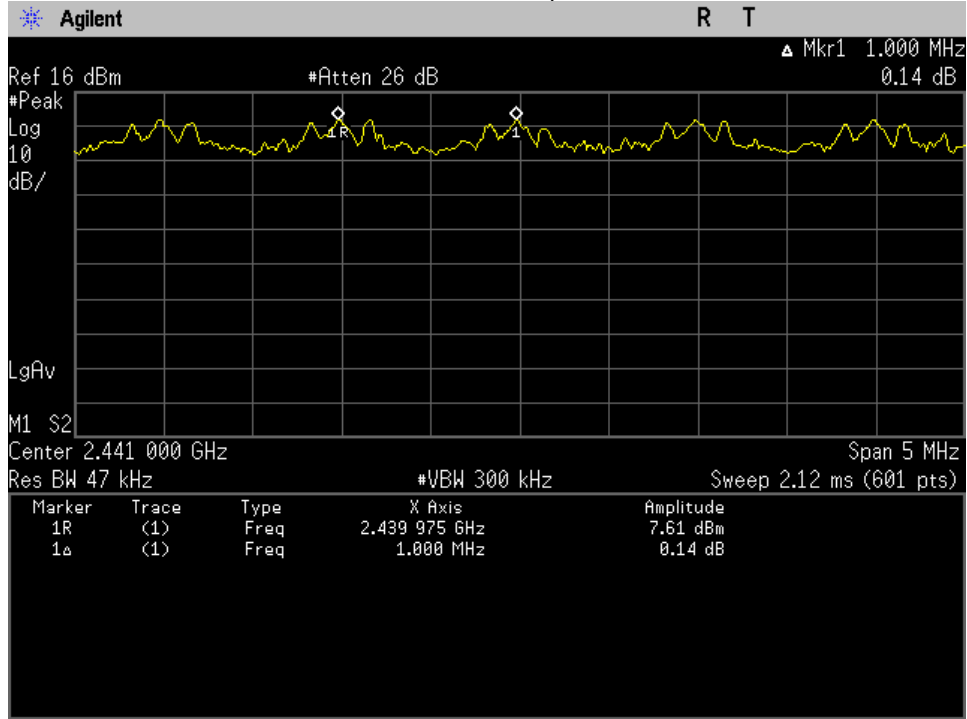


Pi_4DQPSK_2DH5_Channel Separation_Port 1

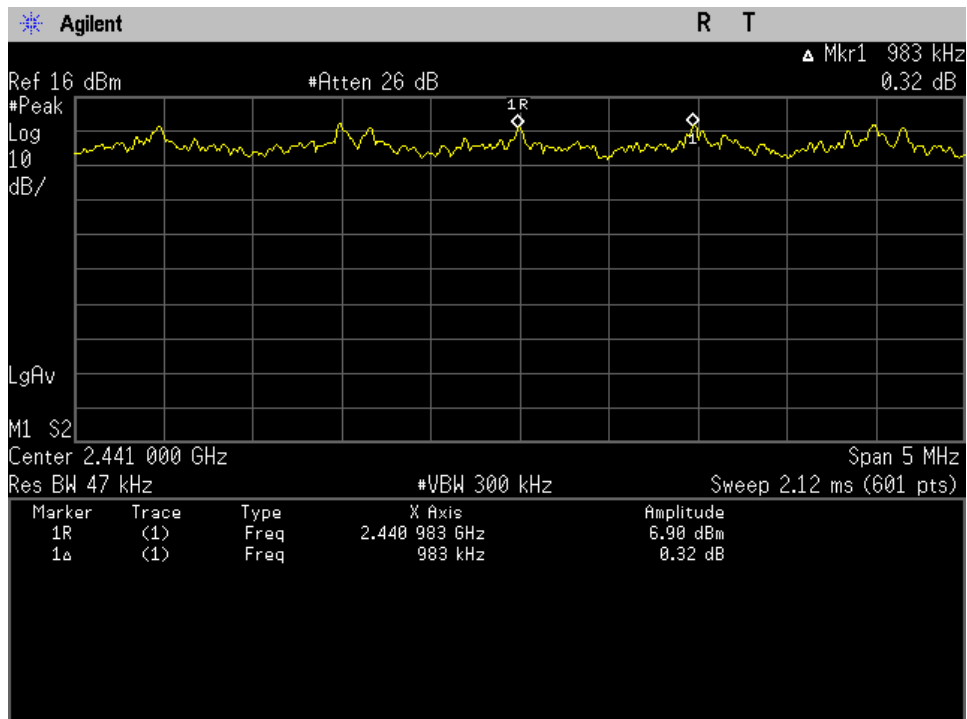


8DPSK

8DPSK_3DH1_Channel Separation_Port 1



8DPSK_3DH3_Channel Separation_Port 1



8DPSK_3DH5_Channel Separation_Port 1

