

## 1.1 System Block Diagram (with a Carrier Board)

The following figure shows the system block diagram of a Wireless USB Module and a carrier board. It accepts +5V from a power adapter or +5V from USB device and converts it to various voltages required by the MAC-PHY chip.

Only one crystal is used in the system, which is 66MHz. The system is designed to support both BG1 and BG3 UWB bands as defined in the WiMedia standard.

