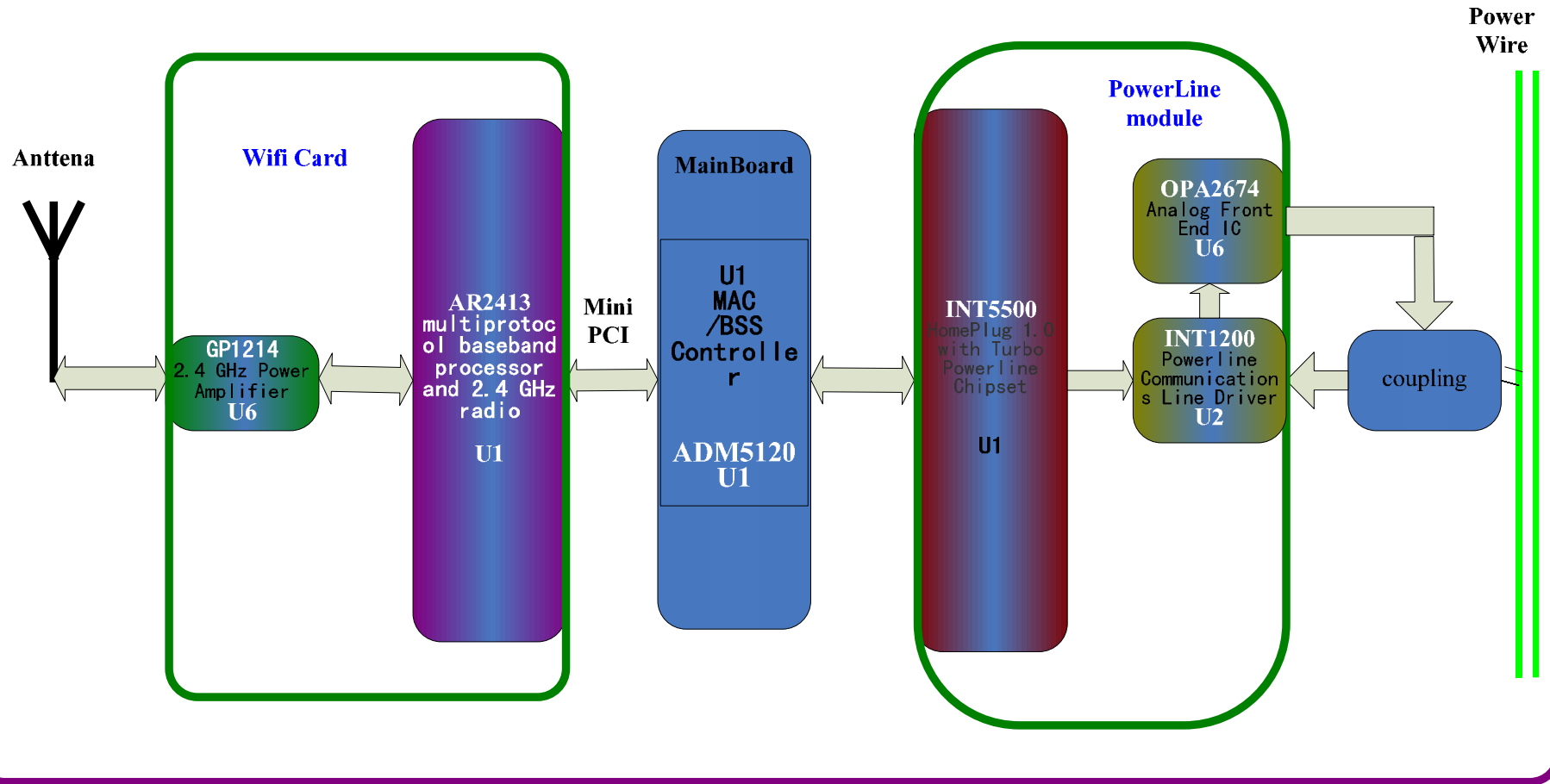


8350-WAP Block Diagram



## PL8350 - WAP Operation Description

### Wireless Part signal transmit

MAC /BSS Controller **【ADM5120: highly integrated, high performance and flexible System-on-Chip Home Gateway controller】**

Determine the RF modulation type **【DBPSK, DQPSK, CCK, OFDM】**

U1 of WIFI Card RF Part **【AR2413 :Highly integrated single chip CMOS solution with multi protocol baseband processor and 2.4 GHz radio】**

To Generate signal and Receive RF signal

U6 RF Power Amplifier **【GP1214: 2.4 GHz Power Amplifier】**

To amplify Radio signal

Antenna

To transmit signal out from Antenna

### Powerline Part signal transmit

MAC /BSS Controller **【ADM5120: highly integrated, high performance and flexible System-on-Chip Home Gateway controller】**

Determine the PowerLine modulation type **【QAM, QPSK, BPSK ,ROBO, OFDM】**

U1 PowerLine Module **【INT5500 : HomePlug 1.0 with Turbo PowerLine Chipset】**

Generate signal and Receive PowerLine signal

U2 **【INT1200 Analog Front End IC】**

mixed-signal front-end optimized specifically for high speed home network communications

U6 Line Driver **【OPA2674: Dual Wideband, High Output Current Operational Amplifier with Current Limit】**

To amplify PowerLine signal

PowerLine Coupling To coupling PowerLine Signal to Power wire

Power Wire To transmit PowerLine Signal from Power wire